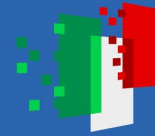




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PIANO NAZIONALE
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Future
Artificial
Intelligence
Research

Streaming Data Acquisition System Meeting

MSU-JLab

February 12, 2025

Jefferson Lab, Newport News (VA)

INFN SRO framework prototype

Fabio Rossi (presenter), **Marco Battaglieri**

Istituto Nazionale di Fisica Nucleare

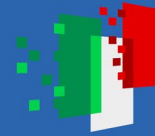
Genova (Italy)



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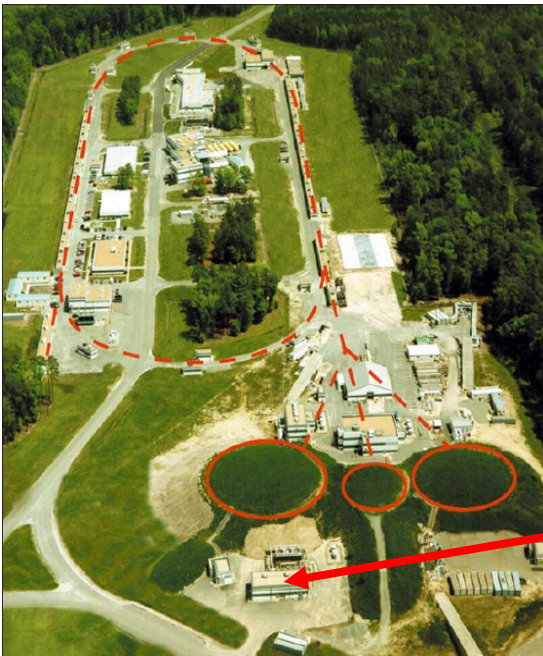
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Future Artificial Intelligence Research

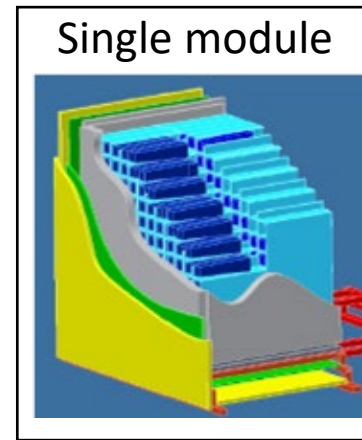
INFN Goal: Beam Dump eXperiment (BDX)

Jefferson Lab

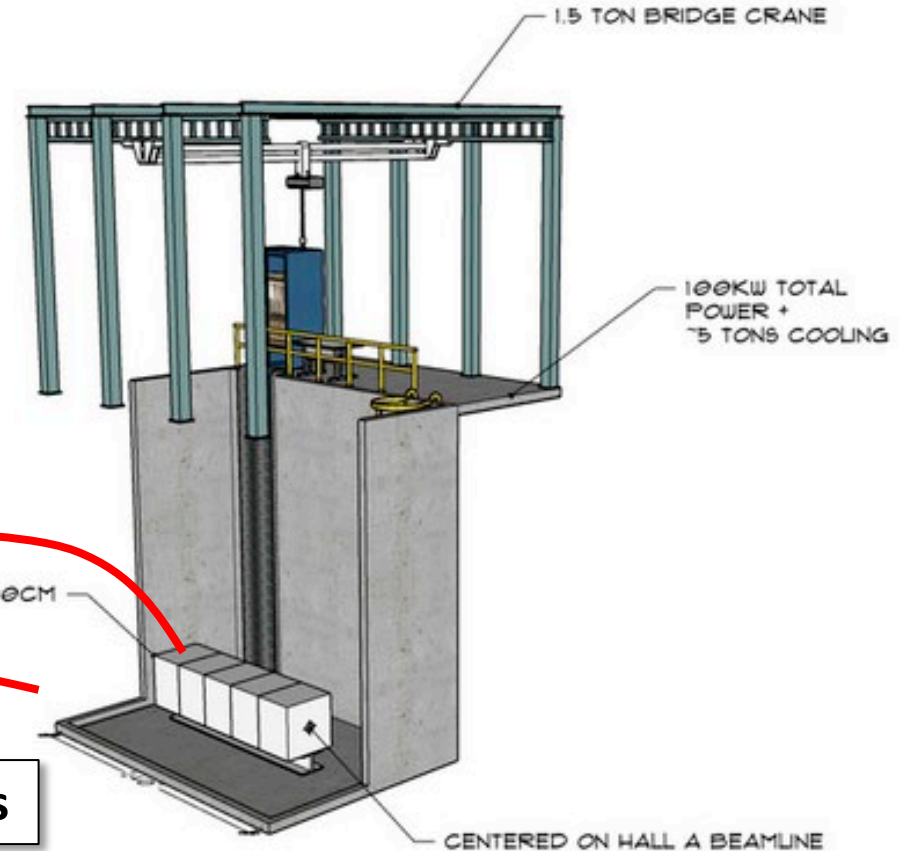


≈ 1000
Calorimeter channels
(30MB/s)

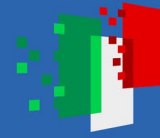
≈ 300
Veto channels
(500MB/s)



5 MODULES AT 80CM X 100CM X 100CM



Very rare occurrence of Dark Matter events



BDX data flow scheme

Location:

Underground

Tent

T.B.D

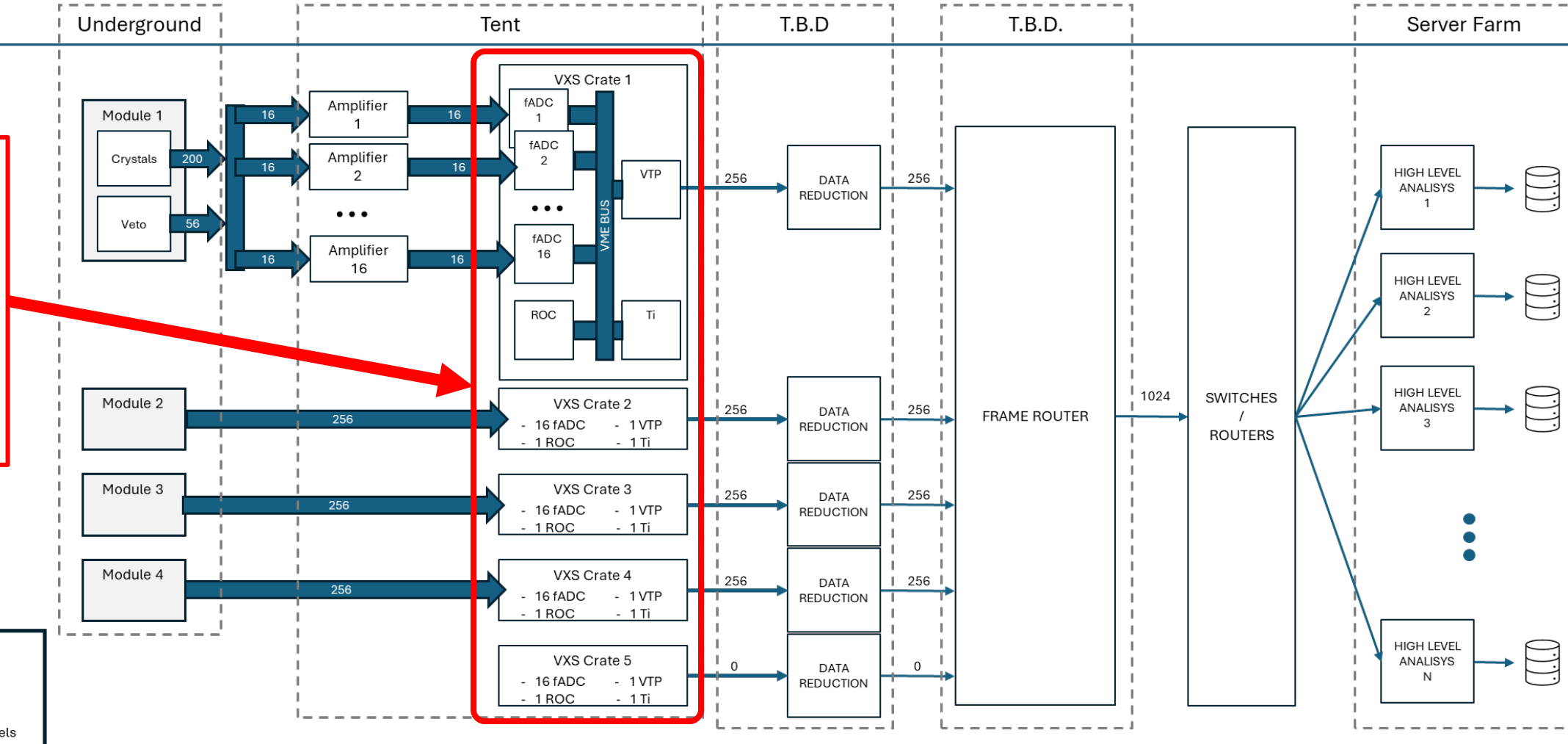
T.B.D.

Server Farm

Available at Jlab

- 64 fADC
- 4 ROC
- 4 Ti
- 4VTP

FOR FREE!

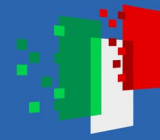




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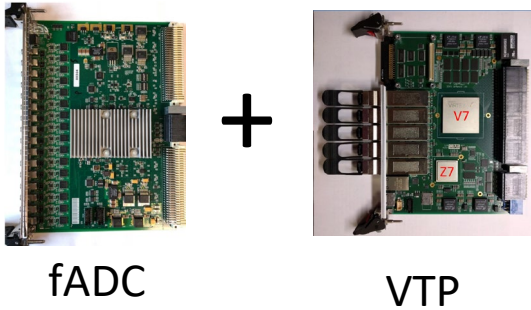
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Future Artificial Intelligence Research

VTP Emulation for testing: Old Approach

Not Available Now



EVIO

The entire Acquisition system is based on specific Hardware

CAEN
DIGITIZER

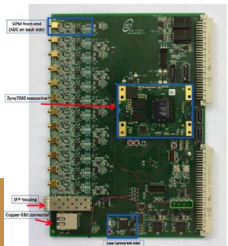


CAEN

CAEN / EVIO

EVIO

WAVE
Board



WAVE

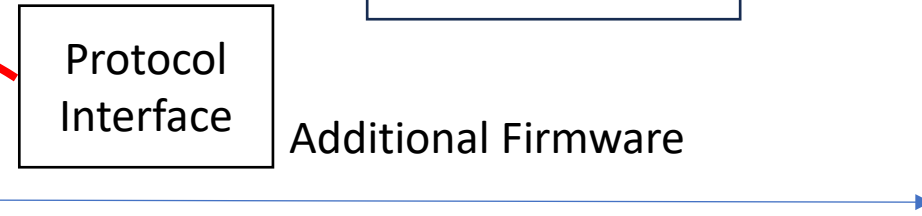
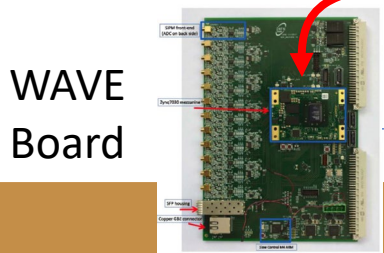
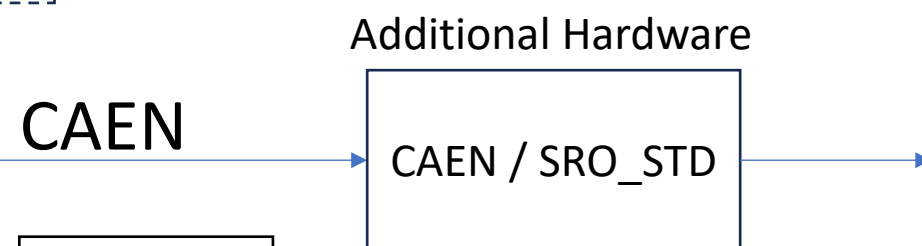
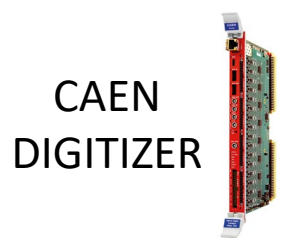
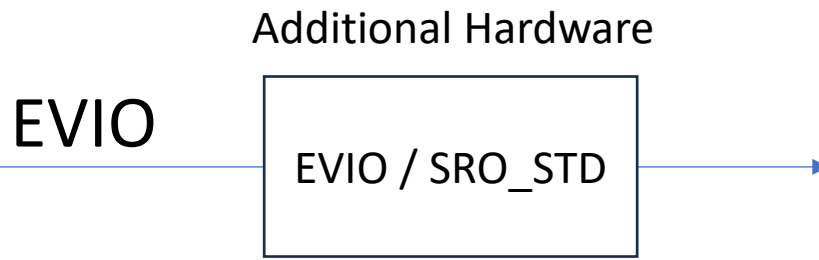
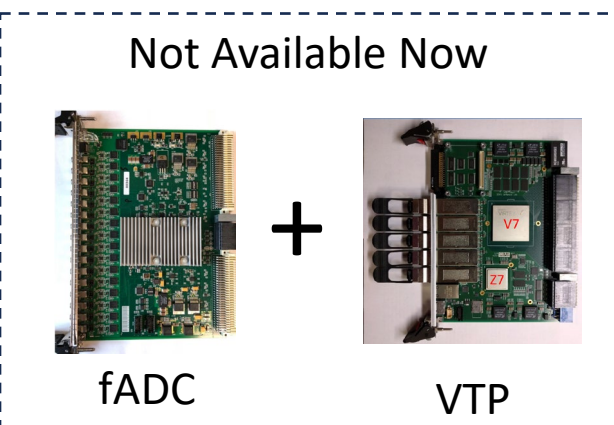
WAVE / EVIO

EVIO

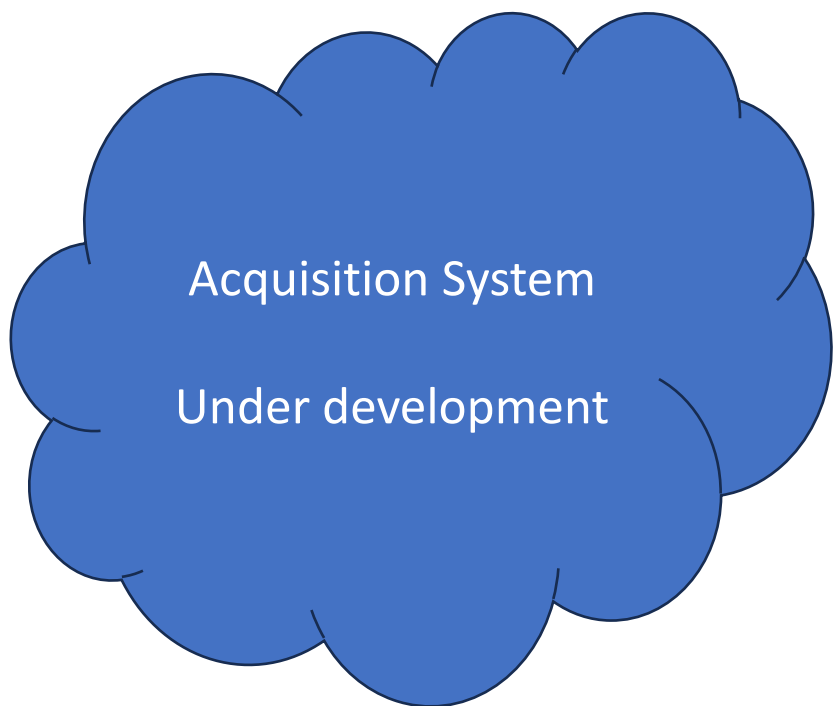
Acquisition System

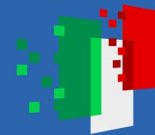
Under development

VTP Emulation for testing: New Approach



The entire Acquisition system is Hardware independent





SRO_STD: Streaming Read-Out Standard protocol

```
struct sro_t
{
    uint16_t id;           //!< Identification of protocol
    uint32_t byte_len;    //!< Total length of packet in byte
    uint8_t board_id;     //!< Unique board identifier number
    uint16_t signal_number; //!< Number of signals in the packet
    uint8_t sample_number; //!< Number of samples of the waveform
    sgn_t *signals;       //!< Signals
};

struct sgn_t
{
    uint64_t timestamp;   //!< Global timestamp
    uint8_t channel_id;   //!< Unique channel identification number
    uint32_t charge;      //!< Sum of all waveform samples
    uint16_t *waveform;   //!< Waveform
};
```



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Thank you for your attention



FUTURE AI RESEARCH

<https://fondazione-fair.it>



<https://www.jlab.org>



<https://www.ge.infn.it>

ACKNOWLEDGMENT

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