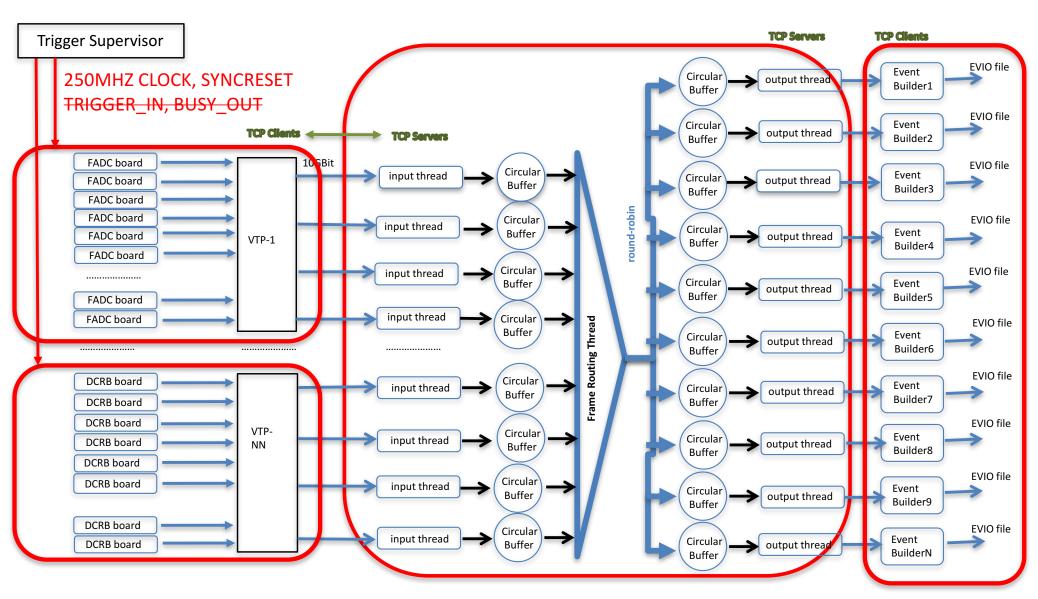
JLAB CLAS12 SRO

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Feb 12, 2025

Hall B Streaming DAQ Diagram

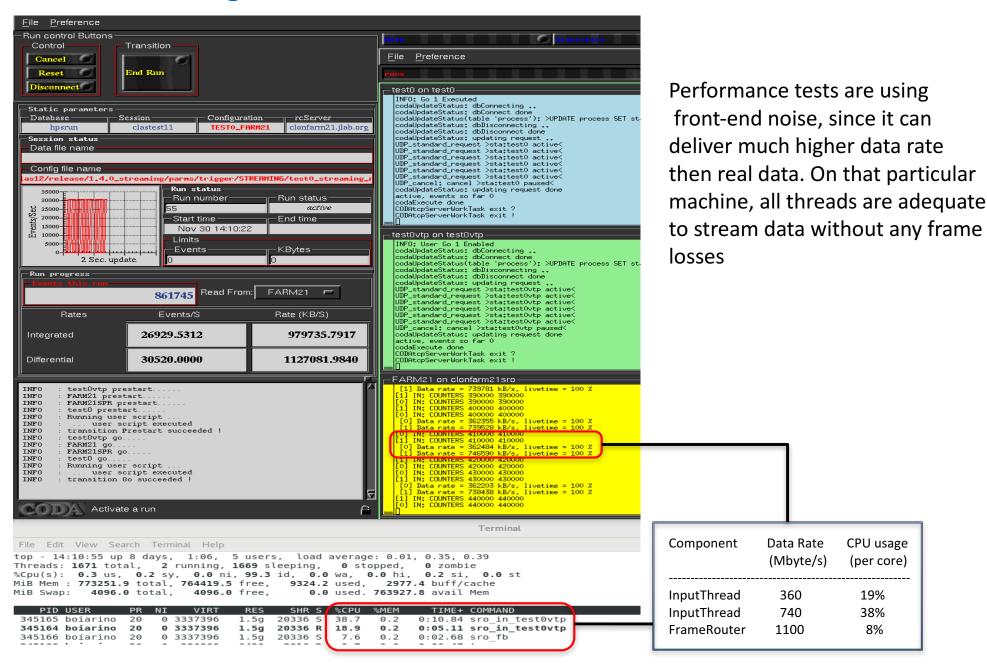


VXS CRATES

FRAME ROUTERS (shown one, can be as many as needed, all running using the same scheduler)

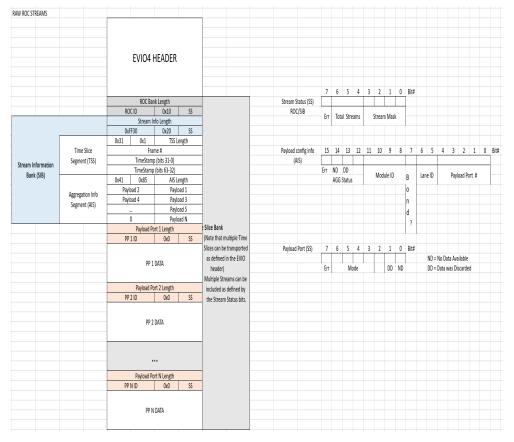
DATA PROCESSORS/ EVENT BUILDERS

CPU usage on clonfarm21: FADC noise test, 2 streams



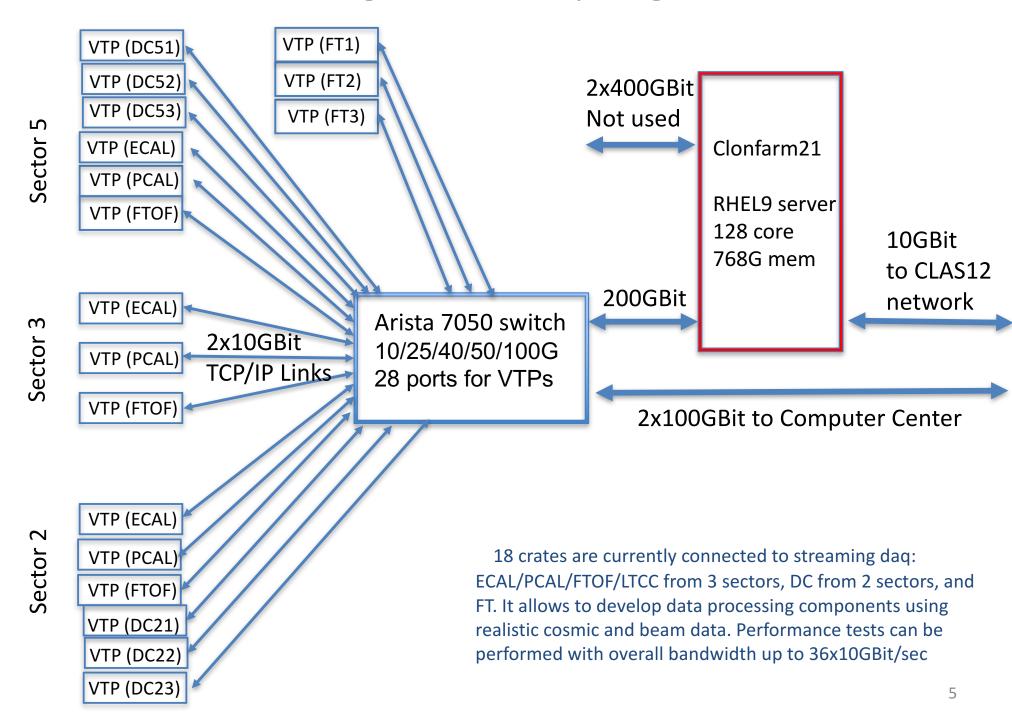
Streaming Data Format

Following is the streaming data format recommended by JLAB DAQ Group (David Abbott / Carl Timmer). Hall B adopted that format for current development. Shown data structure is suitable for raw streaming data only. In case of online data processing, we will try to stick with existing data banks used in triggered daq operation.

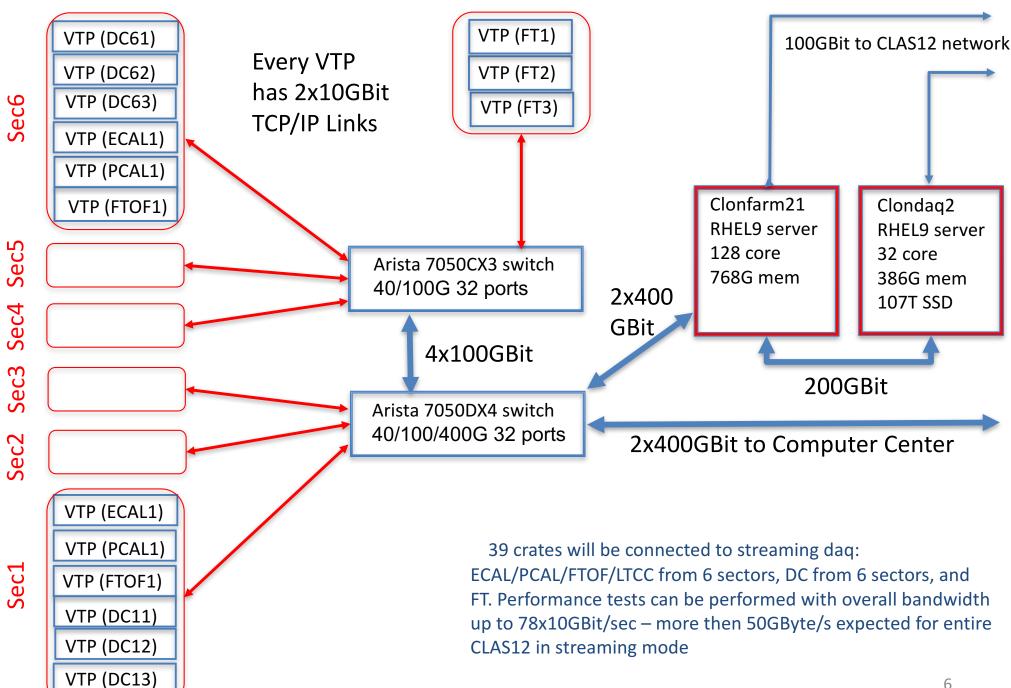


GREGATED ROC STREAM	IS																							
			EVIO6 HEADER																					
											6	5	4	3	2	1	0	Bit#						
			AGG Bank Length 0xFF60 0x10 SS					Stream Status (SS) AGG Bank																
										Err Total Streams														
		Stream Info Length						ious seculis																
,				FF31	0x20	SS																		
Stream Information Bank (SIB) —	Time Slice Segment (TSS)		0x32	0x1		Length																		
			Frame #			_																		
			Average TimeStamp (64 bits)			its)																		
	Aggregation Info Segment (AIS)		0x42	0x1		Length												_			\rightarrow			_
			ROC1			-		ROC N (AIS)	_	ROC ID								SS2	52			SS1		
			ROC 2																					
							-						_											
			ROC N ROC 1 Length				Aggregator (Built) Time		SS1:	Ihis	is a du	plicati	on of t	he Stream Status comir			comin	g drec	tly from	m the H	IUC (so	blank if	the fram	e is
							Bank		***			40						B1. II						
			KO	IC ID	0x10	SS	(Note that multiple Time		SS2:	15	14	13	12	11	10	9	8	Bit#						
							Slices can be transported			-	\vdash								r		r	Γ 1		
			ROC 1 DATA			as defined in the EVIO			Err		Reserved					For missing Frames Err=1 and								
							header)						Reserved holds the low 7 bits of the miss											
			ROC 2 Length ROC ID 0x10 SS			Multiple Streams can be included as defined by																		
			nu	CID	0.10	33	the Stream Status bits.																	
			ROC 2 DATA																					
			ROC N Length																					
						_	_																	
			RO	ICID	0x10	SS																		
			RO	IC ID	0x10	SS																		
			RO			22																		
			RO		0x10 C N DATA	SS																		
			RO			22																		

CLAS12 streaming network/computing- current status



CLAS12 streaming network/computing- plan for 2025



Conclusion

- CLAS12 operates using triggered DAQ with typical event rate up to 30kHz, and will continue to operate same way through high luminosity upgrade, reaching event rate up to 100kHz
- In the same time, CLAS12 readout system being upgraded and eventually will become fully compatible with streaming DAQ mode
- Data processing solutions for the future streaming operation being developed and tested as part of current operation