LD2309 - Fast Timing Detector based on Resistive Micro Well (µRWELL-PICOSEC)

Kondo Gnanvo

JLab Radiation Detector and Imaging Group (RD & I Group)

Quarterly Meeting – January 19th, 2023

Wenze Xi, Jack McKisson, Brian Kross

JLab - RD & I Group

Klaus Dehmelt Stony Brook University

LD2309 - µrPICOSEC: Quarterly report (Q1) Progress and Issues Jefferson Lab

LD203 - µrPICOSEC R&D Group:

- ✤ Group Indico page: <u>https://indico.jlab.org/category/28/</u> Weekly meeting every Monday at 1pm
- ✤ Group Wiki page: <u>https://wiki.jlab.org/urwell_picosec/index.php/Urwell-picosec</u>
- ✤ Member of CERN MM-PICOSEC collaboration

Design and prototyping effort:

- Development of single-pad μ rPICOSEC prototype and tested in the GDD lab at CERN, Dec. 2022. \checkmark
- ✤ Analysis with promising results ongoing
- ✤ Design of µRWELL PCBs with optimized geometry ✓ procurement of the parts ongoing

Multi-channel readout electronics and DAQ

- Test of the single channel fast timing electronic based on programmable differential amplifier (PDA) –
 Wenze Xi, ongoing.
- Design of the pre-prototype board to test multi-channel fast timing electronics Jack McKisson, ongoing

Hiring a postdoctoral research associate

- Position has been created and advertised. \checkmark
 - https://jefferson-lab.us.hire.com/p/a3328681ebdf46ef89d43c61e6ae4703-ldrd-urpicosec-postdoctoral-fellow
- Very few candidates so far have applied \times
- Hope for a few more applications \rightarrow will wait until end January to start candidate selection.

Single-pad urPICOSEC in test @ CERN, Dec 2022



Preliminary results – gain vs. signal arrival time



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LD2309 - µrPICOSEC: Path to completion / Plans for publication



- 1. Complete the hiring of the postdoctoral research scientist → ongoing done by early Q2
 - Start the candidate interview and select candidate for offer (early Q2)
 - ♦ We are a bit worried by the lack of applications so far \rightarrow this is our main concern right now
- 2. Studies of the optimization of µRWELL parameters with single-channel prototypes → ongoing completion Q3
 - Procurement of several single-channels prototypes with different holes geometry (started Q1 Q2)
 - Test of the prototypes in lab setting and in beam at CERN (Q2 Q3)
- 3. Large 100-pad (10 cm × 10 cm) µrPICOSEC prototype → ongoing completion end of Q4
 - Set up of the μ rPICOSEC test bench in RD&I lab for characterization of the prototypes (Q2 Q3)
 - Design finalization of μ RWELL/readout PCB with optimal hole μ RWELL configuration (Q2 Q3) Procurement & assembly (Q3 Q4)
 - Final test of the prototype (possibly also in high magnetic field) at the CERN test beam facility (Q3 Q4) analysis & results (FY24)
- 4. Development of multi-channel fast timing readout electronics → ongoing completion end of Q4
 - PCB layout of PDA-PCB interface board and fabrication of PDA-PCB (Q2 Q3)
 - Integration of a 25-channel readout system with CAEN picoTDC DAQ (Q3 Q4)
 - Test in beam at CERN the multi-channel readout system with the large μ rPICOSEC prototype (Q3 Q4)
- 5. Presentation of the results in conference and plans for publication -> Draft peer-reviewed paper in Q4)
 - Presentation of the main optimization studies results on single-pad prototypes at the IEEE NSS/MIC conference at Vancouver (Nov 2023)
 - Plans to publish the findings of these studies in NIM A or TNS peer-reviewed Start draft manuscript (Q4 FY24)
 - Progress and preliminary results of the large (100-pad) μrPICOSEC prototype will be presented at future RD51 collaboration meetings.

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DEVELOPMENT OF LARGE AREA PICOSECOND TIMING BASED ON RESISTIVE MICRO-WELL DETECTOR (µRWELL-PICOSEC) FOR FUTURE EXPERIMENTS AT JEFFERSON LAB AND AT THE EIC K. GNANVO (LD2309) WBS 1.03.LD.010 (Loaded \$k)



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