

Slow Control (HV/ADC)

Hall-A DVCS Collaboration meeting Dec. 19-20, 2013





Jefferson Lab Thomas Jefferson National Accelerator Facility

DVC\$2010







Thursday, December 19, 2013



DVC\$2010





Thursday, December 19, 2013



Insight E12-06-114







Current status

Year = 2010



E07-007 / E08-025

PbF2, 208=13x16 blocks D= 3x3x18.4 cm³ $\sigma(x) = 2 \sim 3mm$ $\sigma(E) = 5\%/\sqrt{E}$ Year = 2013











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Current status

Year = 2010



Year = 2013







High Voltage System –power-supply

(1) Mount two HV supplies (LeCroy) in rack Mount VME/24V power supply

- @ 1st week in Oct., 2013
- @ 3rd week in Nov., 2013









High Voltage System - cabling

(1) Mount two HV supplies (LeCroy) in rack Mount VME/24V power supply

(2) HV cabling is completed

- 208 + (16 spares) cables are stored in the cart
- Connecting HV cable to patch-panel/HV supply
- Fixed the broken SHV plugs / secure 24V silver supply box





- @ 1^{st} week in Oct., 2013
- @ 3^{rd} week in Nov., 2013

@ 2nd week in Oct., 2013



High Voltage System – cabling to channels

- (1) Mount two HV supplies (LeCroy) in rack Mount VME/24V power supply
- (2) HV cabling is completed
 - 208 + (16 spares) cables are stored in the cart
 - Connecting HV cable to patch-panel/HV supply
 - Fixed the broken SHV plugs / secure 24V silver supply box





Thursday, December 19, 2013 9



- @ 1st week in Oct., 2013
- @ 3rd week in Nov., 2013
- @ 2nd week in Oct., 2013

High Voltage System - electricity

(1) Mount two HV supplies (LeCroy) in rack Mount VME/24V power supply

- (2) HV pre-cabling is completed
 - 208 + (16 spares) cables are stored in the cart
 - Connecting HV cable to patch-panel/HV supply -
 - Fixed the broken SHV plugs / 24V silver supply box

(3) Power line has been secured

- (4) Secure the HV control software
 - RS232 serial port connection control
 - GUI available and editable/ Will test with
 - Secure pre-loadable HV value table (fror
 - Need to more study about PMT R7877 -

- @ 1st week in Oct., 2013
- @ 3rd week in Nov., 2013
- @ 2nd week in Oct., 2013



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(4) Secure the HV control software

@ 3rd week in Nov., 2013

- RS232 serial port connection control (new serial card)
- GUI available and editable/ Will test with local PC (or VME intel w/ serial-port)
- Secure pre-loadable HV value table (from run#9870 @ 12/23/2010)





High Voltage System - control



- (4) Secure the HV control software
 - ol software @ 2nd week in Dec., 2013
 - Secure a land-line network and computer is online
 - Successful communication / control the both HV crates
 - HV GUI is working properly
 - Need a bit more improvement (speed/diagnostics)
 - Another network jack installation for phone was requested







🧠 Applications	Places	System 🎈	€\$\$\$		
			GUI		
		1 ()	1.4		High Voltage Sv

∑ 🔋 4:40 PM 🚯

		GUI											
	Monitor:dvcs_tlab1:2004 in sleep()	<u></u>				Hig	h Voltage S	iystem Co	ntrol				
	Client to clrlpc:2001:GS	<u>File Edit Vie</u>	w <u>M</u> ap <u>/</u>	<u>A</u> larm <u>T</u> ools									Help
	Client to dvcs tlab1:2004:GS	Cirinc200	1 🕒 dvcs	s tlab1:2004]								
	Monitor:dvcs_tlab1:2004 in sleep()	Chipazoo			L								
	Client to clrlpc:2001:GS	1458	S0 S2	2 54 56	S8 S10	S12	S13 S14	7					
	Client to dvcs_tlab1:2004:GS	[]	Ch name	Moas uA	Moas V	Target V	RUp V/s	PDn V/s	Trip uA	Ch En	Status	MVDZono I	UCDZopo
	Monitor:dvcs_tlab1:2004 in sleep()	PANIC	150	03	-50.2	_49 5	612	61.2	-2550.0		0001	15	13-3120
	Client to clrlpc:2001:GS Monitortclplpc:2001 in sleep()		L5.1	0.0	-44.0	-43.2	61.1	61.1	-2550.0	~	0001	1.5	1.3-3120
	Client to dvcs_tlab1:2004:GS		L5.2	0.1	-23.9	0.0	61.3	61.3	-2550.0		0000	1.5	1.3-3120
	Monitor:dvcs_tlab1:2004 in sleep()		L5.3	0.4	-26.0	0.0	60.9	60.9	-2550.0		0000	1.5	1.3 - 3120
	Client to ciripo:2001:65 Client to ducs tlab1*2004*65	(ETV)	L5.4	-0.7	-23.8	0.0	61.6	61.6	-2550.0		0000	1.5	1.3 - 3120
	Monitor:dvcs_tlab1:2004 in sleep()		L5.5	-0.1	-23.4	0.0	61.3	61.3	-2550.0		0000	1.5	1.3-3120
	Monitor:clrlpc:2001 in sleep()	OFF	L <u>5.6</u>	-0.3	-22.5	0.0	61.2	61.2	-2550.0		0000	1.5	1.3 - 3120
	Monitor:dvcs_tlab1:2004_in_sleep()		L5./	0.0	-26.8	0.0	61.2	61.2	-2550.0		0000	1.5	1.3 - 3120
		ctature	15 9	-0.0	-22.4		61.1	61.1	-2550.0		0000	1.5	13-3120
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	> (Wed Dec 11 16:33:35 2013) 9	e remote	15.11	0.3	-24.4	0.0	61.5	61.5	-2550.0		0000	1.5	1.3-3120
	> (Wed Dec 11 16:39:35 2013) r	e	1	1010							0000	110	
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	> (Wed Dec 11 16:39:36 2013) 9												
	> (Wed Dec 11 16:39:36 2013) r												
	> (Wed Dec 11 16:33:37 2013) g												
	> (Wed Dec 11 16:39:37 2013) 9	e 04:36:42 PM	í Dec 11, 2	013 > clrlpc	:2001:HVO	V							
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	> (Wed Dec 11 16:33:38 2013) r	e 04:35:16 PM	í Dec 11, 2	2013 > Start 1	HV server –	> port: 55	55						
	> (Wed Dec 11 16:39:39 2013) 9	e 04:35:16 PM	í Dec 11, 2	2013 > INIT 1	TIME:99 (sea	:)							
	> (Wed Dec 11 16:39:39 2013) r > (Wed Dec 11 16:39:39 2013) [04:35:15 PM	í Dec 11, 2	2013 > HV m	onitor starte	ed -> clrlp	c:2001						
	> (Wed Dec 11 16:39:44 2013) [1 04:34:13 PM	í Dec 11, 2	$2013 > dvcs_$	tlab1:2004:	HVOFF							
	> (Wed Dec 11 16:39:44 2013) H	V 04:34:13 PN	[Dec 11, 2	2013 > HV m	onitor starte	ed -> dvcs	_tlab1:2004						
	> (Wed Dec 11 16:33:44 2013) [04:33:45 PM	1 Dec 11, 2	2013 > End in	nitialization								
	> (Wed Dec 11 16:39:49 2013) H	04:33:45 PM	1 Dec 11, 2	$2013 > dvcs_$	tlab1:2004:	HVOFF							
	> (Wed Dec 11 16:39:49 2013) [R 04:33:43 PM	1 Dec 11, 2	2013 > ciripc	:2001:								
	> (Wed Dec 11 16:33:54 2013) H	.4∥ V 1400_nanaror, o	,										
	> (Wed Dec 11 16:39:54 2013) [R] 'GS 0005 0002 (001 0158 00	27'					-				
	> (Wed Dec 11 16:39:59 2013) [> (Wed Dec 11 16:39:59 2013) H	1 - 129.97.36.179. 171458 handler: '69	11 65 S'				3			Termi	nal		
	> (Wed Dec 11 16:39:59 2013) U	pdating stale GS.					File Edit \	/iew Termi	inal Tabs H	Help			
	> (Wed Dec 11 16:39:59 2013) g	eneric_cmd(PSUM, l	.0)					d Dec 11	16:30:47 2	 0.13) [B	1 'GS 000B	0006 0002 0	233 0018'
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	> (Wed Dec 11 16:40:00 2013) r	emcmd: 1 0 PSUM					onnection!	d Dec 11	10.50.47 2	.015) 00	boo. etien	[125.57.50	.175] (10 2) 000
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	> (Wed Dec 11 16:40:01 2013) 9	eneric_cmd(PSUM, l	.3)				dvcs@clrlp	.ilab.ord	i> ls				
	> (Wed Dec 11 16:40:01 2013) r	emend: 3 0 PSUM	4				bob hvs.ta	enabl	, le telnet.t	xt L	ecroyHV sh	im script	s
	> (Wed Dec 11 16:40:02 2013) 9	eneric_cma(PSUM, i emcmd: 5 0 PSUM	_4)				cidserver_	.5 hvg.t	bob	L	ecroyHV_Sh	im shim	
	> (Wed Dec 11 16:40:02 2013) 9	eneric_cmd(PSUM, l	.5)			 (OVCS	hvs		р	erl	stderr	.out
	> (Wed Dec 11 16:40:02 2013) r	emend: 7 0 PSUM	c)			1	DVCS_lower	hv_se	etup_5dec13	.tar R	EADME		
	> (Wed Dec 11 16:40:03 2013) g	eneric_cma(FSUM, i emcmd: 9 0 PSUM	-6)			. .	DVCS_upper	Lecro	yHV_FE	R	unHVCrate1	.exe	
	> (Wed Dec 11 16:40:03 2013) 9	eneric_cmd(PSUM, l	.7)				dvcs@clrlp	.jlab.org]> pwd				
	> (Wed Dec 11 16:40:03 2013) r	emend: 11 0 PSUM	8)				/home/dvcs	/slowc					
	> (Wed Dec 11 16:40:04 2013) g	emend: 13 0 PSUM	.0)				dvcs@clrlp	.jlab.org	g> ./RunHVC	rate1.e	xe		
	> (Wed Dec 11 16:40:05 2013) 9	eneric_cmd(PSUM, l	.9)			1	Wed Dec 11	16:32:36	EST 2013				
	> (Wed Dec 11 16:40:05 2013) r	emend: 15 0 PSUM R1 '65 0005 0002 (001 0163 00	28'			connection	is establ	ished				
	(wed bec 11 10;40;00 2013) [M 03 0003 0002 (ANT 0103 00.	20			Wed Dec 11	16:33:36	EST 2013				
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Monitor:clrlpc:2001 in sleep()		High Volta	ge System Control									
Monitor:dvcs_tlab1:2004 in sleep()	dit <u>View Map A</u> larm <u>T</u> ools				Help							
Client to clrlpc:2001:6S	lpc2001 🛛 오 dvcs_tlab1:2004											
Monitor:dvcs_tlab1:2004 in sleep()												
Monitor:clrlpc:2001 in sleep()	8 S0 S1 S2 S3 S	5 S7 S9 S11 S1	3 S15									
Monitor:dvcs_tlab1:2004 in sleep()	ANIC Ch_name Meas_uA M	eas_V Target_V RUp_V	/s RDn_V/s Trip_uA	Ch_En Status	MVDZone MCDZone HVL							
Client to clrlpc:2001:GS		1.8 _55.0 6	1.1 61.1 -2550.0	0001								
Monitor:clrlpc:2001 in sleep() Client to dycs tlab1:2004:GS	L0.2 0.0 -13	3.1 0.0 6	1.2 61.2 -2550.0	0000	1.5 1.3 -3120							
Monitor:dvcs_tlab1:2004 in sleep()	LO.3 -0.0 -13	3.2 0.0 6	1.5 61.5 -2550.0	0000	1.5 1.3 - 3120							
Monitor:clrlpc:2001:65	LO.4 0.2 -13	3.9 0.0 6	1.1 61.1 -2550.0	0000	1.5 1.3 - 3120							
Client to dvcs_tlab1:2004:GS	INI LO.5 -0.1 -12	2.0 0.0 6	1.4 61.4 -2550.0	0000	1.5 1.3 3120							
Monitor:dvcs_tlab1:2004 in sleep()	[107 02 -12]		1.0 61.0 -2550.0									
Monitor:clrlpc:2001 in sleep()	LO.8 0.0 -14	4.8 0.0 6	1.4 61.4 -2550.0	0000	1.5 1.3 3120							
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> (Wed Dec 11 16:39:44 2013) [1 > (Wed Dec 11 16:39:44 2013) HM 04:37	7:33 PM Dec 11, 2013 > dvcs_tlab	1:2004:HVON										
> (Wed Dec 11 16:39:44 2013) [R 04:36	5:42 PM Dec 11, 2013 > clrlpc:200	1:HVON										
> (Wed Dec 11 16:39:49 2013) [1] 04:35	1 04:35:16 PM Dec 11, 2013 > clrlpc:2001:HVOFF											
> (Wed Dec 11 16:39:49 2013) [R 04:35	5:16 PM Dec 11, 2013 > Start HV s	server -> port: 5555										
> (Wed Dec 11 16:39:54 2013) [1] 04:35	5:16 PM Dec 11, 2013 > INIT TIME 5:15 PM Dec 11, 2013 > HV monit	or started => ciripc:2001			=							
> (Wed Dec 11 16:39:54 2013) [R 04:34	4:13 PM Dec 11, 2013 > dvcs tlab	1:2004:HVOFF										
> (Wed Dec 11 16:39:59 2013) [1] 04:34	4:13 PM Dec 11, 2013 > HV monito	or started -> dvcs_tlab1:2	004									
> (Wed Dec 11 16:39:59 2013) Up 04:33	3:45 PM Dec 11, 2013 > End initial	lization										
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> (Wed Dec 11 16:40:02 2013) generic_cmd	(PSUM, L5)	>	(Wed Dec 11 16:30:47	2013) [R] 'GS 000B	0006 0002 0233 0018'							
> (Wed Dec 11 16:40:02 2013) remond: 7 0 > (Wed Dec 11 16:40:03 2013) generic cmd) PSUM I(PSUM, L6)	>	(Wed Dec 11 16:30:47	2013) DEBUG: Client	[129.57.36.179] (id 2) clo							
> (Wed Dec 11 16:40:03 2013) remcmd: 9 0	PSUM	onnect1	on! (11 16:30:40 Corver cl	acinal								
> (Wed Dec 11 16:40:03 2013) generic_cmd	0 PSUM	2013/12 dvcs@c1	rlnc ilab orga ls	USING:								
> (Wed Dec 11 16:40:04 2013) generic_cmd	I(PSUM, L8)	bob hvs	tar enable telnet.	txt LecrovHV shi	m scripts							
> (Wed Dec 11 16:40:04 2013) remond: 13 > (Wed Dec 11 16:40:05 2013) generic cmd	U PSUM H(PSUM, L9)	cidserv	er_0.5 hvg.bob	LecroyHV_Shi	m shim							
> (Wed Dec 11 16:40:05 2013) remond: 15	O PSUM	DVCS	hvs	perl	stderr.out							
> (Wed Dec 11 16:40:05 2013) [R] 'GS 0000 > (Wed Dec 11 16:40:10 2013) [1 - 129.57	7.36.179]: GS	DVCS_lo	wer hv_setup_5dec1	3.tar README								
> (Wed Dec 11 16:40:10 2013) HV1458_hand	fler: 'GS'	DVCS_up	per LecroyHV_FE	RunHVCrate1.	exe							
> (Wed Dec 11 16:40:10 2013) [R] 'GS 0000 > (Wed Dec 11 16:40:15 2013) [1 - 129.57	7.36.179]: GS	dvcs@cl	ripe.jiab.org> pwd									
> (Wed Dec 11 16:40:15 2013) HV1458_hand	fler: 'GS'	dvcs@c1	rlpc.ilab.org> /RunHV	Cratel.exe								
> (Wed Dec 11 16:40:15 2013) [R] 'GS 000 > (Wed Dec 11 16:40:20 2013) [1 - 129.57	/_36.179]: GS	Wed Dec	11 16:32:36 EST 2013	er decirente								
> (Wed Dec 11 16:40:20 2013) HV1458_hand	fler: 'GS'	connect	ion is established									
> (Wed Dec 11 16:40:20 2013) [R] 'GS 000	05 0007 0001 01PP 002B.	Wed Dec	11 16:33:36 EST 2013		=							

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High Voltage System – map file

=== DVCS Calorimeter ===

recording at Thu Dec 23,05:26:49 EST 2010

Measured HV voltages

0	:	-495	-515	-494	-537	-515	-520	-588	-482	-516	-538	-489	-476	-526
1	:	-528	-512	-538	-472	-509	-469	-458	-486	-453	-484	-479	-473	-463
2	:	-546	-536	-518	-478	-488	-479	-501	-470	-472	-502	-502	-493	-525
3	:	-542	-483	-478	-444	-466	-450	-468	-456	-487	-479	-486	-495	-471
4	:	-576	-525	-524	-466	-469	-566	-500	-473	-473	-489	-488	-480	-518
5	:	-569	-467	-492	-529	-479	-522	-484	-442	-475	-467	-445	-528	-490
6	:	-565	-520	-529	-509	-500	-470	-509	-510	-518	-470	-454	-499	-505
7	:	-545	-548	-524	-470	-501	-551	-487	-450	-467	-508	-454	-494	-552
8	:	-534	-537	-499	-480	-511	-501	-555	-433	-477	-475	-453	-517	-477
9	:	-506	-485	-482	-547	-509	-527	-501	-493	-537	-472	-456	-527	-447
10	:	-512	-528	-517	-518	-500	-468	-498	-508	-442	-515	-451	-516	-475
11	:	-545	-523	-475	-521	-482	-549	-538	-518	-487	-520	-490	-486	-488
12	:	-488	-501	-506	-466	-472	-470	-483	-454	-496	-473	-459	-511	-500
13	:	-476	-542	-501	-521	-507	-553	-484	-526	-532	-472	-496	-497	-492
14	:	-561	-532	-537	-520	-474	-492	-502	-475	-525	-467	-465	-500	-510
15	:	-512	-504	-499	-490	-527	-490	-485	-532	-535	-516	-504	-462	-516
Meas	ure	ed HV c	currents											
0	:	-473	-519	-490	-544	-510	-525	-550	-478	-512	-540	-485	-471	-523
1	:	-499	-516	-532	-475	-504	-474	-431	-481	-448	-488	-476	-469	-461
2	:	-518	-540	-511	-484	-484	-482	-466	-466	-468	-510	-497	-489	-523
3	:	-522	-486	-472	-453	-461	-454	-432	-453	-480	-485	-482	-490	-470
4	:	-528	-528	-521	-469	-463	-524	-495	-468	-470	-495	-483	-479	-516
5	:	-538	-470	-489	-533	-475	-490	-440	-438	-472	-475	-442	-527	-490
6	:	-528	-522	-525	-512	-498	-465	-504	-505	-515	-476	-451	-498	-503
7	:	-509	-552	-519	-477	-495	-512	-465	-445	-465	-514	-452	-489	-551
8	:	-529	-533	-494	-485	-519	-455	-522	-432	-472	-481	-451	-515	-478
9	:	-475	-482	-482	-551	-515	-503	-499	-491	-532	-476	-454	-524	-450
10	:	-509	-524	-513	-523	-506	-462	-465	-504	-437	-520	-448	-512	-470
11	:	-541	-520	-470	-526	-488	-507	-518	-516	-484	-525	-487	-481	-487
12	:	-492	-495	-498	-458	-477	-432	-480	-451	-490	-470	-456	-508	-499
13	:	-484	-535	-499	-517	-511	-496	-480	-524	-528	-471	-492	-493	-493
14	:	-567	-527	-532	-518	-479	-460	-498	-466	-521	-465	-461	-497	-502
15	:	-516	-499	-493	-483	-532	-485	-480	-526	-530	-512	-499	-459	-513
reco	recording at Thu Dec 23 05:26:50 EST 2010													





http://www Hall-A wiki













500			bloc	k 63 H	nstead	of 23	3							-	
15	-512	-504	-499	-490	-527	-490	-485	-532	-535	-516	-504	-462	-516		
4540	-5 <mark>61</mark>	-532	-537	-5 <mark>2</mark> 0	- <mark>47</mark> 4	-492	-502	-475	-525	-467	-465	-500	-510	30	1.0
13	-476	-542	- <mark>5</mark> 01	-521	-507	-553	-484	<mark>-5</mark> 26	-532	-472	-496	-497	-492		
12	-488	-501	-506	-466	-472	-470	-483	-454	-496	-473	-459	- 511 243	-500		
3510	-545	-523	-475	-521	-482	-549	-538	<mark>-5</mark> 18	- <mark>48</mark> 7	<mark>-52</mark> 0	-490	- <mark>4</mark> 86	-488	-30). /
10	-512	-528	- 51 7	-518	-500	-468	-498	-508	-442	- 515	-451	<mark>-5</mark> 16	-475		
3 09	-506	-485	-482	-547	- <mark>50</mark> 9	-527	-501	-493	-537	-472	-456	-527	-447		
08	-534	-537	- <mark>49</mark> 9	-480	- 511 1.46	-501	- 555 126	-433	-477	<mark>-475</mark>	-453	- <mark>5</mark> 17	-477	30).6
07	-545	-548	- <mark>52</mark> 4	-470	- <mark>50</mark> 1	-551	-487	-450	-467	<mark>-5</mark> 08	-454	<mark>-4</mark> 94	-552		
206)	-565	-5 <mark>2</mark> 0	-529	-509	-500	-470	-509	<mark>-510</mark>	- <mark>51</mark> 8	<mark>-47</mark> 0	-454	<mark>-4</mark> 99	-505		
05	-5 <mark>69</mark>	-467	-492	-529	- <mark>47</mark> 9	<mark>-522</mark>	-484	-442	-475	-467	-445	- <mark>5</mark> 28	-490	- 30).5
04	-576	-5 <mark>2</mark> 5	-524	- 466 284	- <mark>46</mark> 9	-5 <mark>6</mark> 6	- 50 0	<mark>-473</mark>	- 47 3	<mark>-4</mark> 89	-488	<mark>-480</mark>	-518		
1 030	-542	-483	- <mark>47</mark> 8	-444	-466	-450	-468	-456	-487	-479	-486	-495	-471		
02	-546	-536	-518	- 478	- <mark>48</mark> 8	- 479	- 501	<mark>-47</mark> 0	- 472	<mark>-502</mark>	-502	<mark>-4</mark> 93	-525 121	- 30).4
010	-528	-512	- 538	-472	-509	-469	-458	-486	-453	-484	-479	-473	-463		
00	-495	-515	- <mark>49</mark> 4	-537	- <mark>51</mark> 5	-520	-588	<mark>-482</mark>	- <mark>51</mark> 6	-538	-489	-476	-526		
4	0	50	1	00	15	0	200	2	50	30	0 3	850	40	0	



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Thank you





Electronics

- 1 VME64X crate with intel CPU in FPP rack
- Readout and decoder using Intel CPU (90 MB/s per crate)
- Calorimeter trigger installed
 - Logic and readout on separate FPGA
 - larger bus for interFPGA transfer to not loose resolution on threshold
 - block transfer mode implemented but need to be debugged and tested
- Review in March

- from Alexandre Camsonne at Hall-A meeting





TASK time line

- Calorimeter
 - Stack calorimeter 2 weeks 0
 - Test cosmics Ο
 - Test DC current ADC (missing patch panel) 1 week Ο
 - Layout RG213 install stand 2 month Ο
- **DVCS** Electronics
 - Install 2nd and 3rd VME crate 3 days 0
 - Test trigger Block Transfer (Need engineer for debugging) Ο 2 weeks
 - Cable trigger 3 days Ο
 - Timing with HRS 3 days Ο

- from Alexandre Camsonne at Hall-A meeting





- end Dec. 2013
- Jan. 2014 to Jun. 2014

- Feb. 2014

– Jan. 2014

- Jan. 2014
- Jan. 2014

TASK time line

- DVCS electronics
 - Test high rates full setup
- Hall A standard equipment
 - HRS
 - High Resolution VME TDC 2 weeks
 - Scintillator timing 1 week
 - BCM -1 week
 - BPM 1 week
 - Helicity 1 week
 - Polarimetry

- end Feb. 2014

- Feb. 2014
- Feb / Mar. 2014
- Jan. 2014
- Jan. 2014
- Jan. 2014

- from Alexandre Camsonne at Hall-A meeting





Crystals

- (1) Separated into two steps for wrapping
 - Take a photo/Clean Crystal/white paper wrap
 - Applying optical grease (~90sec)/engage PMT/black paper wrap
- (2) 86% completion of inventory
 - 180 crystals are done, 28 are under-going
 - Completed 4 spares/ 5(+3) spares will be available (due to limited base)
 - All crystal work : Thank to Chris Wooten
 - Repair brass (soldering)/storage shelves : Thanks to Tom Hartlove

(3) Testing will start

(4) Will document a correct assembly procedure





@ Dec. 07, 2013

@ Nov. 14, 2013

Crystals

- (1) Separated into two steps for wrapping
 - Take a photo/Clean Crystal/white paper wrap
 - Applying optical grease (~90sec)/engage PMT/black paper wrap
- (2) 86% completion of inventory
 - 180 crystals are done, 28 are under-going
 - Completed 4 spares/ 5(+3) spares will be available (due to limited base)
 - All crystal work : Thank to Chris Wooten
 - Repair brass (soldering)/storage shelves : Thanks to Tom Hartlove
 - Delivered the 156 crystals to Jlab
 - Rest of them will be Jlab soon

(4) Will





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@ Nov. 14, 2013



 \mathbb{DVCS}

Hadron

CJSA

(D

Hadron







DVCS full amplitude =



$$T_{VCS}(e^{\pm}) = \bar{u}(k',\lambda)\gamma_{\mu}u(k,\lambda)\frac{\pm e}{q^2}\boldsymbol{H}^{\mu\nu}\epsilon_{\nu}^{\dagger}$$

Hadronic tensor =

$$\begin{aligned} \boldsymbol{H}^{\mu\nu}{}_{LO,twist2} &= \\ \frac{1}{2} (-g^{\mu\nu})_{\perp} \ \overline{U}(p') \left[(n \cdot \gamma) H(\xi, t) + \frac{i}{2M} n_{\kappa} \sigma^{\kappa\lambda} \Delta_{\lambda} E(\xi, t) \right] U(p) \\ &- (\epsilon^{\mu\nu})_{\perp} \ \overline{U}(p') \left[(n \cdot \gamma \gamma_5) \widetilde{H}(\xi, t) + (\gamma_5 n \cdot \Delta) \widetilde{E}(\xi, t) \right] U(p) \end{aligned}$$





DVCS

CFF by the integration over the quark loop =



$$\begin{aligned} \mathbf{F}_{\mathbf{F}} &= \int_{-1}^{+1} dx \left[\frac{1}{x - \xi + i\epsilon} + \frac{1}{x + \xi - i\epsilon} \right] [H, E](x, \xi, t) \\ &= \int_{-1}^{+1} dx \left[\frac{1}{x - \xi + i\epsilon} - \frac{1}{x + \xi - i\epsilon} \right] [\widetilde{H}, \widetilde{E}](x, \xi, t) \end{aligned}$$

Azimuthal modulation =

$$|T_{DVCS}|^{2} = \frac{e^{6}(s_{e} - M^{2})^{2}}{x_{B}^{2}Q^{6}} \left[\sum_{n=0}^{2} c_{n}^{DVCS} \cos(n\phi_{\gamma\gamma}) + \sum_{n=1}^{2} s_{n}^{DVCS} \sin(n\phi_{\gamma\gamma}) \right]$$



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\mathbb{DVCS}



Azimuthal modulation =

Physics

$$|T_{DVCS}|^{2} = \frac{e^{6}(s_{e} - M^{2})^{2}}{x_{B}^{2}Q^{6}} \left[\sum_{n=0}^{2} c_{n}^{DVCS} \cos(n\phi_{\gamma\gamma}) + \sum_{n=1}^{2} s_{n}^{DVCS} \sin(n\phi_{\gamma\gamma}) \right]$$



