EIC in the Streaming/AI Era

Rolf Ent, EIC Project, Co-Associate Director, Experimental Program





May 17, 2022



EIC Timeline – Operations Start a Decade from Now



EIC: Fully Integrated Detector/Interaction Region++

Far-Backward Region ~ 40 meter

Far-Forward Region ~ 40 meter





Streaming Readout at the EIC

(remote, small) Streaming Readout I @ JLab & MIT January 2017

January 2018 May 2018

(in-person) Streaming Readout II @ MIT Submitted first eRD23 SRO proposal (Pl's Jan Bernauer, Marco Battaglieri) "Grand Challenge in Readout and Analysis for November 2018 Femtoscale Science" (Boehnlein, Ent, Yoshida)



November 2018 INDRA Facility Ready

Lots of progress, by 2020 streaming was the default assumption in the **EIC Yellow Report!**

May 2022 Streaming Readout X **DMENTUM!!!!**



Innovation in Nuclear Data Readout and Analysis (INDRA) Facility towards streaming readout, right next to DAQ lab & computing facility



Grand Challenge in Readout and Analysis for Femtoscale Science

Grand Challenge in Readout and Analysis for Femtoscale Science

Amber Boehnlein, Rolf Ent, Rik Yoshida

November, 2018

Introduction

Micro-electronics and computing technologies have made order-of-magnitude advances in the last decades. Combined with modern statistical methods, it is now possible to analyze scientific data to rapidly expose correlations of data patterns and compare with advanced theoretical models. While many existing nuclear physics and high-energy physics experiments are taking advantage of these developments by upgrading their existing triggered data acquisition to a streaming readout model, these experiments do not have the luxury of an integrated systems from DAQ through analysis. Hence, we aim to remove the separation of data readout and analysis altogether, taking advantage of modern electronics, computing and analysis techniques in order to build the next generation computing model that will be essential for probing femto scale science.

Integrated Whole-Experiment Model

An integrated whole-experiment approach to detector readout and analysis towards scientific output is summarized in the following figure.



Key Elements

An integrated whole-experiment approach to detector readout and analysis towards scientific output will take advantage of multiple existing and emerging technologies. Amongst these are:

- "Streaming readout" where detectors are read out continuously.
- Continuous data quality control and calibration via integration of machine learning technologies.
- Task based high performance local computing.
- Distributed bulk data processing at supercomputer centers.
- Modern statistical methods that can detect differences among groups of data
 or associations among variables even under very small departures from
 normality.

Existing and Proposed Efforts

Several of the current LDRD proposals as well as separate on-going efforts naturally fit into the framework of the integrated whole-experiment model of data handling and analysis. They are

- Jefferson Lab EIC science related activities
 - Web-based Pion PDF server
- Jefferson Lab and related part of the Streaming Consortium proposal to the EIC Detector R&D committee including
 - Crate-less streaming prototype
 - TDIS streaming readout prototype
 - EM Calorimeter readout prototype
 - Computing workflow distributed heterogeneous computing
- LDRD proposals
 - JANA development 2019-LDRD-8
 - o Machine Learning MC 2019-LDRD-13
 - Streaming Readout 2019-LDRD-10

Grand Challenge

Develop a proof of concept of quasi-instantaneous high-level nuclear physics analysis based on modern statistics from a self-calibrated matrix of detector raw data synchronized to a reference time, without intermediate data storage requirements, with production systems developed for late stage 12 GeV analysis and the Electron Ion Collider. We propose organizing some of the LDRD proposals and other exploratory work around these themes to achieve proof of concept.

See the buzzwords!!!

- Streaming
- Calibration/ML
- Distributed Computing
- Heterogeneous
- Statistical Methods

What Do We Need for Streaming Readout?

- A streaming DAQ design has several key elements :
 - A data source outputting on fiber.
 - A front end buffer and data processing system with FPGA.
 - A high speed low latency network.
 - An online compute resource to buffer and process data.
 - A timing system.

Detector FE HW Front end 5 GB/s Switched Network 1 TB Switched Back end Detector FE HW Online buffe Network and preprocess preprocess FE HW Detector Rest of the Detector

Slide of Graham Heyes to introduce what was going on in the INDRA lab (2019) – all pieces already being worked on...



Benefits of Streaming Readout

Streaming



Based on slide of Marco Battaglieri

All channels continuously measured and hits streamed to a HIT manager (minimal local processing) with a time-stamp

- + All channels can be part of "the trigger", no bias
- + Simplification of readout: No custom trigger hardware and firmware to implement & debug
- + Enables sophisticated tagging/filtering algorithms
- + Allows use of high-level programming languages
- + Ease of scalability
- + Takes advantage of emerging technologies
 - $\circ~$ Allows use of available AI/ML tools
 - Allows use of heterogeneous computing
- + Allows rapid turnaround of physics data



Jefferson Lab Streaming Readout Highlights (2021 slide)

- Streaming: Data streams off the detector at a fixed rate.
 - Flexibility of Streaming allows for more online processing
- Activities:
 - Proof of concepts for CLAS12 High Intensity Running
 - Calorimeter and GEMs
 - Streaming concepts in development for SOLID, Tagged DIS
 - Active EIC Streaming Consortium
- INDRA Lab: Streaming Testbed operational
 - Streaming capable hardware, GEM detector, DAQ servers
- ERSAP system (Environment for Real-time Streaming And Processing)
 - Design Requirement Document in review
- Moving from Proof of Concept towards production design and implementation





Jefferson Lab Grand Challenge – To Be Continued

- Expanding production deployment of streaming readout, real time calibration, and management of data
 - -Keep strong track record of accomplishments
 - -Synergistic with ASCR funded activities
- Goal is full SRO, and use of AI/ML for real time calibration and expedient data/theory/simulation comparisons
- Production real time applications for 12 GeV program
 - Opportunistic detector testing in all four Halls: <u>https://www.jlab.org/eiccenter/detector-testing</u>
 - Augmenting the 12 GeV program and moving towards EIC detectors.
- Building digital twin capacity for EIC future
 - Project eAST: Turnkey Geant4 based simulation for EIC detector design with synergies with Medical Physics Initiatives





Streaming Readout – Modular Approach is Key

ERSAP

- Reactive, event-driven datastream processing framework that implements microservices architecture
- Provides basic stream handling services (stream aggregators, stream splitters, etc.)
- Adopts design choices and lessons learned from TRIDAS*, JANA, CODA and CLARA

ERSAP approach is a level of modularity that leads to an agile framework that can evolve rapidly over time

*TRIDAS = TRIggerless Data Acquisition System, Developed for KM_3NET

TriDAS ERSAP integration **ERSAP** TriDAS FT- ECal VXS Crate Slow Control API TSC Hall-D prototype calorimeter SRO ERSAP VXS Crate Calorimeter GEM prototype detector SRO (INDRA-ASTRA project) ERSAP timing and trigger distribution (TTS) **SMPSource** Physics & **SMPWr** SMPStre SampaSrvice Decoder Aggregator Receiver CRU C++/JANA amTet iter Java front-end links (GBT) CRU (common readout unit. Provided by the ALICE collaboration) 5 SAMPA based front end card (FEC) was fabricated at the

- JLAB for GEM (prototype) detector readout.
 - FEC has 5 SAMPA chips: 160 ADC channels per FEC



- Multiplexes data from front-end links
 - Control and configuration data for FECs
 - Xilinx Virtex-6 FPGA , can be used for data processing and formatting
 - 12 fast serial links



Streaming Readout – Modular Approach is Key

ERSAP also in use for DESY test run

DESY Test Beam

May 2-15, 2022

Douglas Hasell, May 13, 2022

Goals

- Streaming Readout Comparison
 - JLab FADC250 streaming and trigger mode operated stand-alone
 - CAEN Digitizer (streaming) and QDC (triggered) operated in parallel
- TPEX prototype calorimeter
 - 5x5 lead tungstate calorimeter (PbWO4 courtesy of Tanja Horn, CUA)
- EIC EMCal Lead tungstate and scintillating glass
 - five 2x2x20 cm³ "crystals" (courtesy of Tanja Horn CUA)



Early Impressions

- JLab FADC250 system worked very well in both triggered and streaming modes but does not play well with others
- CAEN QDC (triggered) and Digitizer (streaming) worked together in parallel very well
 - Digitizer saw more events



Streaming Readout X Workshop

AI/ML for Streaming Readout at the EIC



Event Reconstruction



Using Examples From Cristiano Fanelli's Presentation Critical Path for Compute-Detector Model for the EIC

Automated Alignment and Calibrations



Reconstruction of DIS Events



Slide courtesy Markus Diefenthaler (JLab)



At a recent meeting I was amused to hear nearly all experiments, including the large LHC ones, proudly mention they were including GPU servers...



Some Highlights of Heterogeneous Computing

First use of CPUs and GPUs together, inspired by Lattice QCD needs



Number 310 | April 26, 2010

Hot Graphics Cards Fuel Supercomputing



GPU cluster

The hottest video games on the market often have the most realistic graphics. And the key to such remarkable video is a device called a graphics processing unit, or GPU. Now, scientists at DOE's <u>Jefferson Lab</u> are using the power of GPUs to study some of the most fundamental problems in the universe.

"The reason graphics processors are so powerful is so that they make your game look realistic. They need to be able to compute and draw lots of things—at least thirty times a second," said Chip Watson, manager of Jefferson Lab's High-Performance Computing group in the IT Division.

This fast computation can also be applied to "drawing things" that are too small to see directly, such as the sub-atomic particles studied by nuclear physicists at Jefferson Lab. Interestingly, GPUs owe their ability to render realistic graphics to physics.



Some Highlights of Heterogeneous Computing – cont.



Jefferson Lab's K20 supercomputer was listed on the TOP500 list of fastest computers in June 2013. This single rack system houses two supercomputers: one built of NVIDIA K20 GPUs and one built with Intel Xeon Phis.

https://science.osti.gov/np/Highlights/2013/NP-2013-06-a

Supercomputing on a Budget

The optimization of commercial hardware and specialized software enables cost-effective supercomputing.



Figure 7.2: The strong reduction of time-to-solution as a result of software development is shown as the speedup gained as compared to the computing time with CPUs or GPUs only as the number of units increases. Shown are speedup factors only utilizing a solver library, and also including the QDP-JIT compilation framework on GPUs. [Image credit: B. Joo].

2015 NSAC Long-Range Plan, Figure courtesy Balint Joo (ORNL)



Heterogeneous Architectures – Advantage for Streaming/AI

Based on slide of Nathan Brei (JLab)

- Heterogeneous hardware:
 - Commercial off-the-shelf
 - Can be used for general computing workloads but not a full CPU
 - Excluding: fast electronics, SAMPA, FELIX
 - In practice: GPUs, FPGAs, coprocessors (e.g. Xeon Phis), (TPUs)
- Themes:
 - -AI/ML
 - Streaming readout
 - High-bandwidth real-time data processing

These technologies reinforce and enable each other.

Imagine a full end-to-end real-time, streaming system. What would it look like?



The Network Bandwidth will grow!



Eli Dart (leader of Science Engagement group of ESnet) e-mail:

"As far as network backbone capacity in the 2030s goes, it's too early to say. ESnet's optical system is highly capable (we just deployed it as Jason said), but we don't yet know what networking technologies will be available in 2032 - ten years is a long time. That said, we can make some guesses. It is likely that 1.6Tbps Ethernet will be available by then."

Jason Zurawski (Esnet, in group of above) e-mail:

"I think BNL plans to support multipole 400G connections, JLab is limited due to a lack of fiber recourses in the area, but will at a minimum upgrade some of the 10G connections that exist today to 100G soon (and move to 400G sometime in the future I would imagine)."

93 1998 2003 2008 2013 2018	Annualized Growth Rate	Growth Over 10 Years	
Internet bandwidth	50%	57×	
Computer power	60%	100×	



EIC Computing Butterfly Model



Proof of Concept of EIC Dual Echelon 1 data center approach during EIC detector proposal phase in 2021

Figure 4: Butterfly model of federated offsite computing. In this model, nearly all storage is contained in echelon 1 while large portions of the raw data processing is delegated to multiple HTC/HPC facilities. The named facilities in this graphic are merely examples and do not represent commitments or final plans.



Benefits of Heterogeneous Hardware to Nuclear Physics

• GFlop/Watt is significantly lower for GPUs than CPUs

O e.g. <u>https://www.karlrupp.net/2013/06/cpu-gpu-and-mic-hardware-characteristics-over-time/</u>

• Price per flop is lower for GPUs than CPUs

o *n.b.* can be hard though to keep GPU fully busy

- Large HPC/HTC systems have significant compute capability tied up in heterogeneous hardware (including cloud services)
- Higher memory bandwidth (good for streaming)
 - See LHCb Allen project: <u>https://arxiv.org/abs/1912.09161</u>
- Well-suited for AI/ML models
 - o not all models are efficient on GPUs, but some (e.g. CNNs) are extremely efficient
 - tools like HLS4ML making FPGAs more accessible (<u>https://fastmachinelearning.org/hls4ml/</u>)
- Faster simulation via GANs

Slide courtesy David Lawrence (JLab)



Benefits of Heterogeneous Hardware to GEANT4 Simulations



Slide courtesy Nathan Brei and Markus Diefenthaler (JLab)



Summary – EIC in the Streaming/AI Era

- Community efforts towards streaming at the EIC started in earnest in ~2018
 - Now, a mere four years later, streaming readout is the default for the envisioned EIC detector
 - The advances in microelectronics and commercial data handling hardware are our friends ©
 - Many efforts are ongoing withing the EIC community
 - And we are only busy ~4 years with a decade to go before EIC detector operations start
- Community efforts towards AI at the EIC started in earnest in ~2020
 - The AI4NP workshop and white paper, AI4NP winter school (369 registered participants), AI4EIC workshop series (1st workshop with 243 registered participants) all were a huge success
 - Al is our friend and a perfect fit for the nuclear science we do
 - Al is being integrated in <u>all</u> aspects of the EIC detector (design, calibration, simulation, reconstruction, analysis)
 - Here also amazing momentum has been gathered
- To take full benefit of streaming and AI implies use of heterogeneous computing
 - Al requires to integrate the power of GPUs in our workflow
 - Our colleagues in Lattice QCD have illustrated the power of combining CPUs, GPUs and modern software
 - The increase in network bandwidth is our friend ☺ just imagine 1.6 Tbps by then...
 - Similar, the developments in statistical methods and data science are our friends ☺



Combining Streaming, AI, heterogeneous computing and modern software in our physics detector, data handling and analysis (from calibration to high-level physics analysis) is **a no-brainer**.

We "just" have to make it work.

Heterogenous architectures, AI/ML, and the other technologies are rapidly evolving... On the timescale of EIC data taking the landscape is likely to be completely different. **We must ensure an agile framework that can evolve rapidly over time!**



Backup

With GPUs computer and data sciences are taking two complementary approaches:

- Data science, via AI/ML, uses training data to create a model of a complex algorithm so that results are inferred from the input data without executing the algorithm. This allows use of GPUs without porting the code of the algorithm itself.
- Computer science is taking the approach of extending computing languages so that the same code can run on CPUs or GPUs without modification. For example, Intel's OneAPI:

https://www.intel.com/content/www/us/en/developer/tools/oneapi/overview.html

