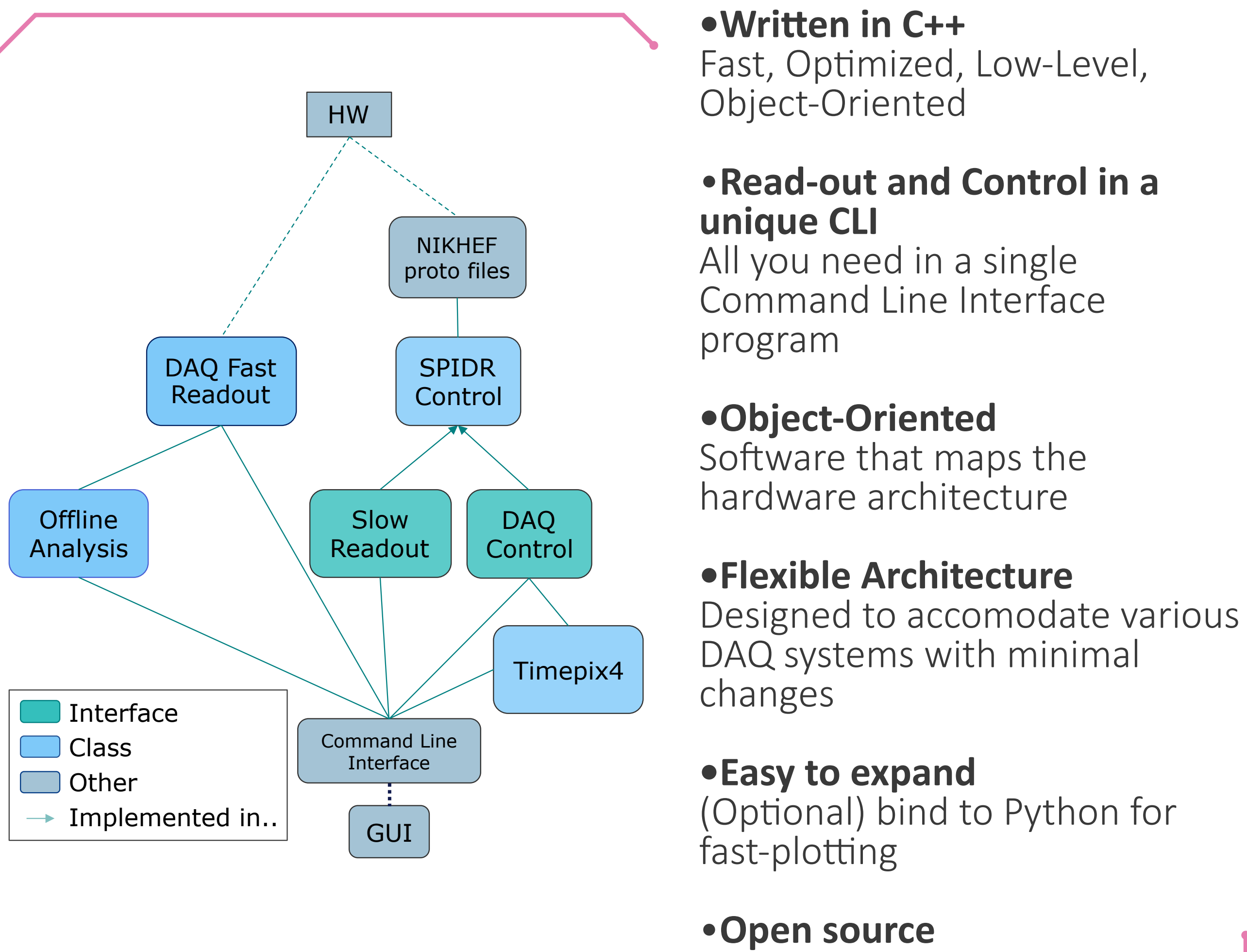


A fast and optimized data-acquisition and control software for the Timepix4 ASIC

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Software structure

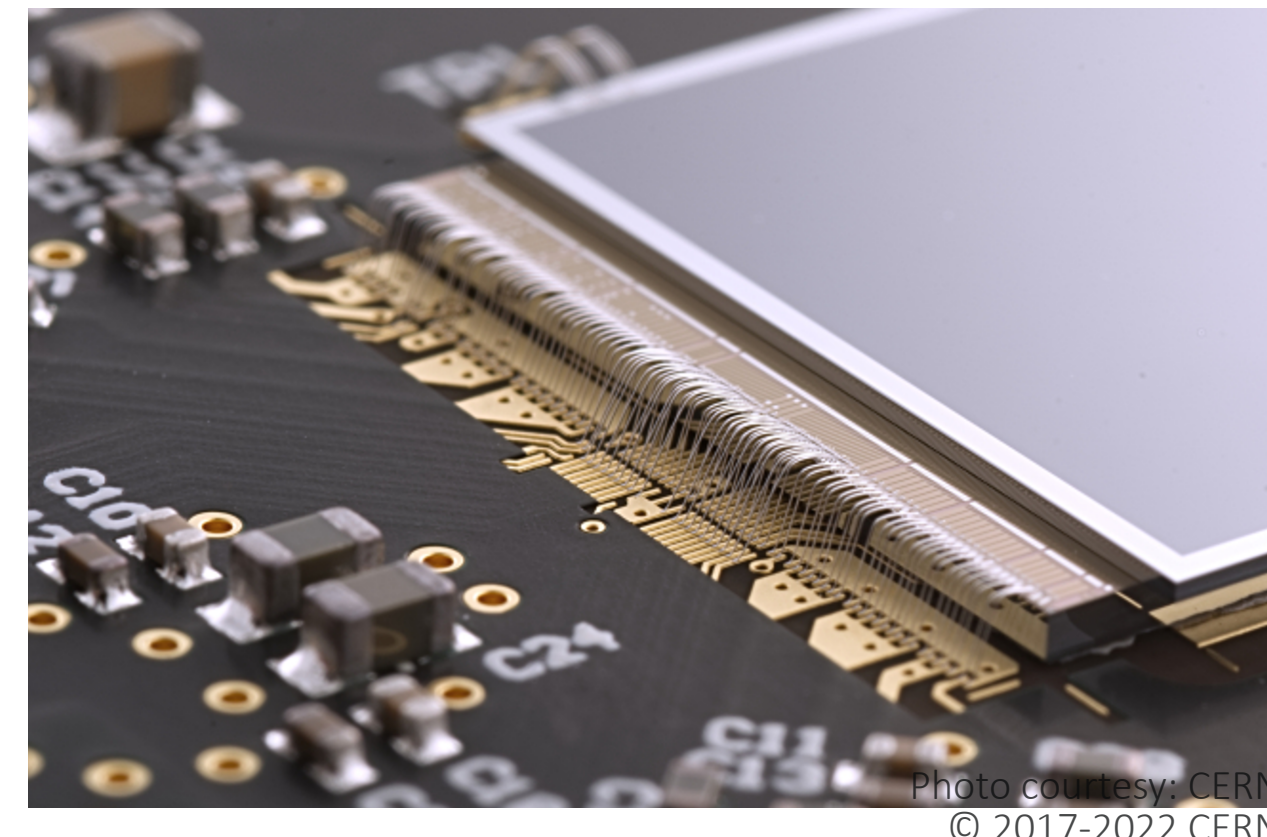


Timepix4

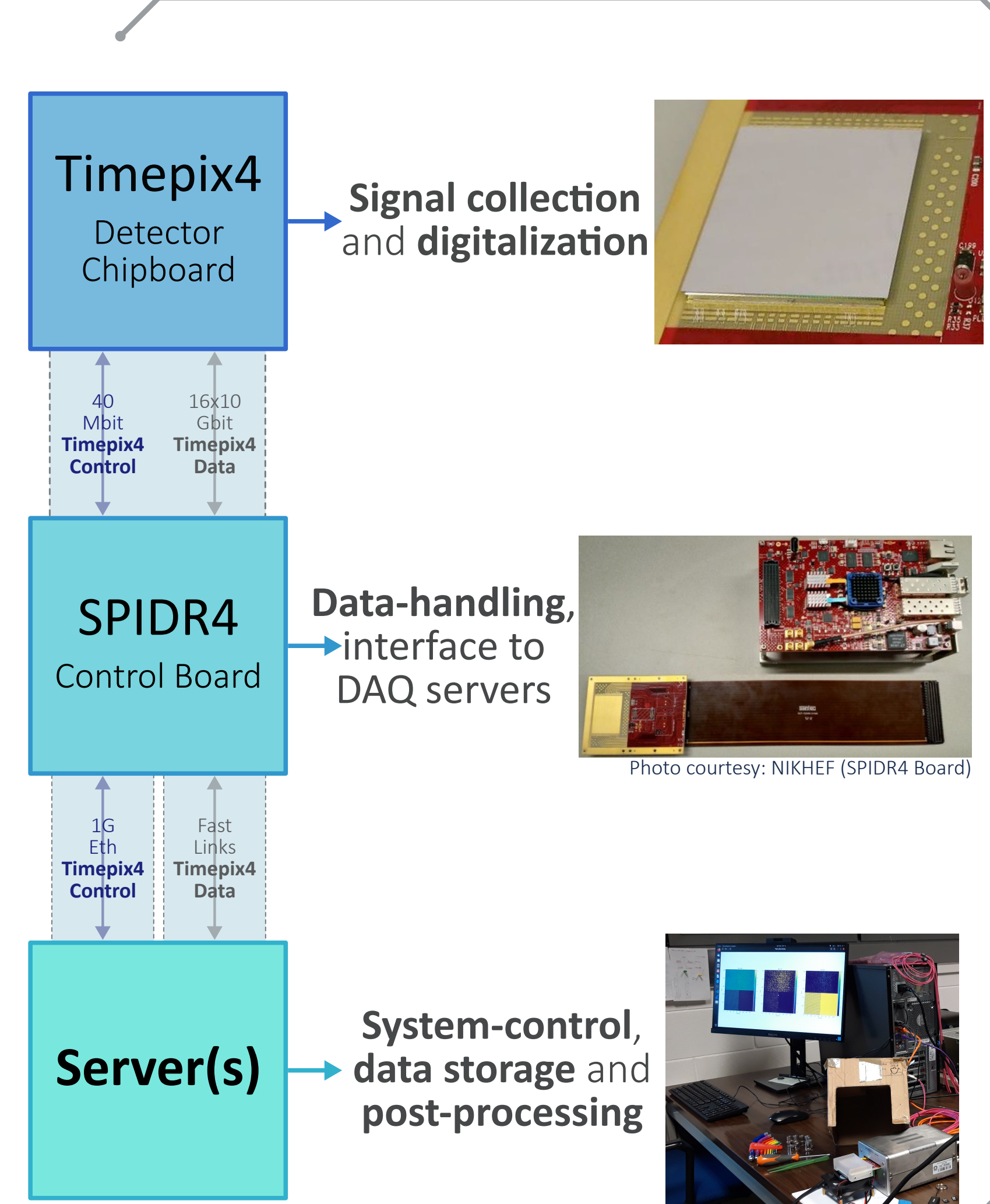
Timepix4 is a **hybrid pixel detector readout ASIC** developed by the **Medipix4 Collaboration**.

It consists of a matrix of about **230k pixels** with **55 μm** pitch, equipped with amplifier, programmable threshold discriminator and time-to-digital converter with **195 ps bin size** that allows to **measure time-of-arrival and time-over-threshold**.

It has two different types of links: a slow control link, for the configuration and the slow readout (up to 1 Gbps), and fast links, 16 serial links with a speed of up to 10 Gbps each[1].



Data-Acquisition System



Configuration

Fast configuration

- **Configure Timepix4 with a simple file**
 Fill the `.conf` file customizing the configuration parameters, or leave it with default values. Call the dedicated API to automatically configure Timepix4 according to the file.
- **Full configuration with less than 40 values**
 Fast configuration uses 37 values to fully configure Timepix4. If not specified in the file, default values are used, allowing users to create shorter files.

Manual configuration

- **Configure Timepix4 using lower-level APIs**
 More than 70 different APIs can be used to manually configure every aspect of Timepix4. You can also create your personalized APIs using the read and write functions for Timepix4 registers.
- **Use both the configuration modes**
 No need to choose between fast or manual configuration, both can be used in the same script.

Abstract

We present a **new software** for the management of Timepix4 ASIC, based on its data acquisition architecture, entirely **open-source**, **extremely flexible** thanks to the **C++** low-level classes and user-friendly. The software is organized into a structure of classes that allows to **configure both the Control Board and the Timepix4** and to handle the **slow and fast readout**. Moreover, storage, post-acquisition analysis and every other custom class can be easily added without modifying the basic organization of the software. This software is **designed to work with every hardware setup** that make use of Timepix4. Changing only a few low-level functions, the ones strictly correlated to the communication protocol, all the higher-level classes will work correctly.

Data Readout

Slow readout

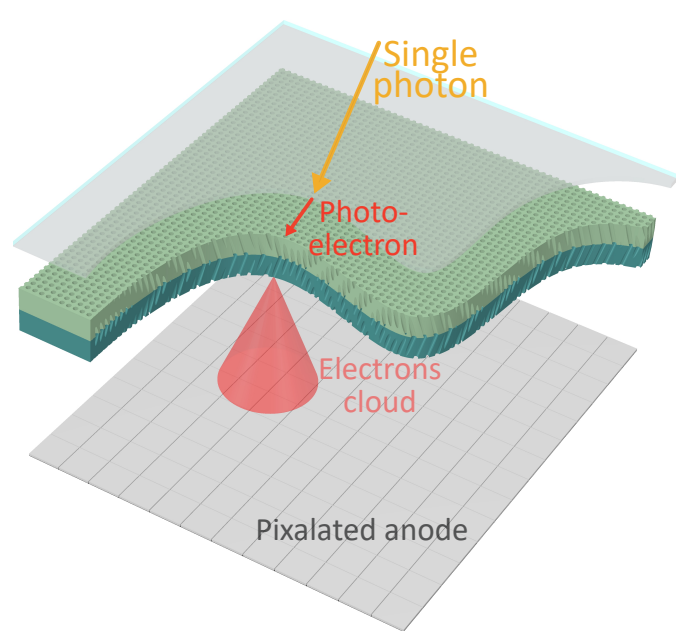
- **Up to 1 Gbit/s**
 Read-out using slow-control link.
- **Continuous polling**
 Acquired data are stored in a dedicated Timepix4 register that is periodically read by the slow read-out thread(s).
- **Dedicated thread**
 Asynchronous thread running in background to get Timepix4 data and write it on file.

Fast readout

- **Up to 160 Gbit/s**
 Read-out using 16 optic links with a maximum bandwidth of 10 Gbit/s each.
- **UDP packets**
 Packet are sent directly to the server via UDP protocol, no need for polling.
- **Dedicated threads**
 Two asynchronous thread, using a reader-writer paradigm, running in background to catch Timepix4 data and write it on file.
- **Independent from Control Board**
 Threads don't require reading or writing registers, so they can be used with every Control Board.

Possible future applications

4DPHOTON

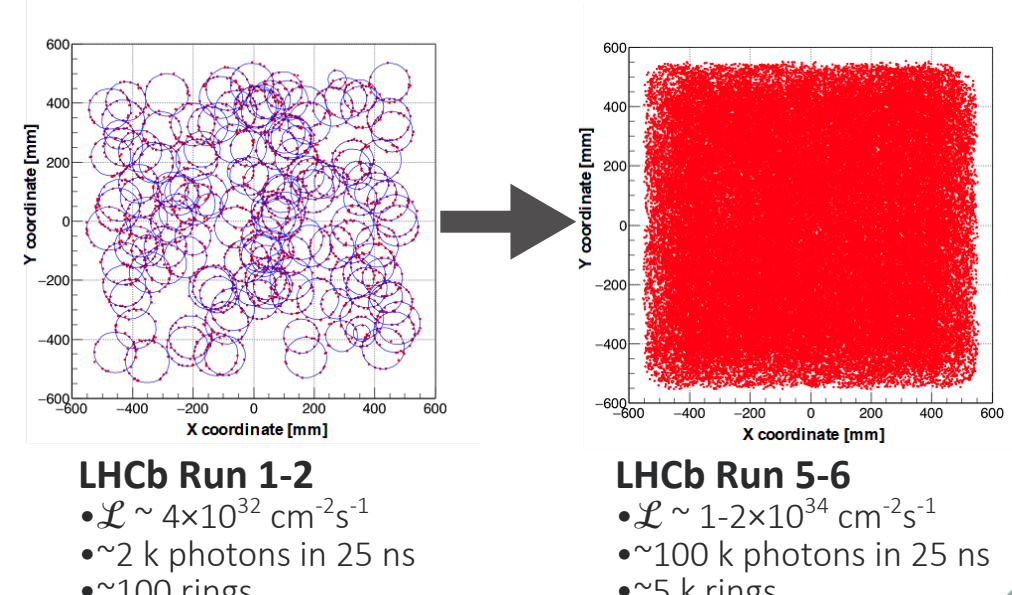


Development of a new photodetector with large active area able to measure single photons with simultaneous excellent timing and spatial resolution, with a low noise level at room temperature[2][3].

LHCb RICH detector Upgrade II

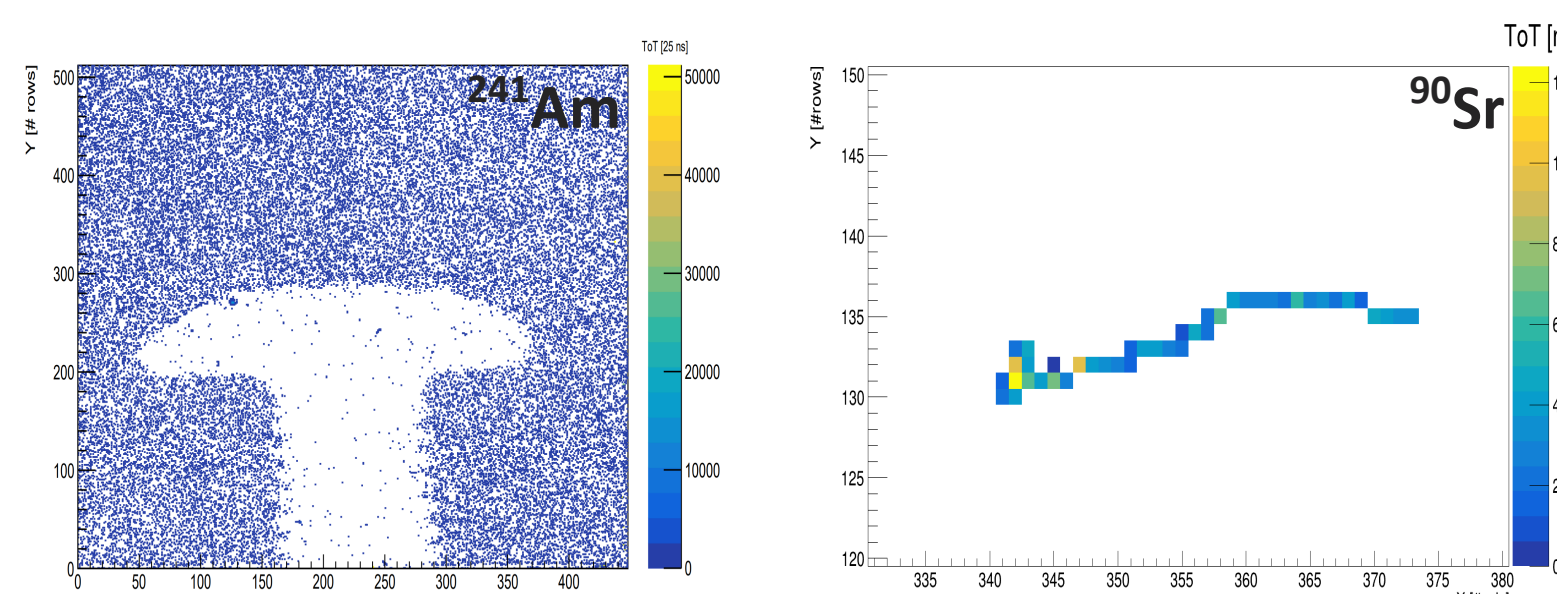
Need for a new photodetector with fine granularity (<1 mm) and time resolution <100 ps (the smaller the better)

LHCb - RICH

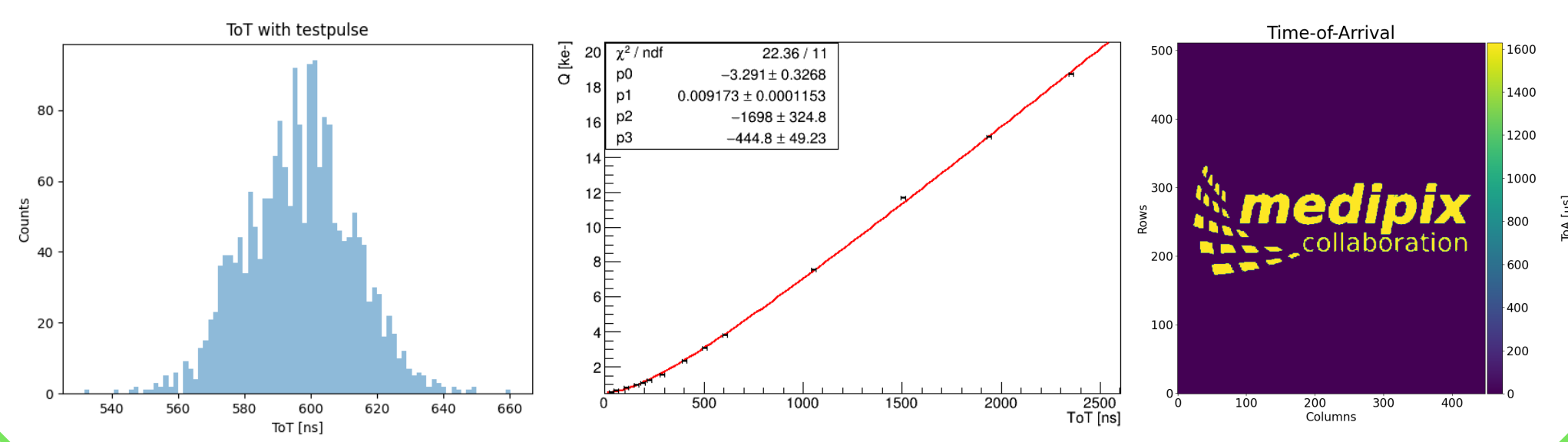


Results

Acquisition with radioactive sources Data-Driven Mode



Calibration with testpulse Data-Driven Mode



[1] X.Llopert et al. "Timepix4, a large area pixel detector readout chip which can be tiled on 4 sides providing sub-200 ps timestamp binning" JINST 17 (2022) no. 01, C01044
 [2] M.Fiorini et al. "Single-photon imaging detector with O(10) ps timing and sub-10 μm position resolutions" JINST 13 (2018) no.12, C12005
 [3] J.A.Álozy et al. "Development of a single-photon imaging detector with pixelated anode and integrated digital read-out" JINST 17 (2022) no.06, C06007

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