

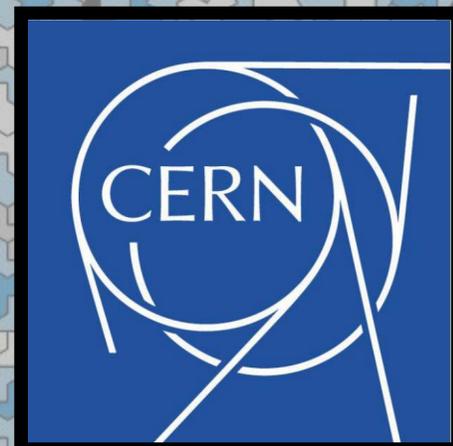
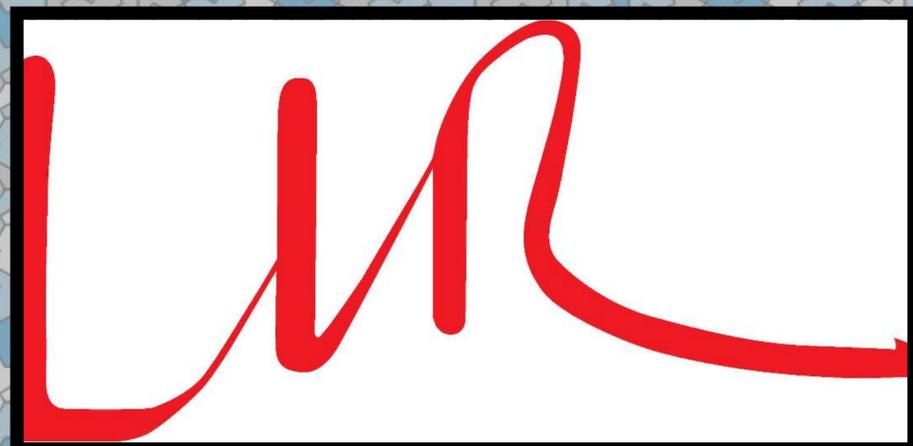
Cluster reconstruction in the HGCAL @ Level 1 trigger

Bruno Alves, on behalf of the CMS Collaboration

Contact: bruno.alves@cern.ch

CHEP2023

8th May 2023

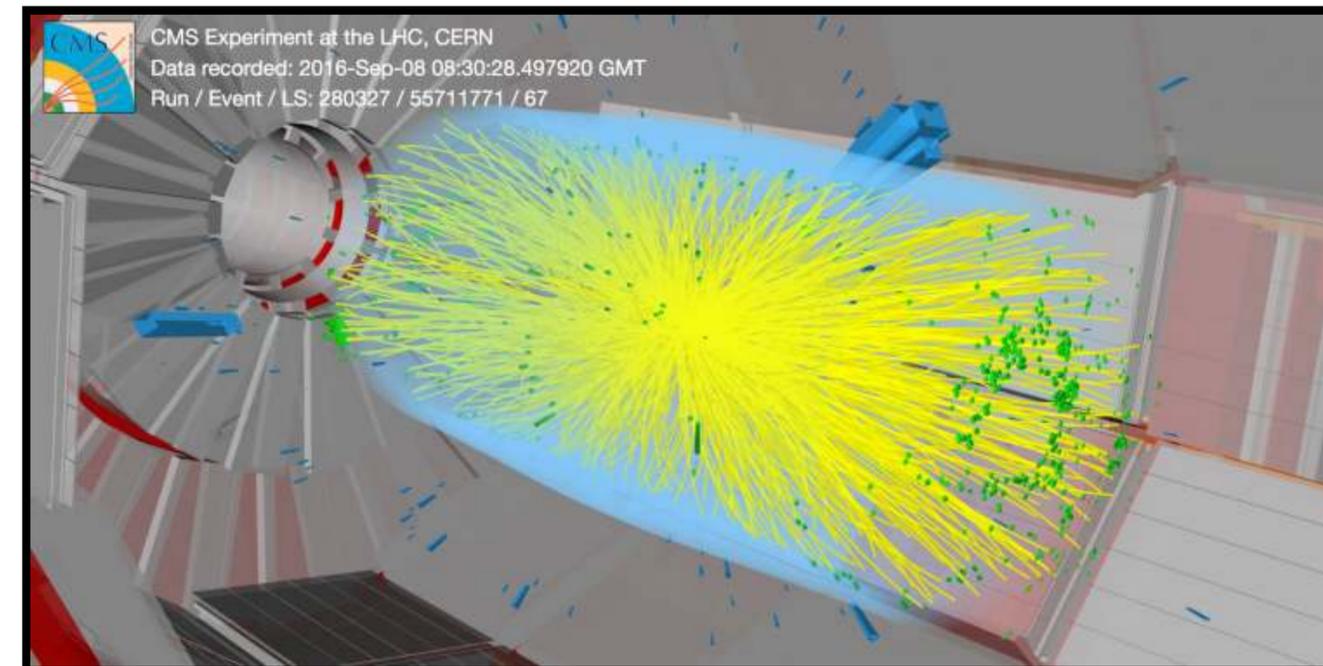


Indico

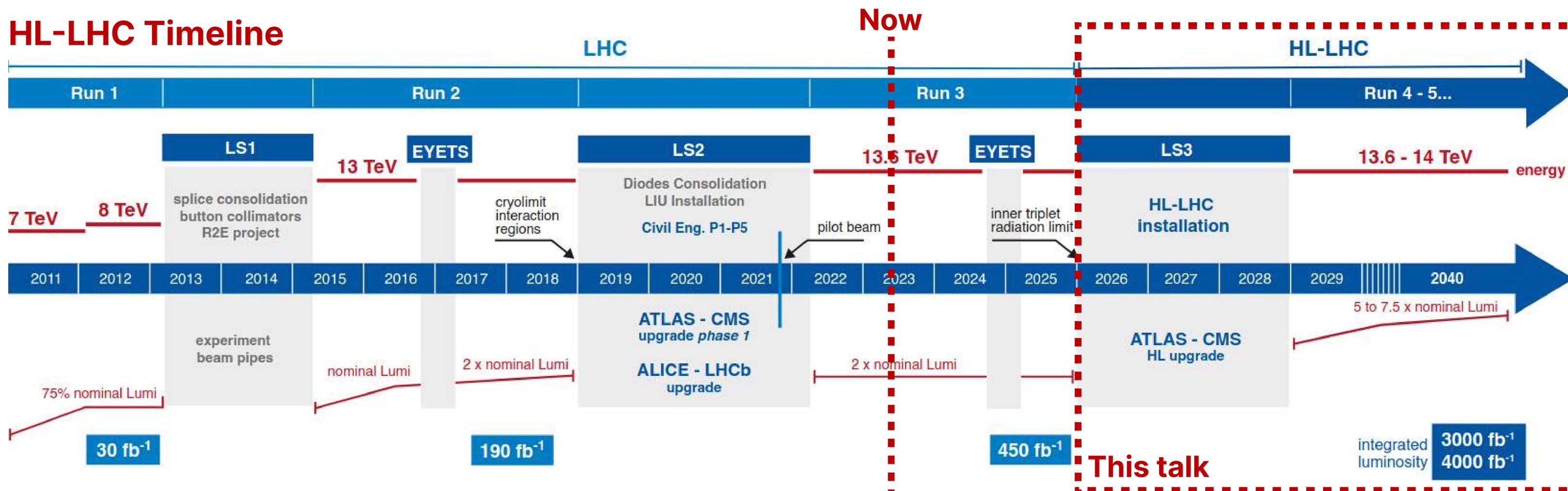


The High Luminosity Challenges

- ~4x more luminosity than the LHC (up to $\sim 7.5 \times 10^{34}$ cm²/s)
- Up to 200 collisions per bunch crossing (1.6 vertices/mm)
- Harsh radiation environment (up to 10¹⁶ neq/cm²)
- Enables full program of Precision and BSM Physics
 - Requires significant detectors upgrades
 - Challenging to maintain current trigger thresholds

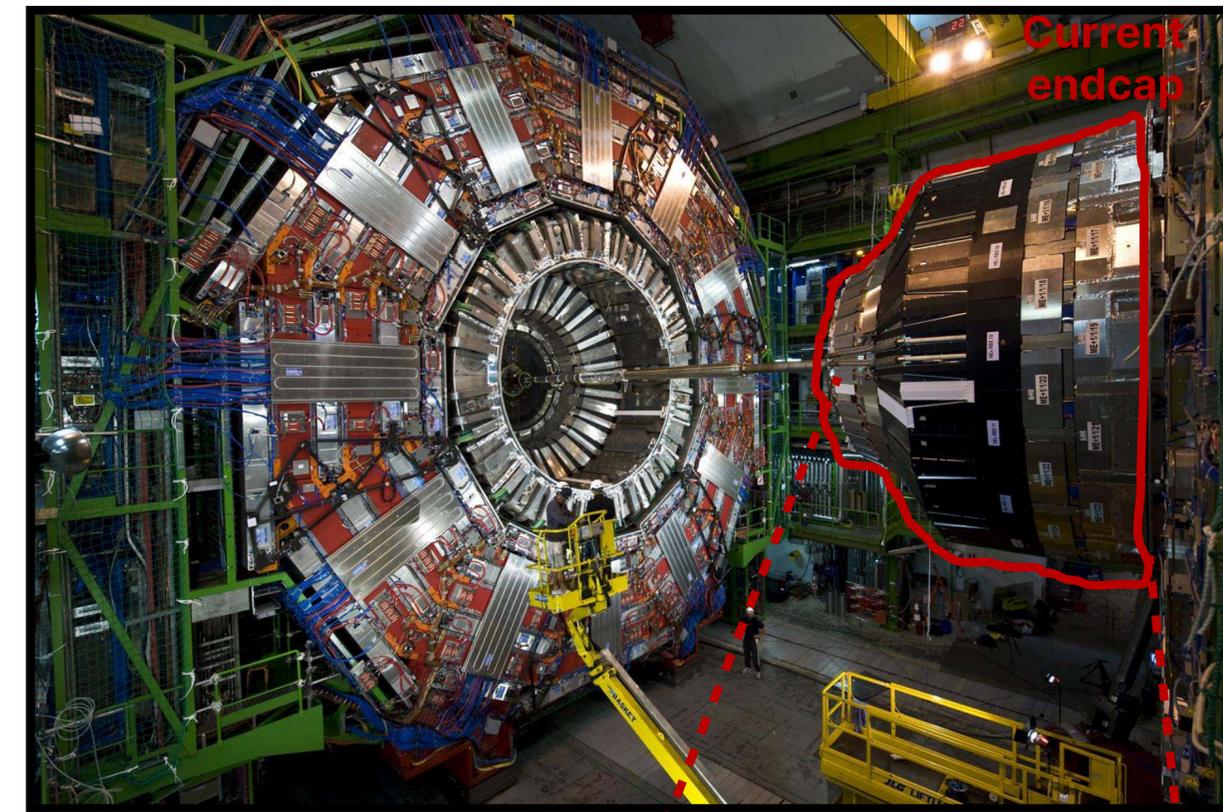


HL-LHC Timeline

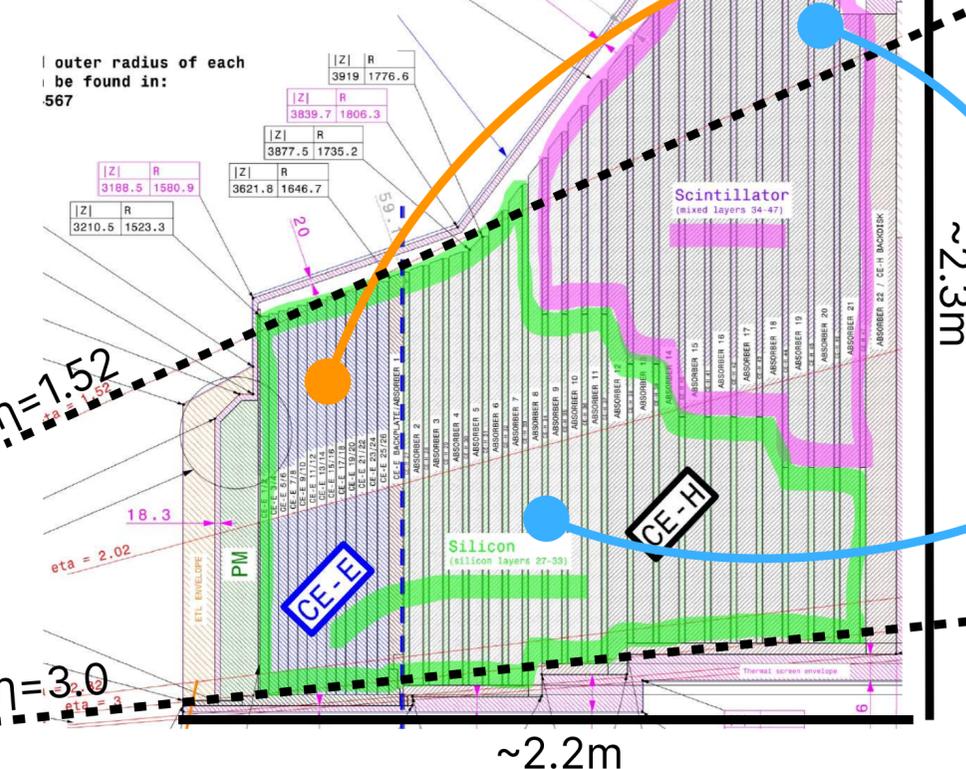


High Granularity Calorimeter (HGCAL)

- Position, energy, timing (down to $\sim 30\text{ps}$): **5D imaging!**
 - 620 m² silicon, 26K modules, 6M channels
 - 370 m² scintillator, 3.7K boards, 240K channels
- Higher granularity boosts particle flow reconstruction
 - tracking with the calorimeter!
- Processes **300Tb per second**

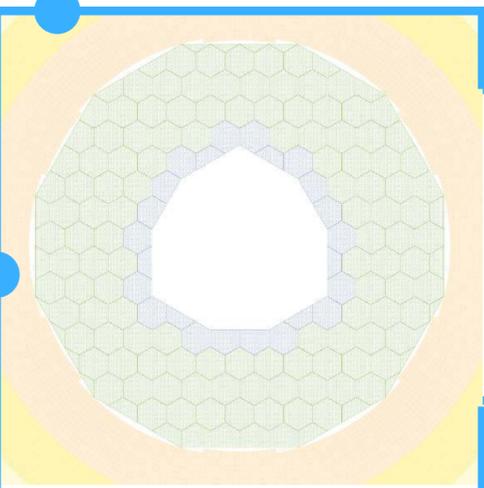
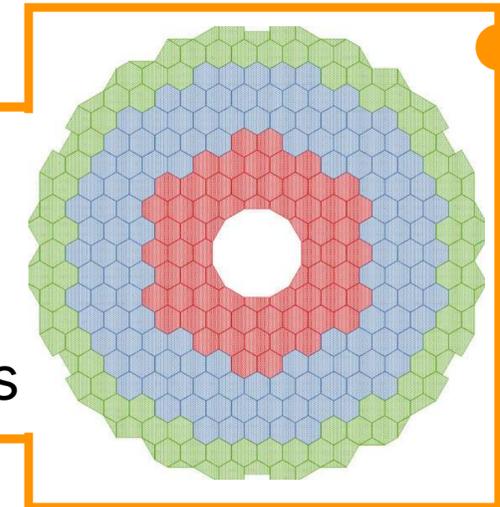


~ 215 tonnes x 2



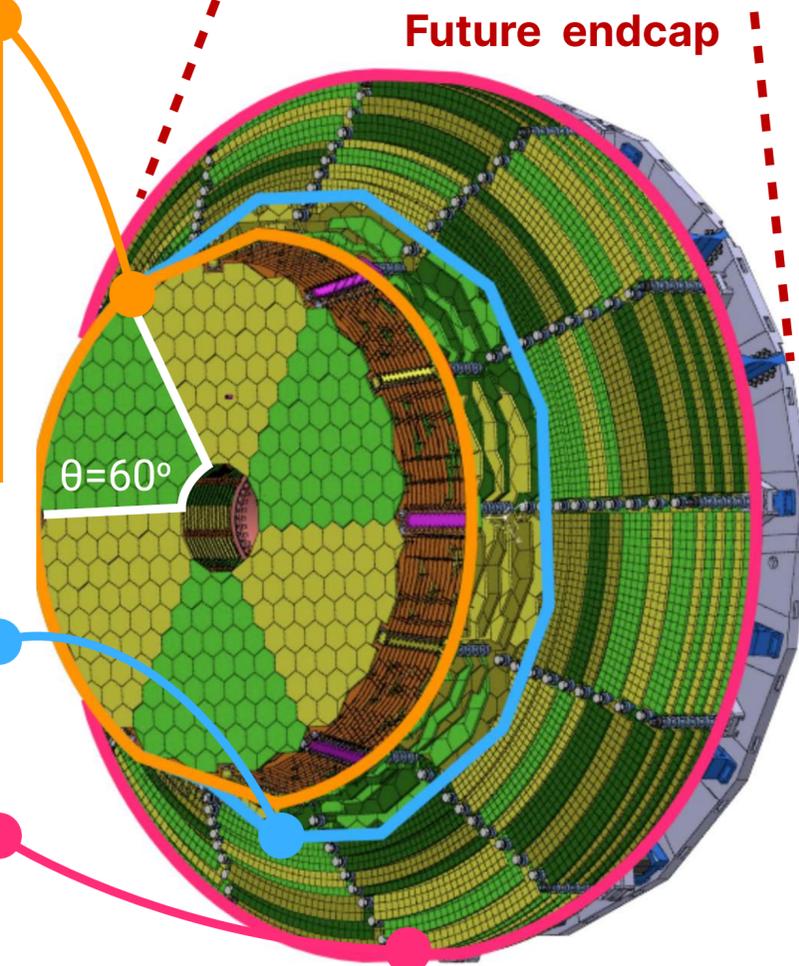
EM silicon section

- 26 layers w/ $27.7X_0$, $\sim 1.5\lambda$
- Absorber: CuW, Pb
- Sensors in hexagonal modules



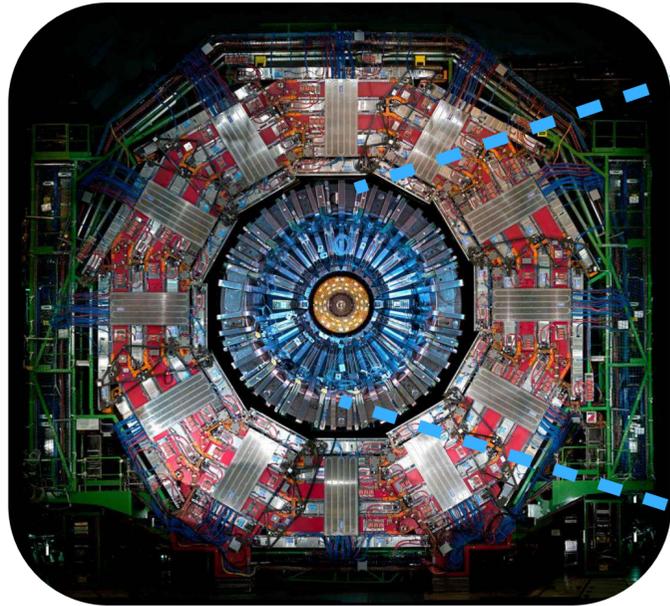
Hadronic Si + Sci section

- 21 layers w/ $\sim 8.5\lambda$
- Absorber: Cu, stainless steel
- On-tile SiPM

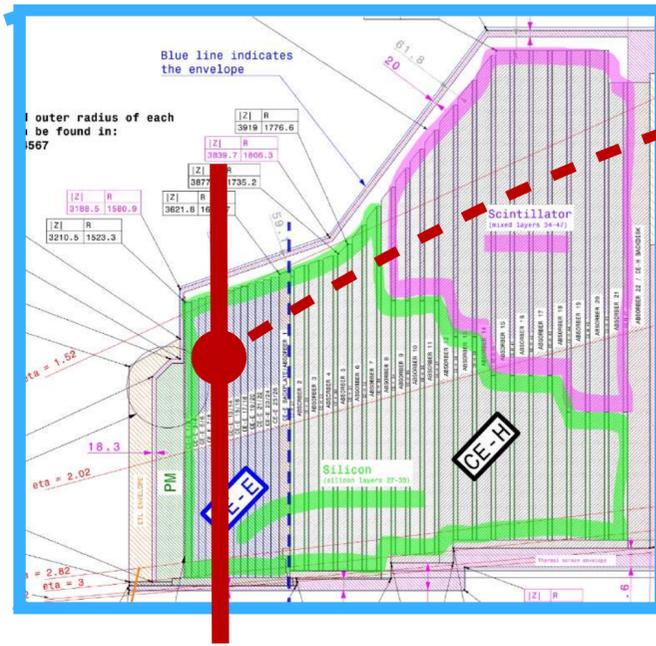


Trigger Primitives' Data Flow

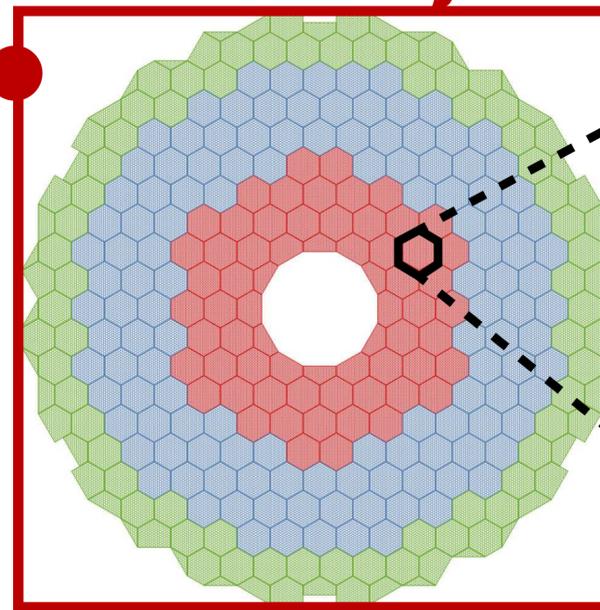
CMS



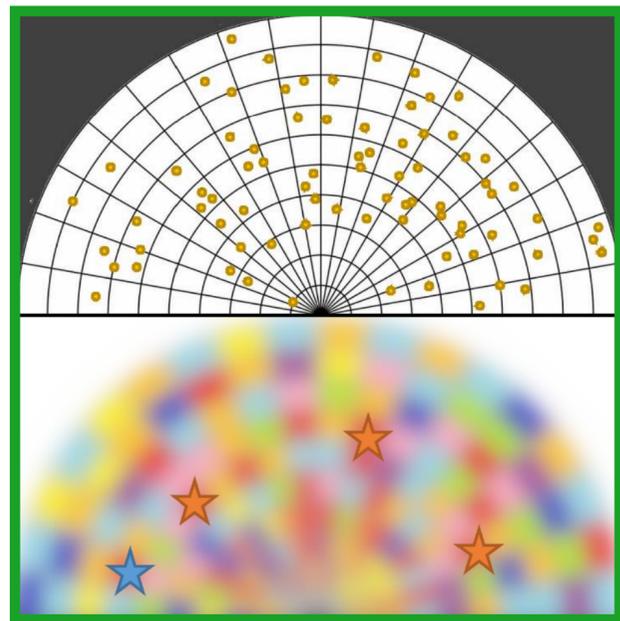
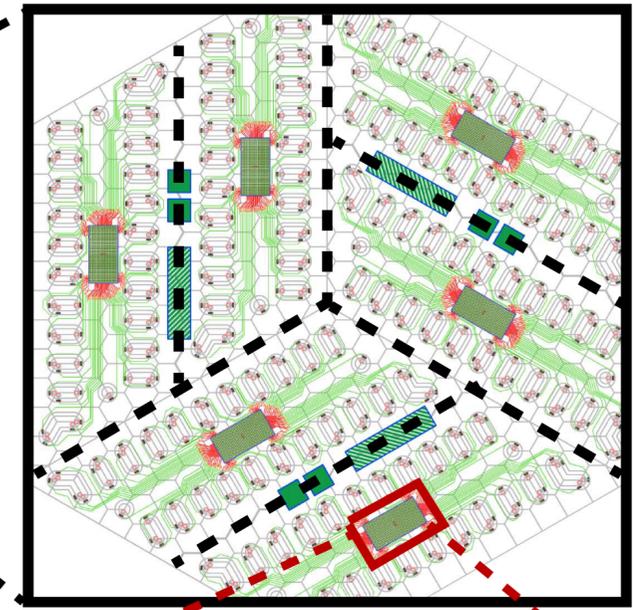
HGCAL



Silicon Layer



Hexaboard



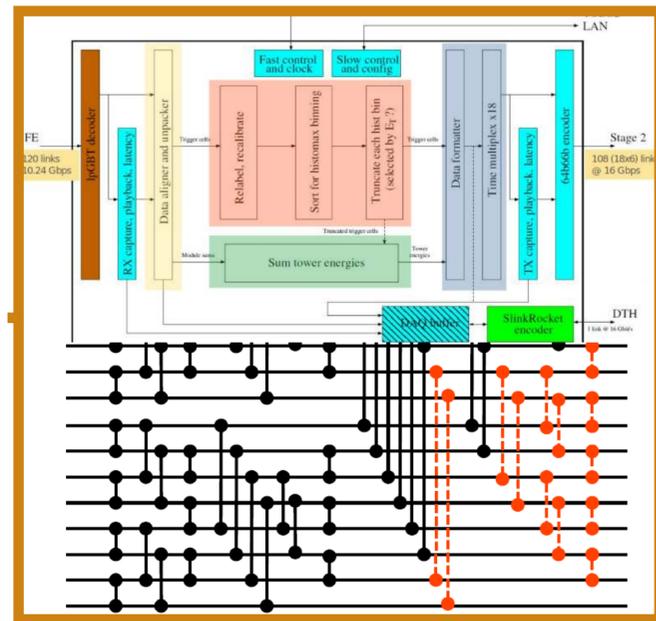
Backend Stage 2

140 Tb/s

16Gb/s links

see S. Sioni's talk!

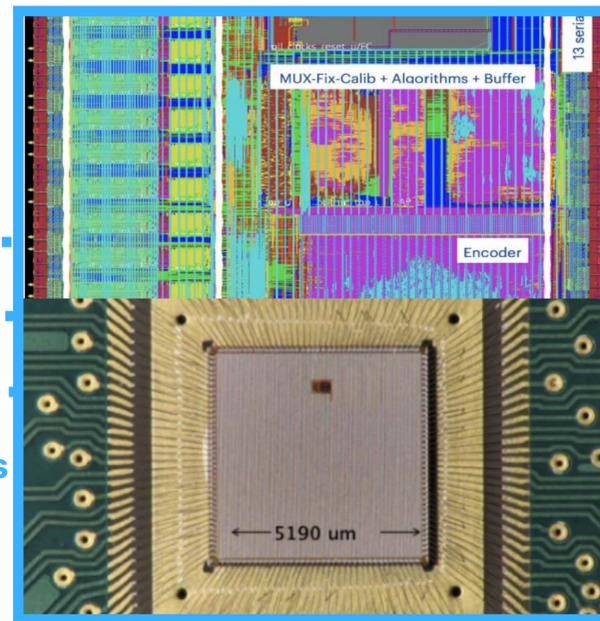
Level 1 Trigger



Backend Stage 1

90 Tb/s

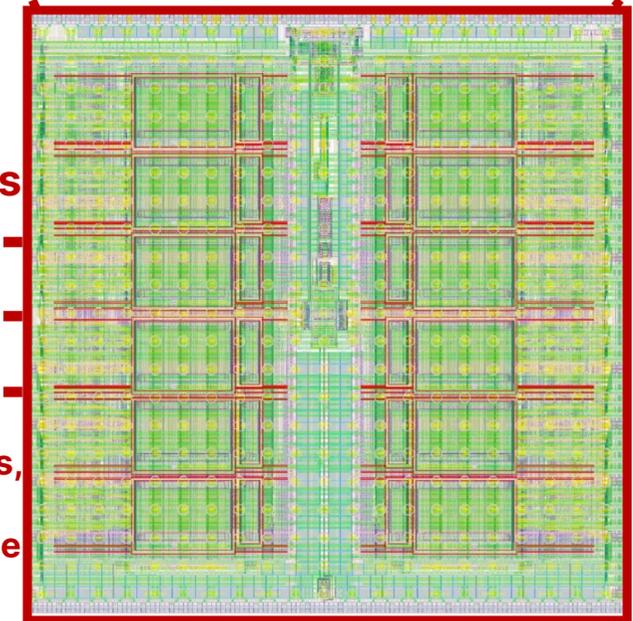
10Gb/s links



ECON-T

300 Tb/s

1Gb/s links,
15Gb/s per module



HGCRROC

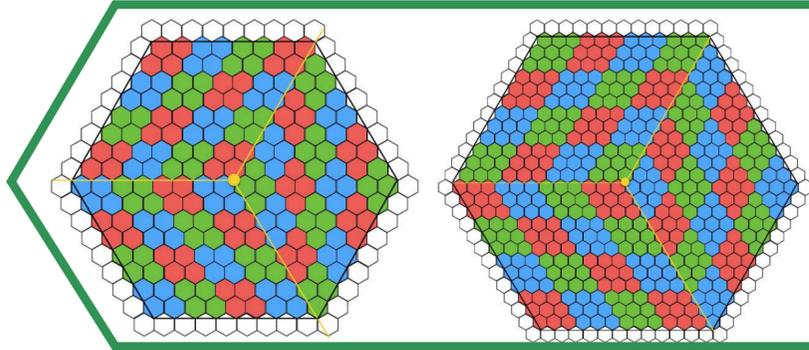
... readout ...

HLT

Physics Analysis

HGCROC

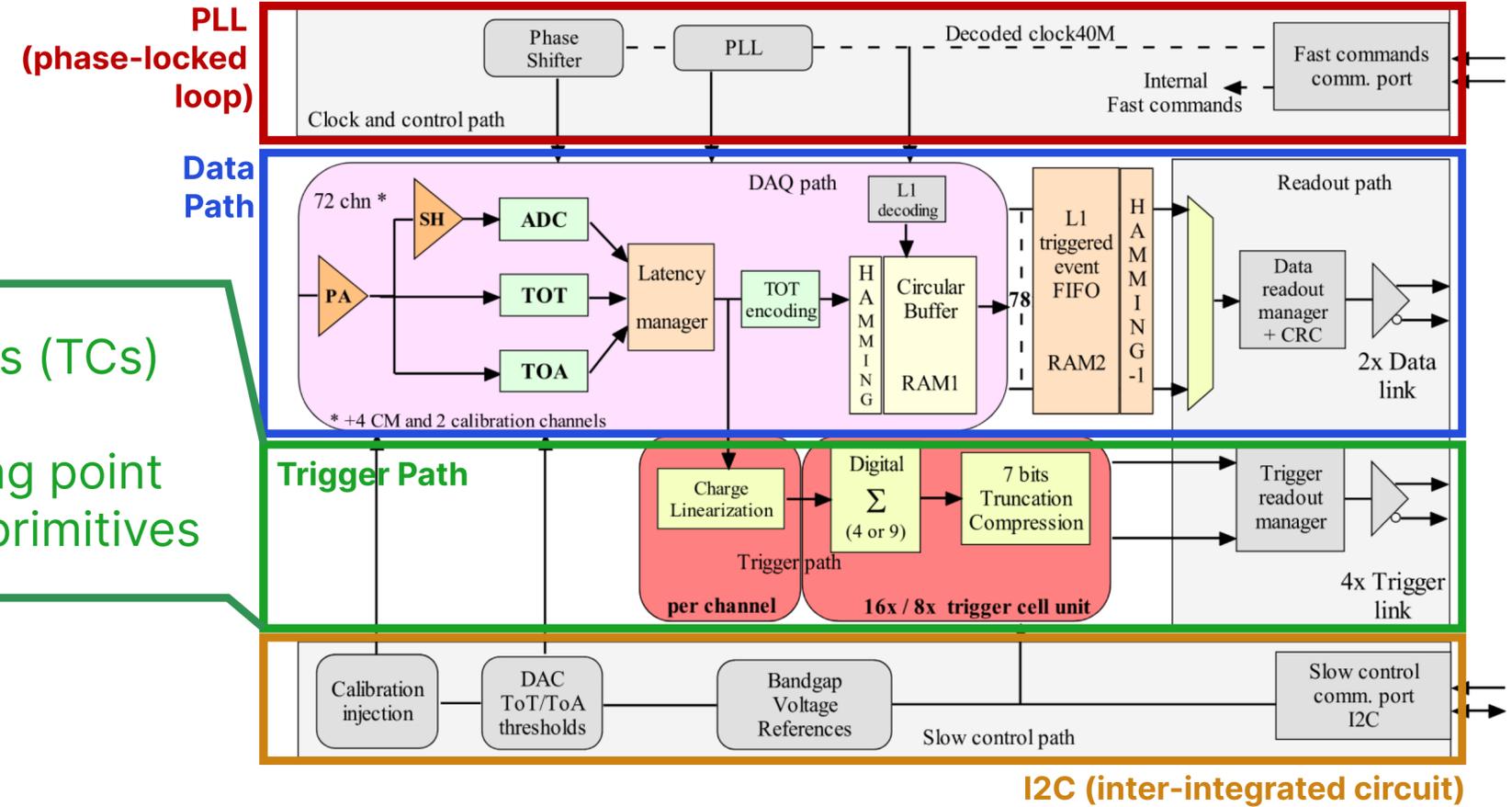
- HGCROC: Front-end read-out ASIC @ 40MHz, outputs 1.28 Gb/s w/ 12.5 μ s latency!



Sum channels into Trigger Cells (TCs)

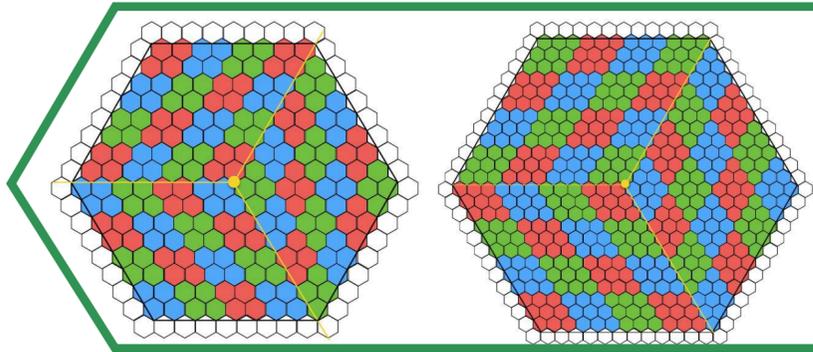
- charge/energy linearization
- compression to 7-bit floating point
- starting point of L1 trigger primitives

HGCROC



HGCROC + ECON-T

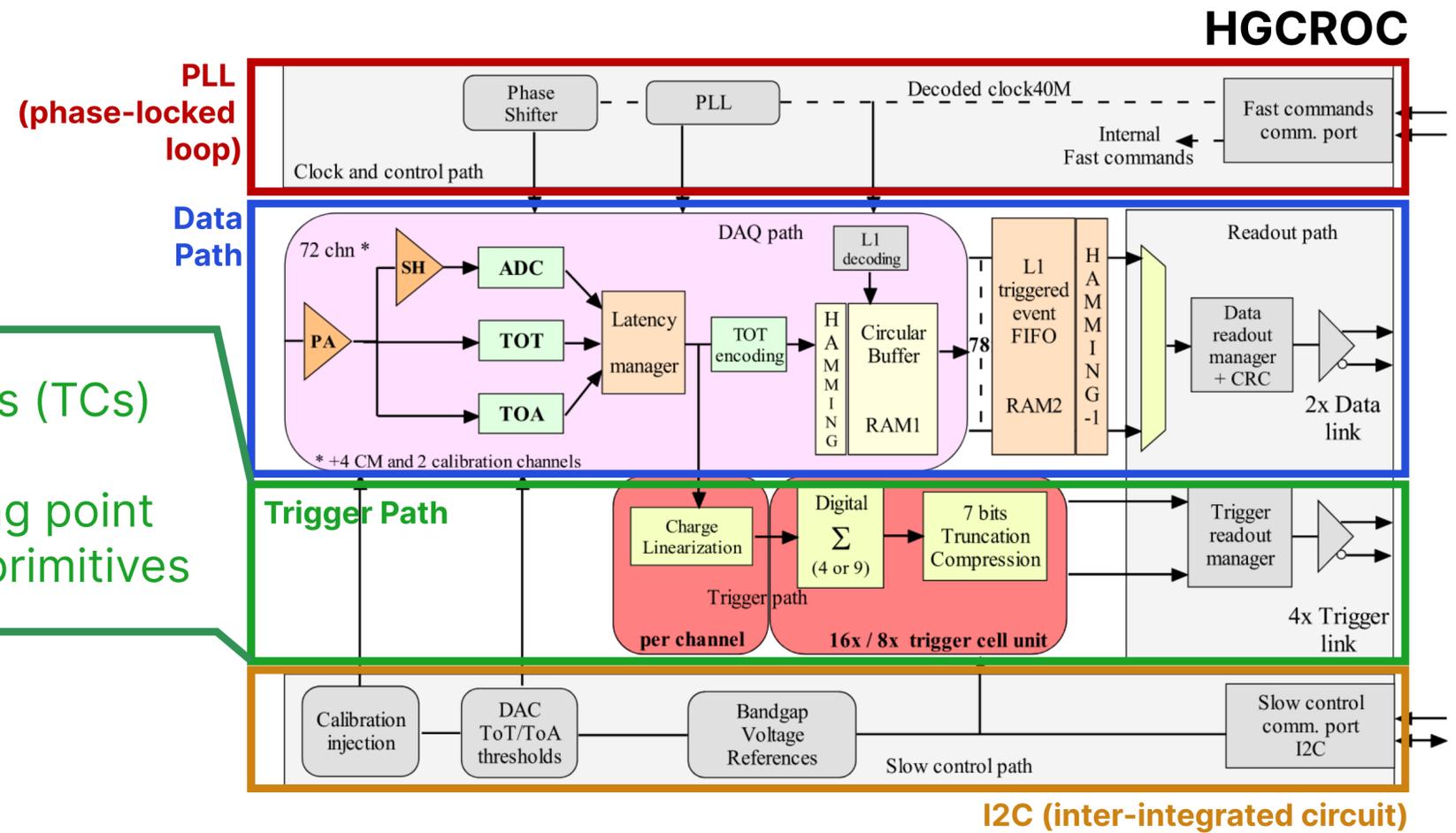
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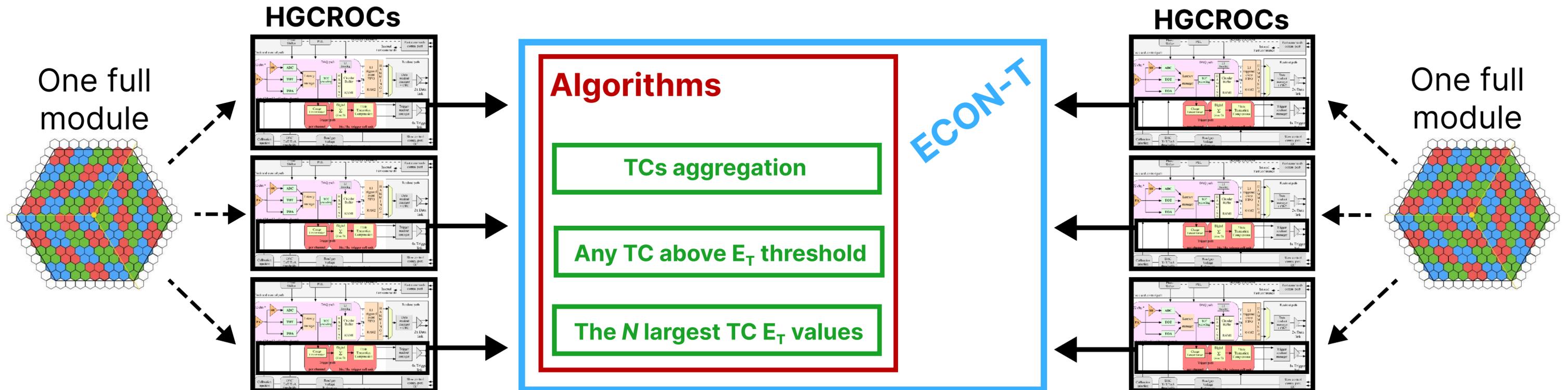
Sum channels into Trigger Cells (TCs)

- charge/energy linearization
- compression to 7-bit floating point
- starting point of L1 trigger primitives

- ECON-T: concentrates HGCROC data w/ dedicated selection and aggregation algorithms
 - Timing cannot be used due to bandwidth constraints



I2C (inter-integrated circuit)



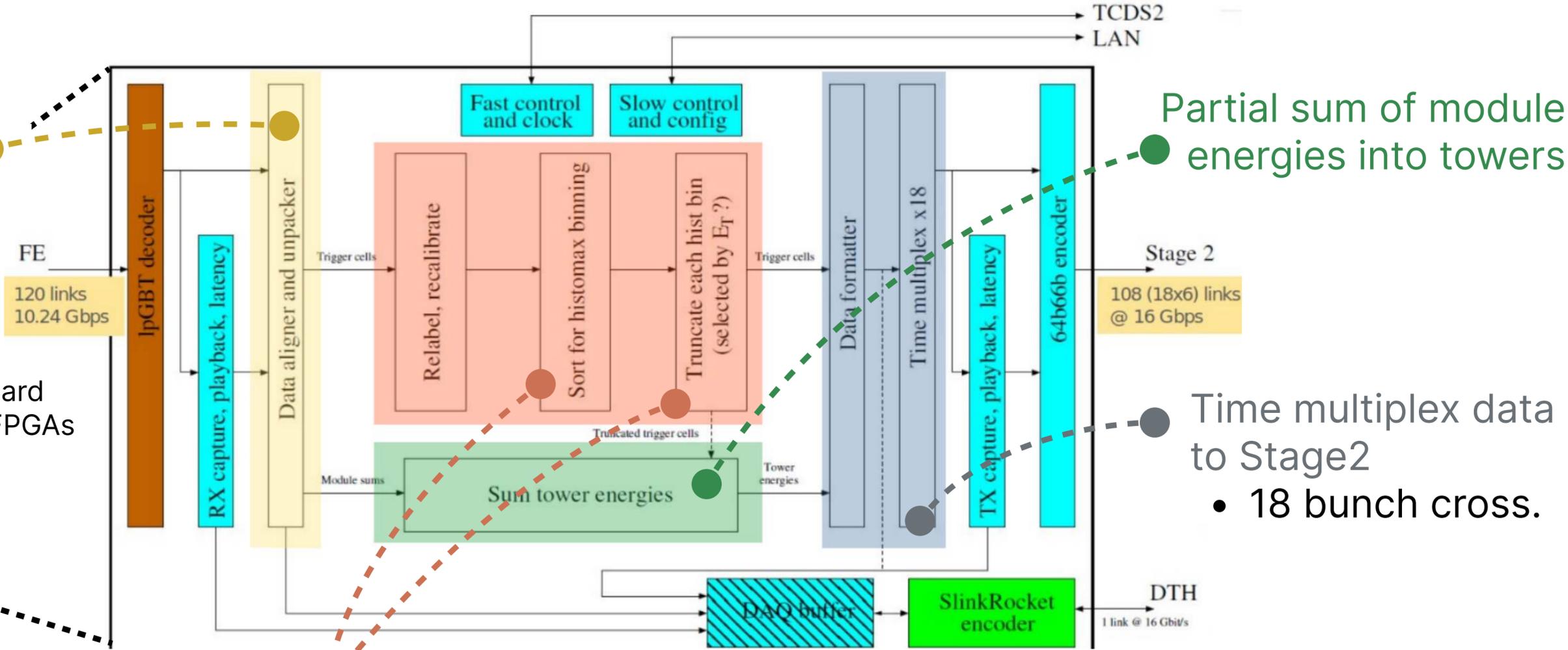
Backend Stage 1

Unpacks data from ECON-T

- Inputs grouped per bunch crossing



Serenity board w/ VU13P FPGAs (128 links)

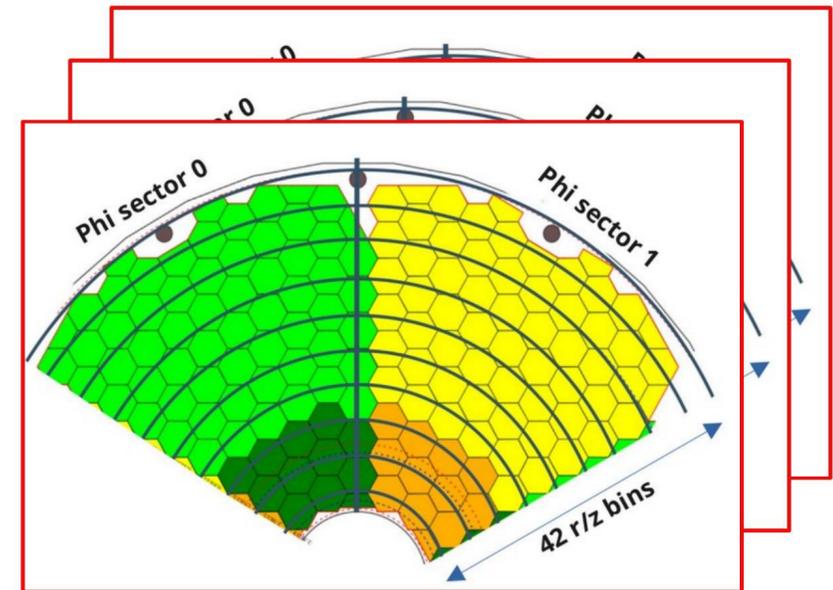
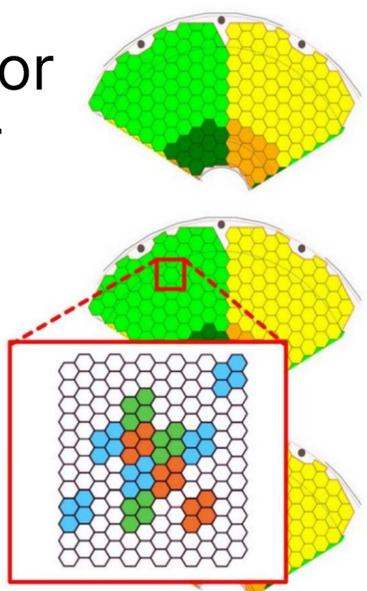


Partial sum of module energies into towers

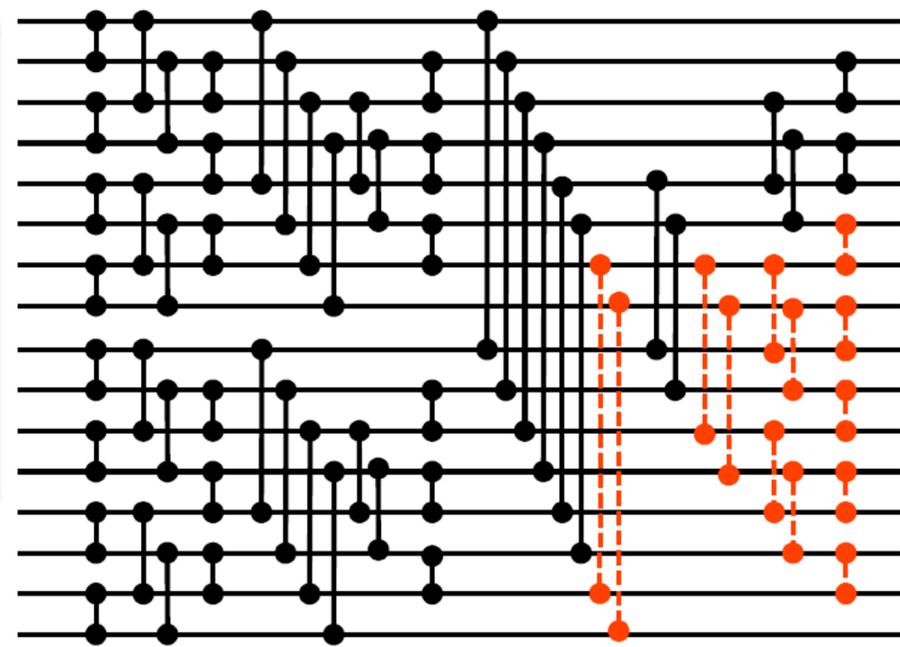
Time multiplex data to Stage2
 • 18 bunch cross.

Sort and truncate TCs in firmware

- TCs routed to bins
- $2(\phi) \times 42(R/z)$ bins per 120° sector
- Energy sorting with the batcher odd-even sorting network
- On-the-fly truncation, fixed output bin size



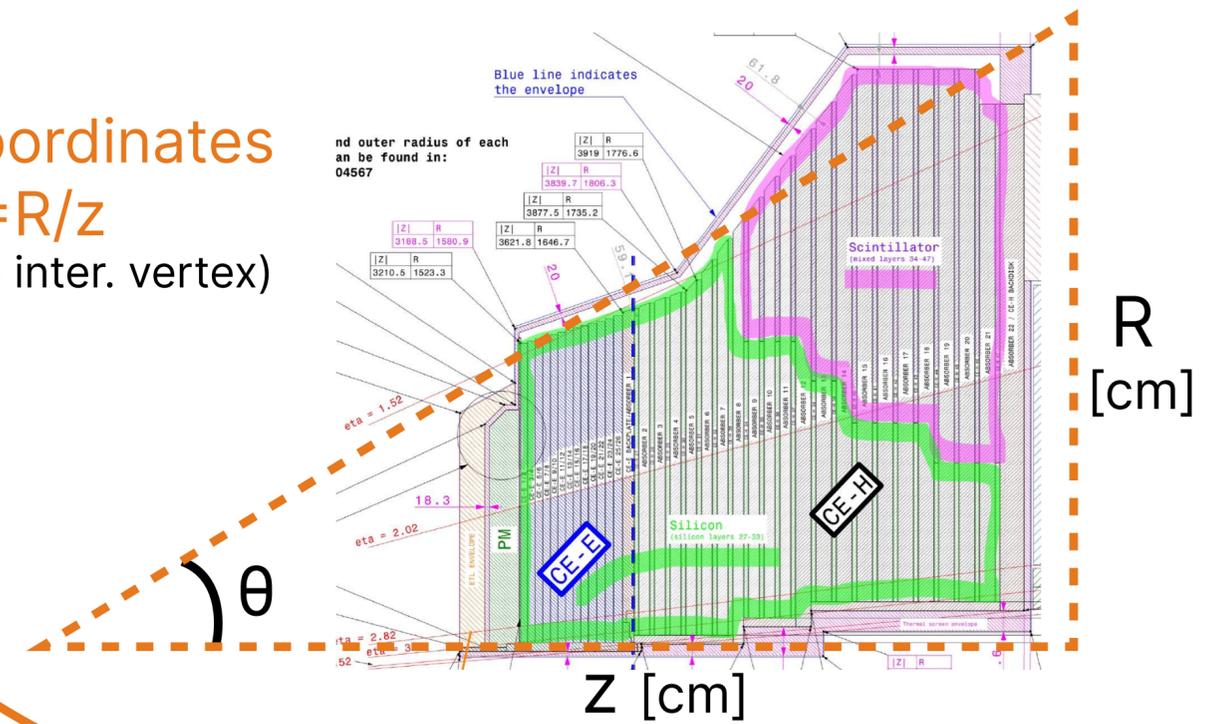
sort each bin independently



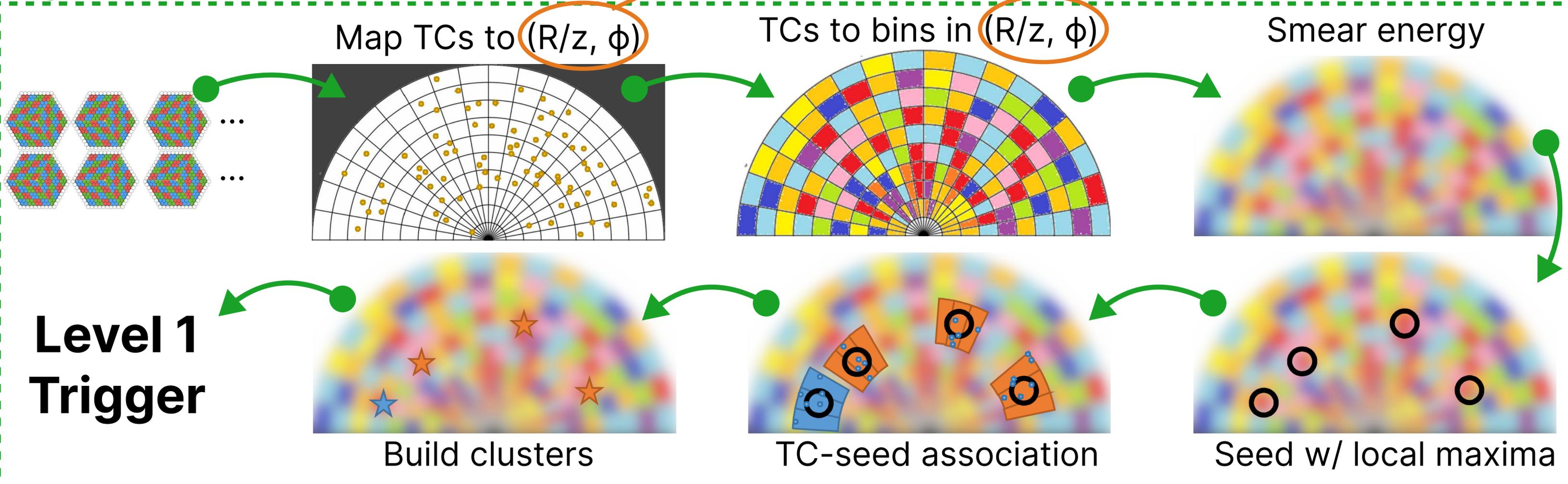
Backend Stage 2

- Finds and builds Trigger Towers + 3D clusters of TCs (positions, energies, shapes)
- Sends trigger primitives to L1 trigger

projective coordinates
 $\tan(\theta) = R/z$
 (relative to the pp inter. vertex)

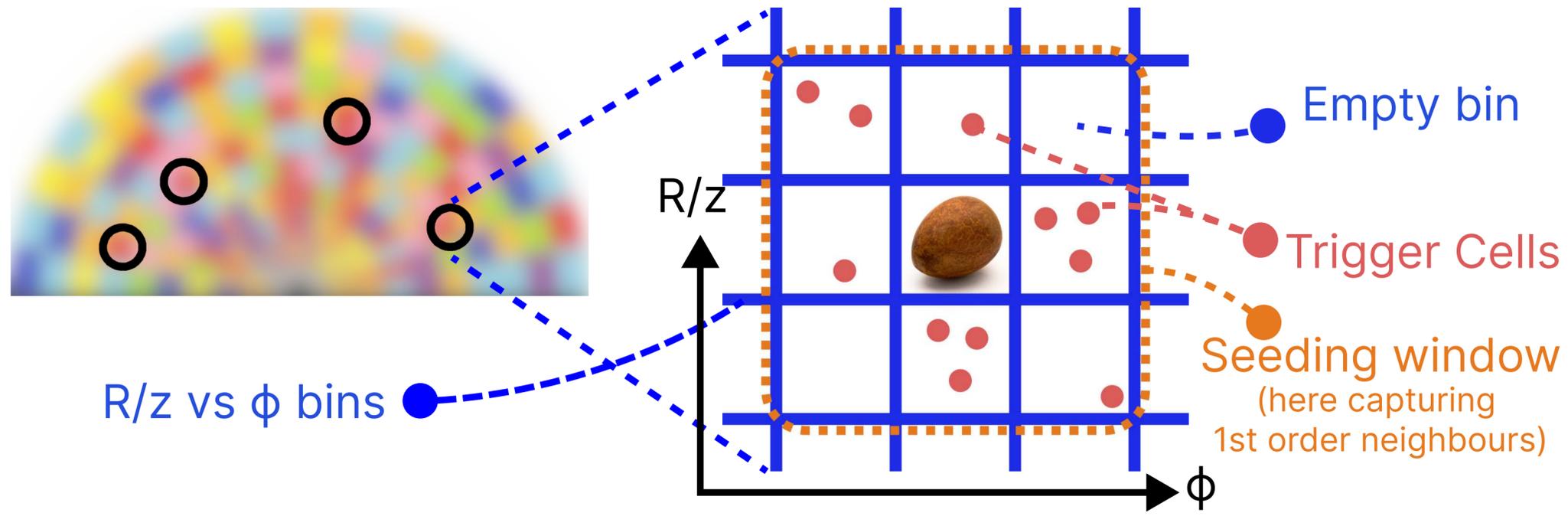


Stage2 firmware reconstruction chain



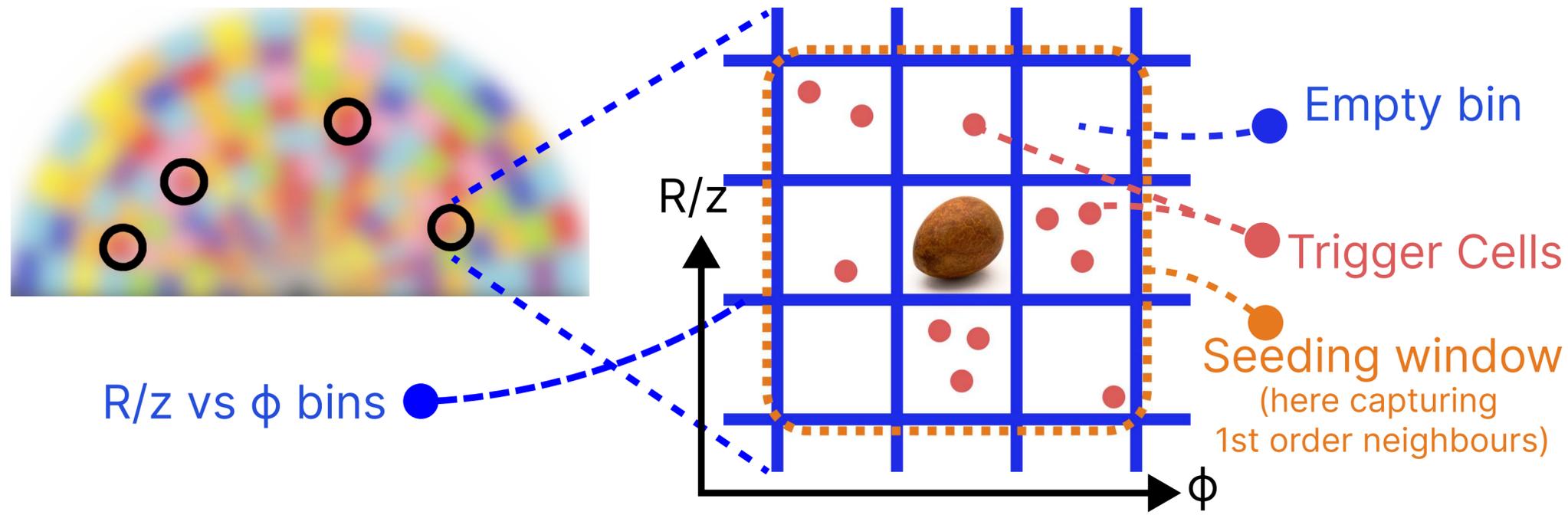
Seeding...

- Finds local maxima: seeds
- Seeds must also pass a p_T threshold selection
- Every seed creates a cluster



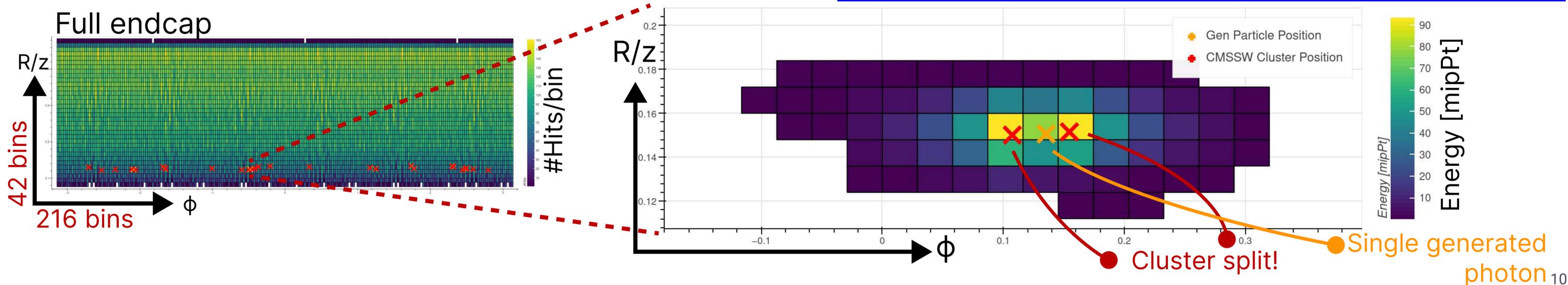
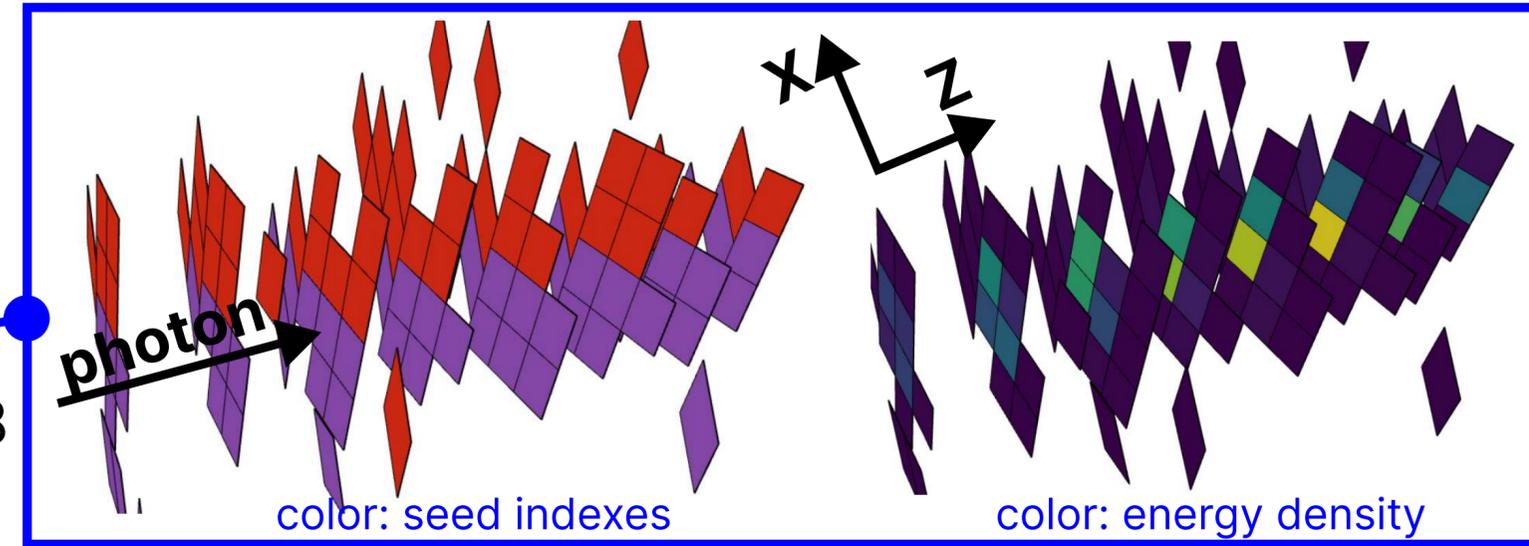
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- Every seed creates a cluster



... but splitting

- Non-homogeneous distribution of TCs into bins
 - some bins have few cells, esp. close to the beam pipe
 - unphysical **cluster splits!**
- Study w/ single OPU unconverted photon gun; $1.7 < |\eta| < 2.8$

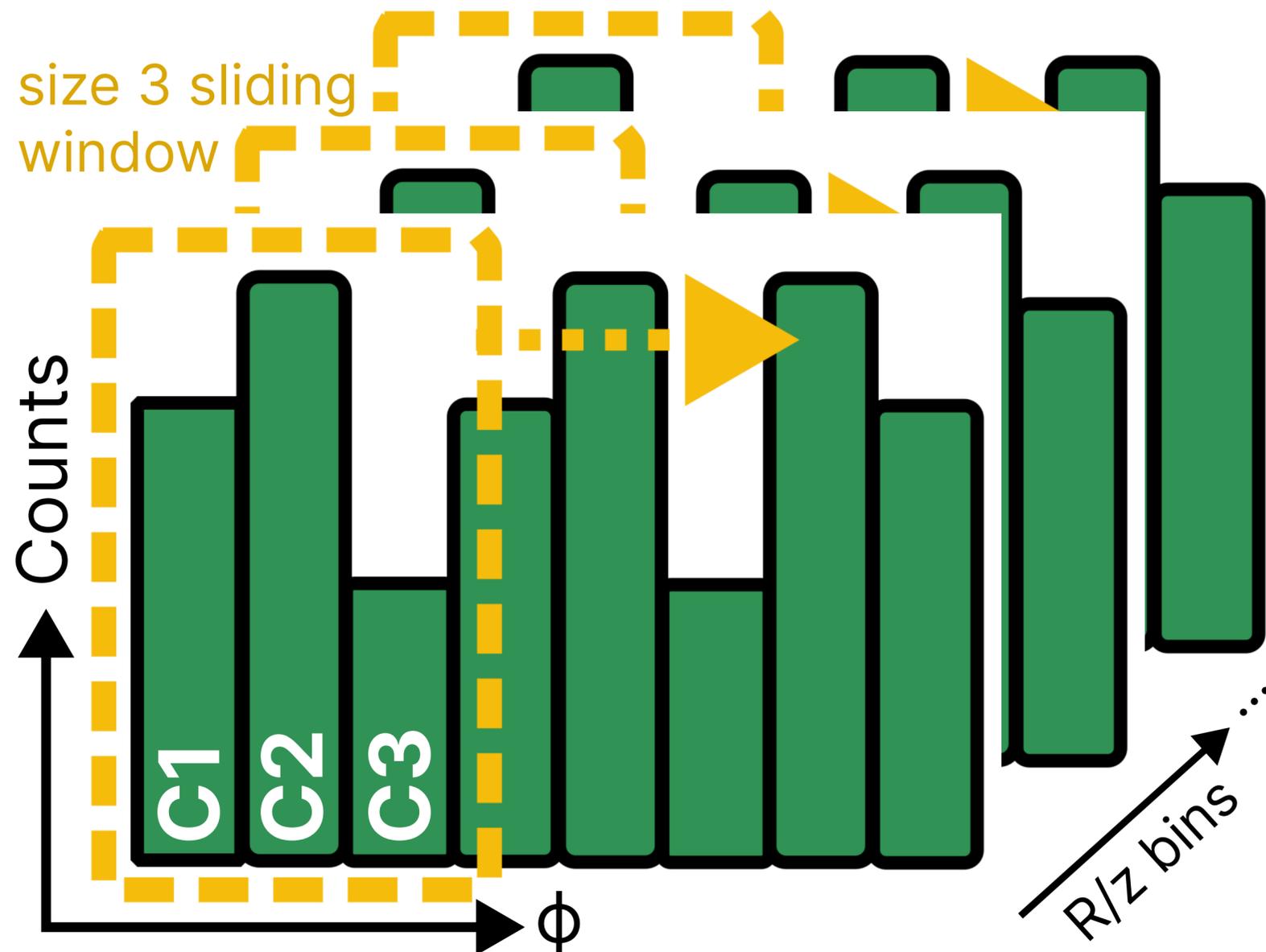


“ByteSplits” Iterative Algorithm

- Optimizes the mapping of TCs to seeding bins, avoiding cluster splits
 - reduces variance of number of TCs/bin along ϕ
- The algorithm does not run on firmware
 - its output can be encoded as a TC-to-bin Lookup table (LUT)

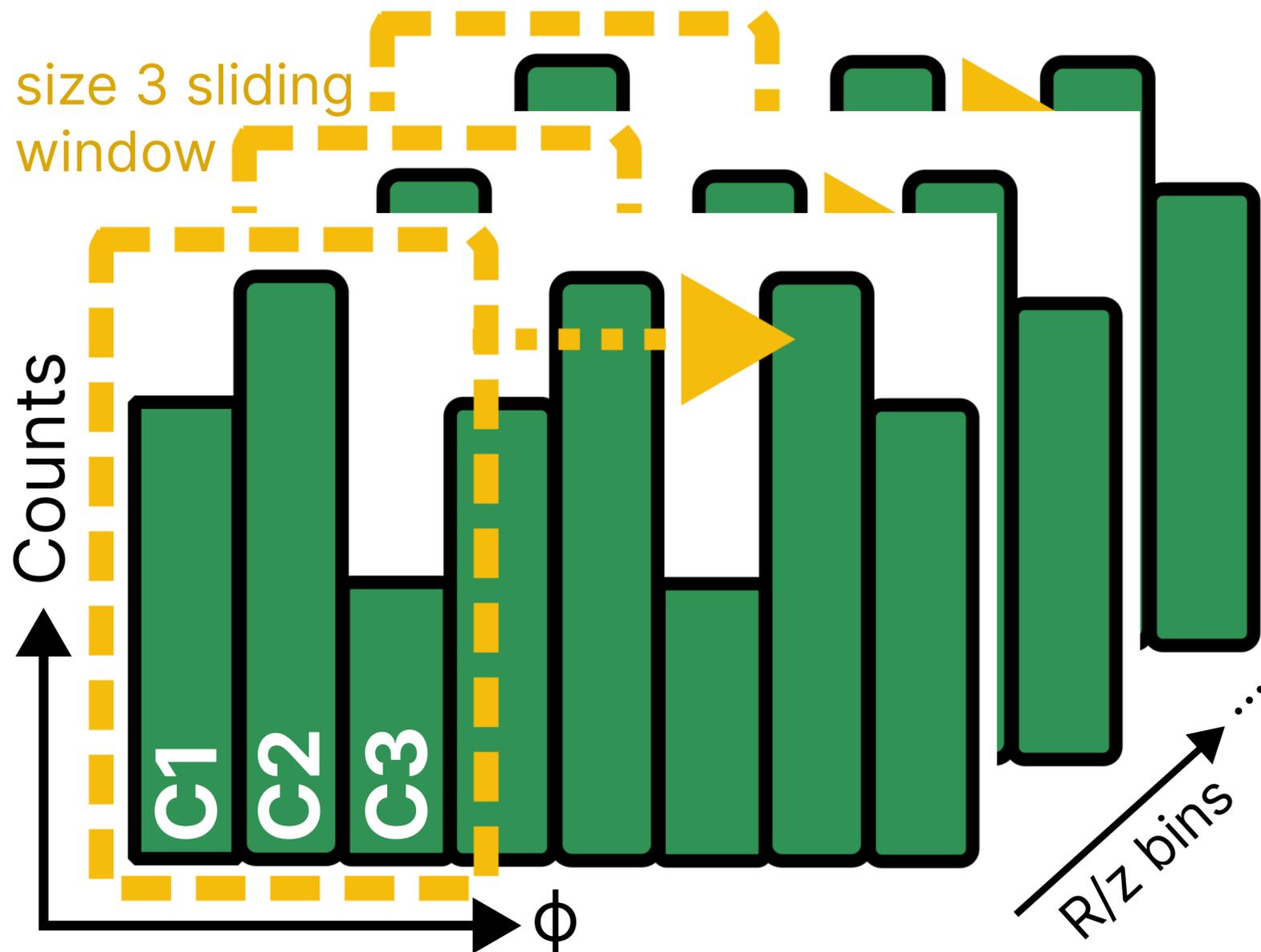
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Calculate differences

$$D_{\text{right}} = C_3 - C_2$$

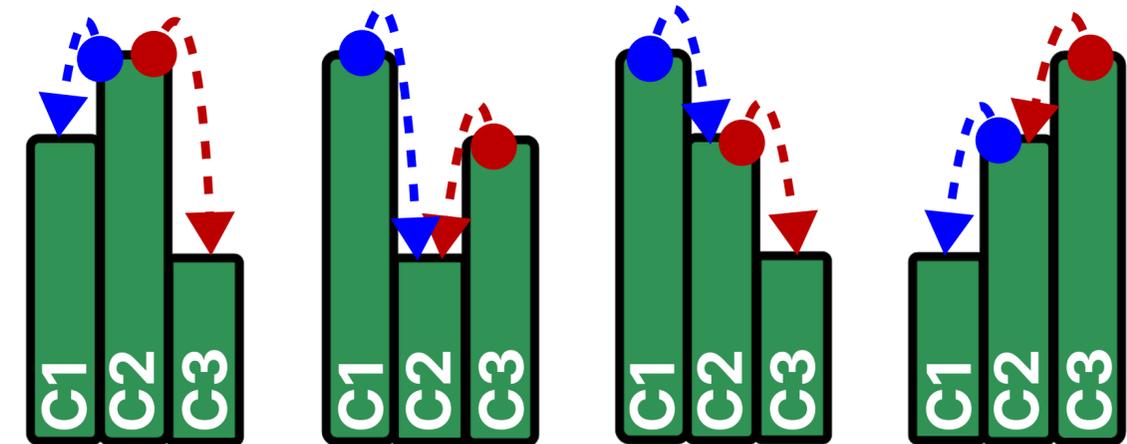
$$D_{\text{left}} = C_2 - C_1$$

On which side should the TC shift be performed?

draw pseudo-random number x from uniform distribution

$$\text{Side} = \begin{cases} \text{left,} & \text{if } x \sim \mathcal{U}(0, 1) < D_{\text{left}} / |D_{\text{left}} + D_{\text{right}}| \\ \text{right,} & \text{otherwise} \end{cases}$$

How to move TCs for the 4 high/low combinations?



Stopping criterion

must be true for all bins i

$$|D_{\text{left},i}| + |D_{\text{right},i}| \leq \max \{ 1, \lambda \times (|D_{\text{left},i}^0| + |D_{\text{right},i}^0|) \}$$

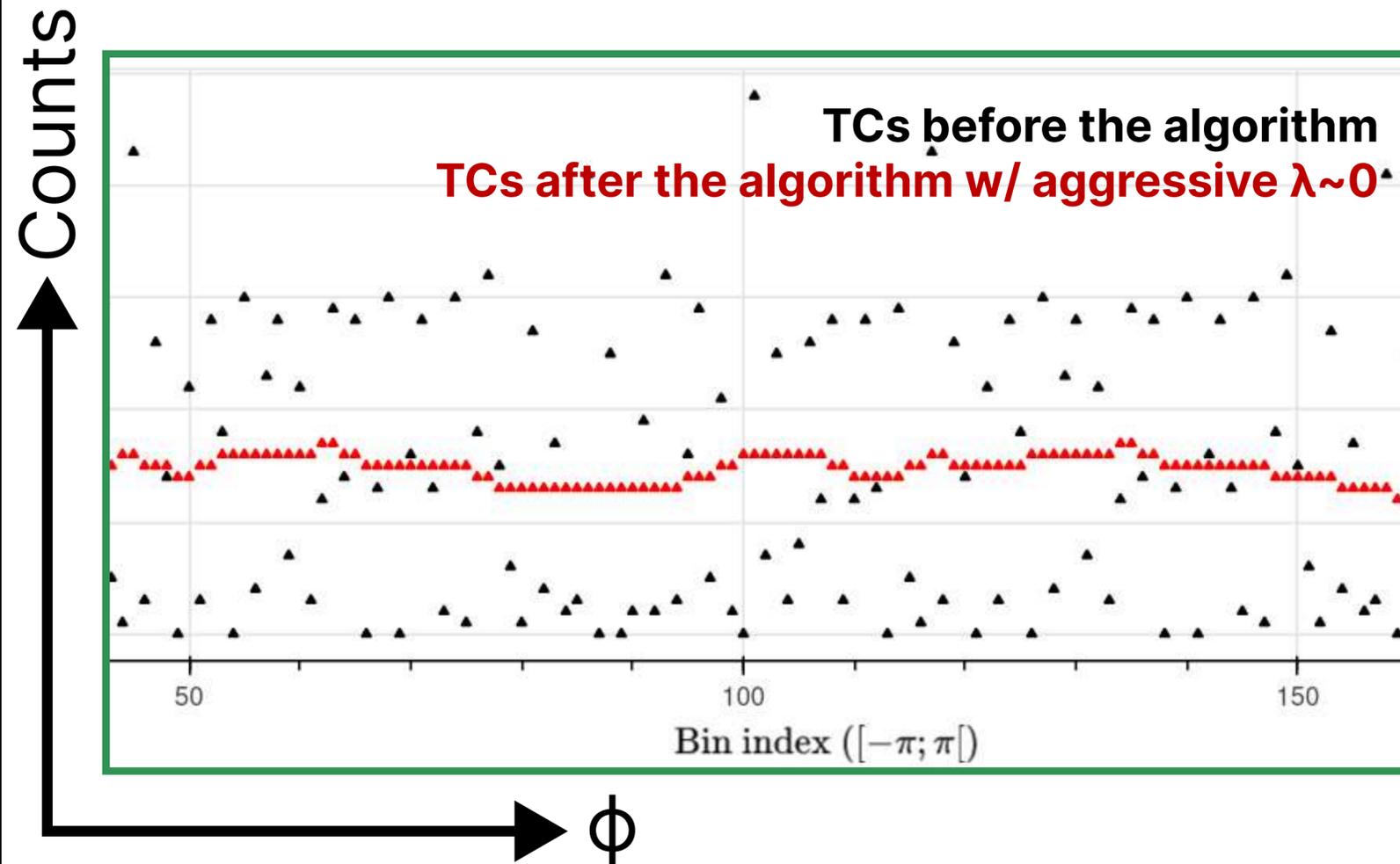
stabilisation for small λ

tunable parameter
 $0 \leq \lambda \leq 1$

initial diffs.
for bin i

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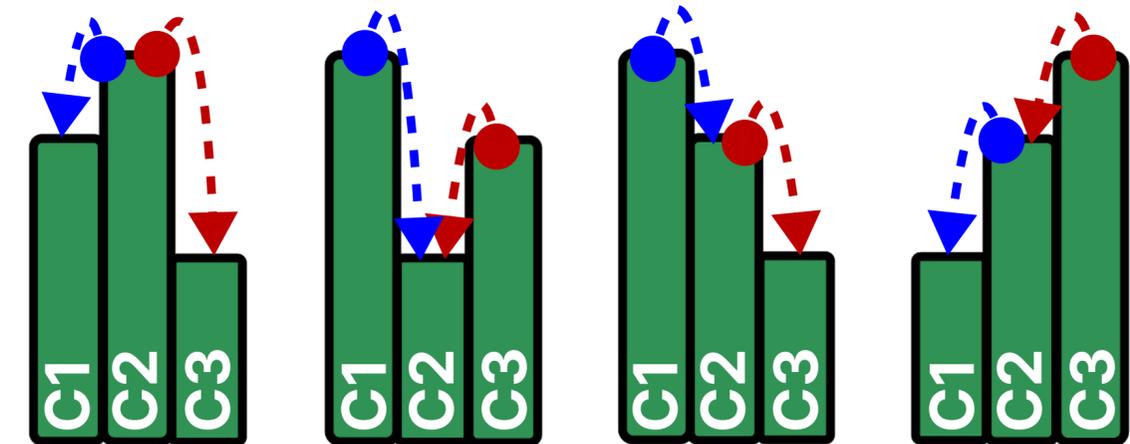
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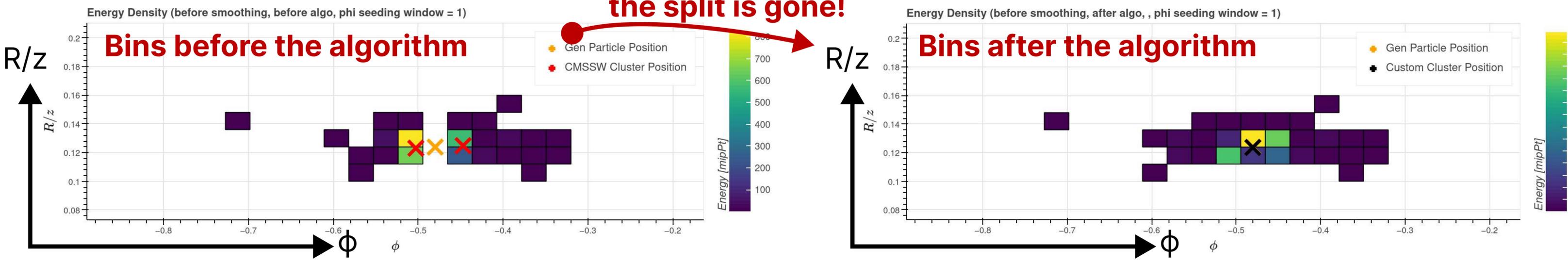
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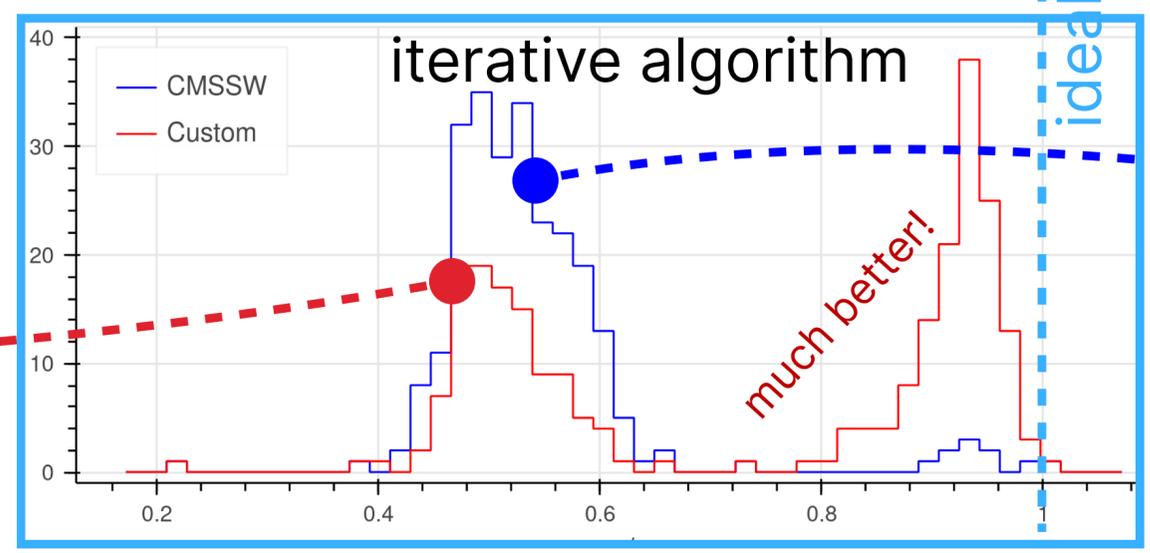
the split is gone!



Momentum Response

$$p_{T,Reco} / p_{T,Gen}$$

custom reconstruction w/ iterative algo

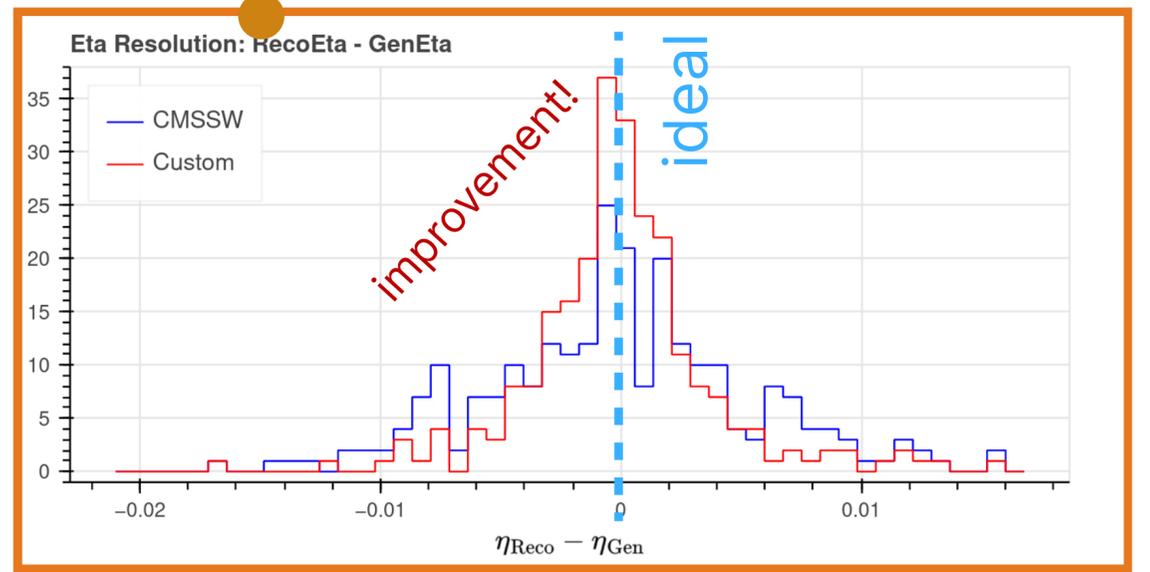
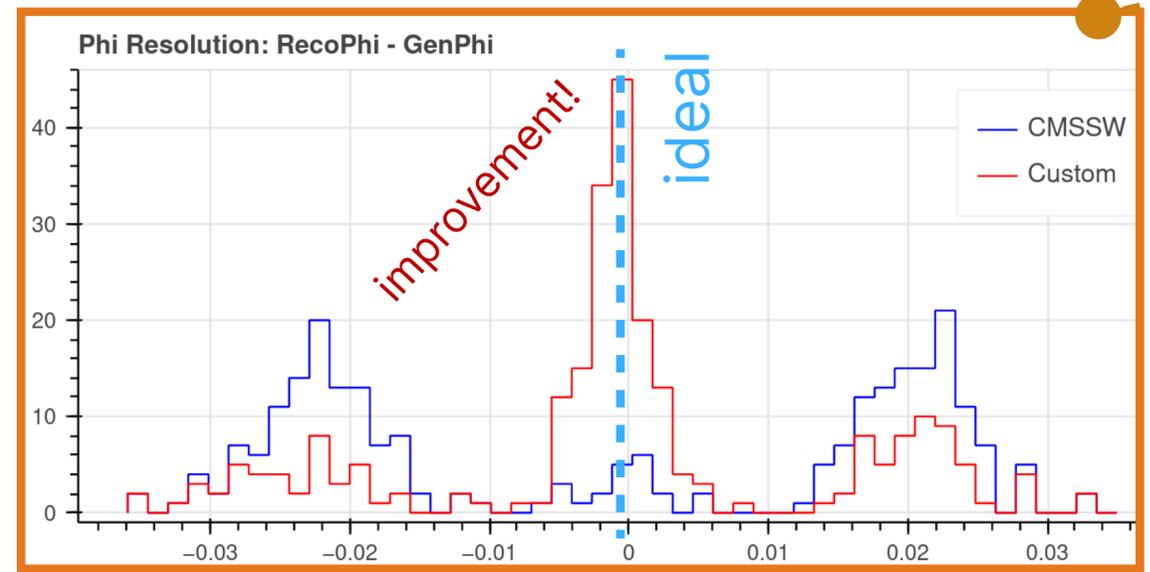


original reconstruction, no iterative algo

better position resolution, despite moving TCs!

Position Resolution

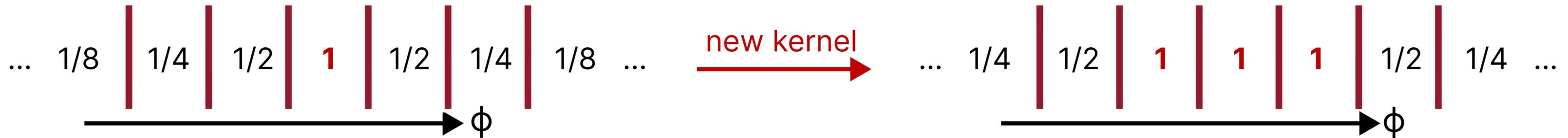
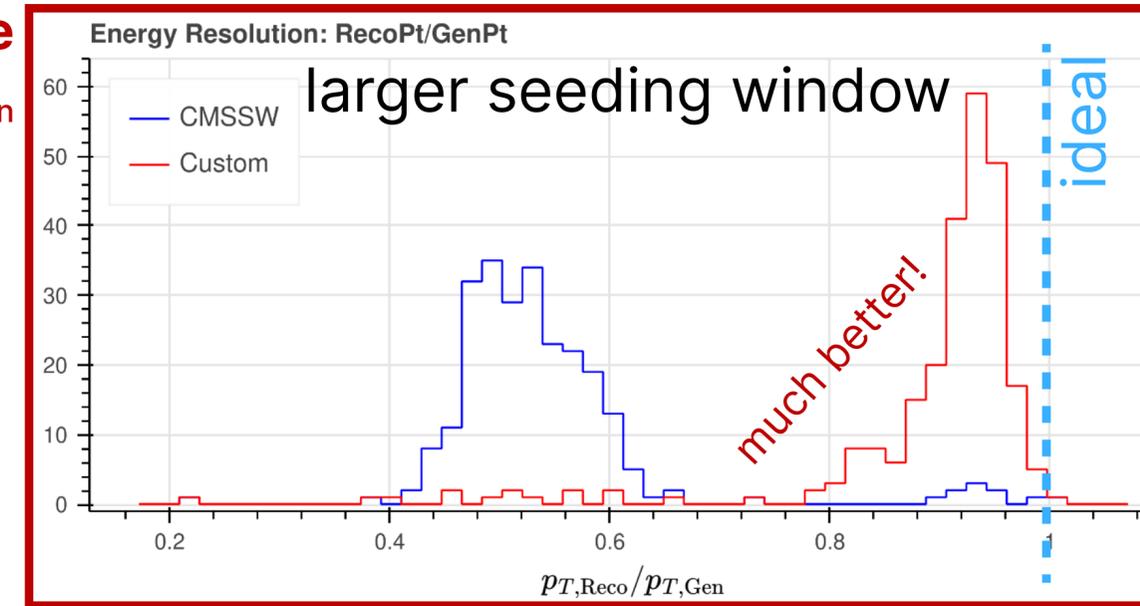
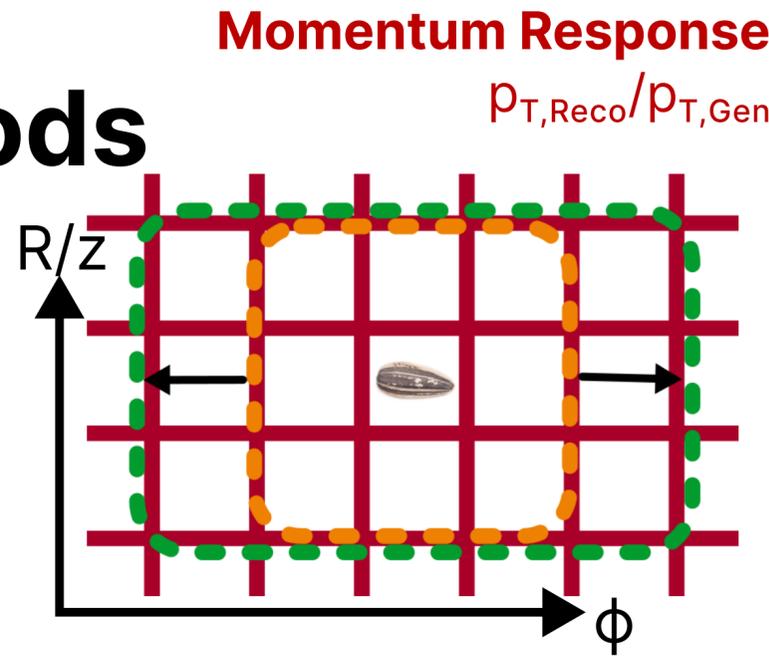
$$\phi_{Reco} - \phi_{Gen}, \eta_{Reco} - \eta_{Gen}$$



Note: resolutions showed for events with split clusters only

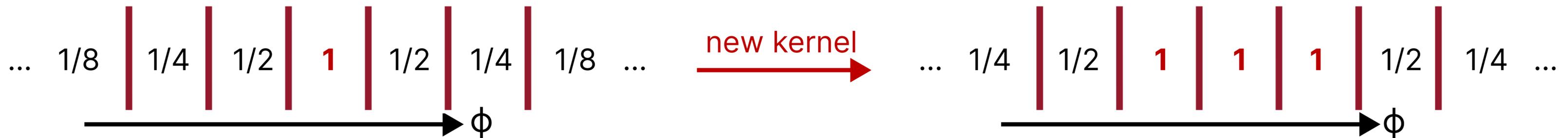
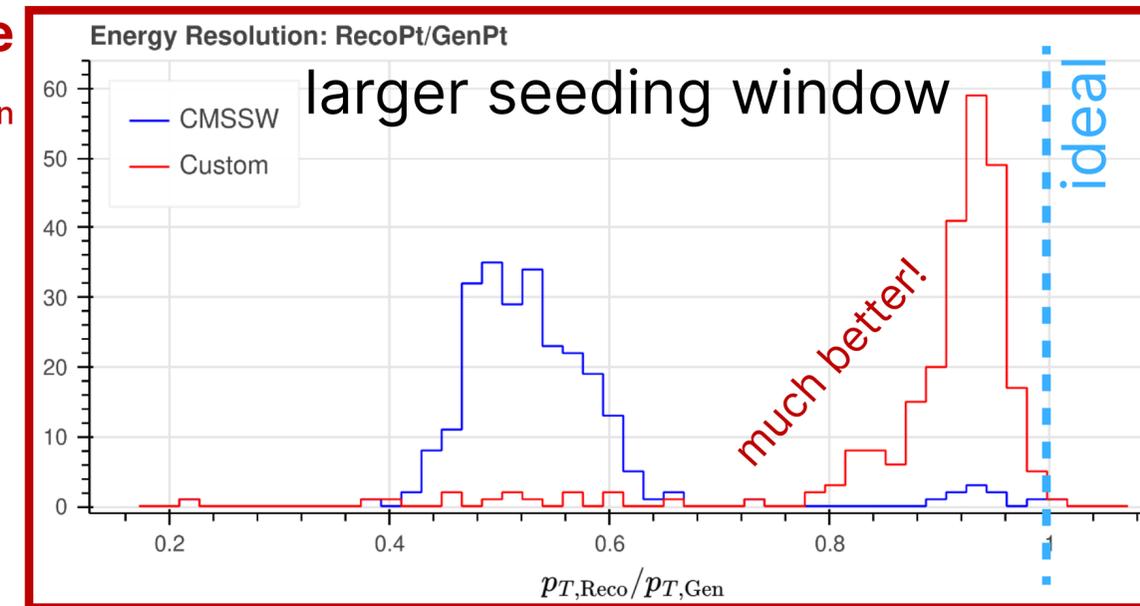
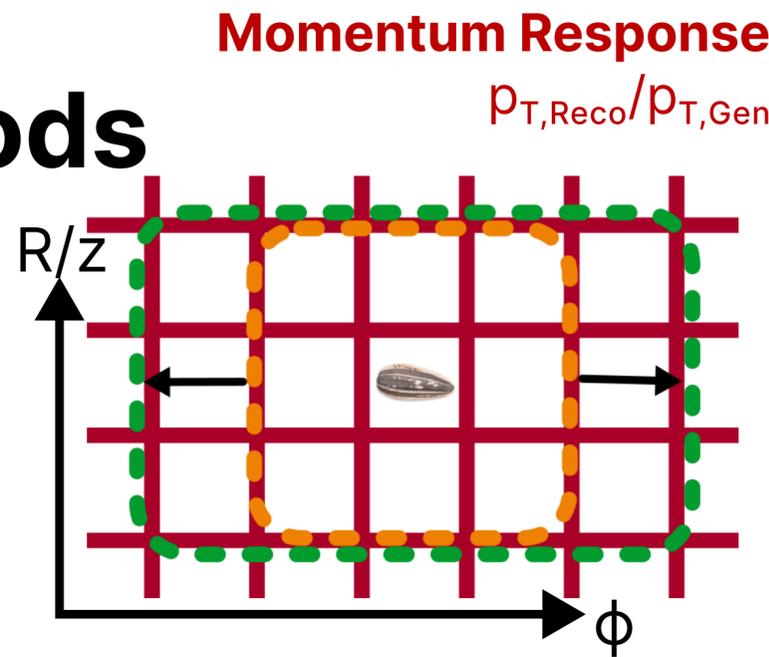
Other split-removal methods

- **Add “2nd order neighbors” in the seeding**
 - seeding window extended along ϕ
 - more FPGA resources required!
- **Modify the smoothing kernel along ϕ**
 - very similar results to method above
 - no additional resources!



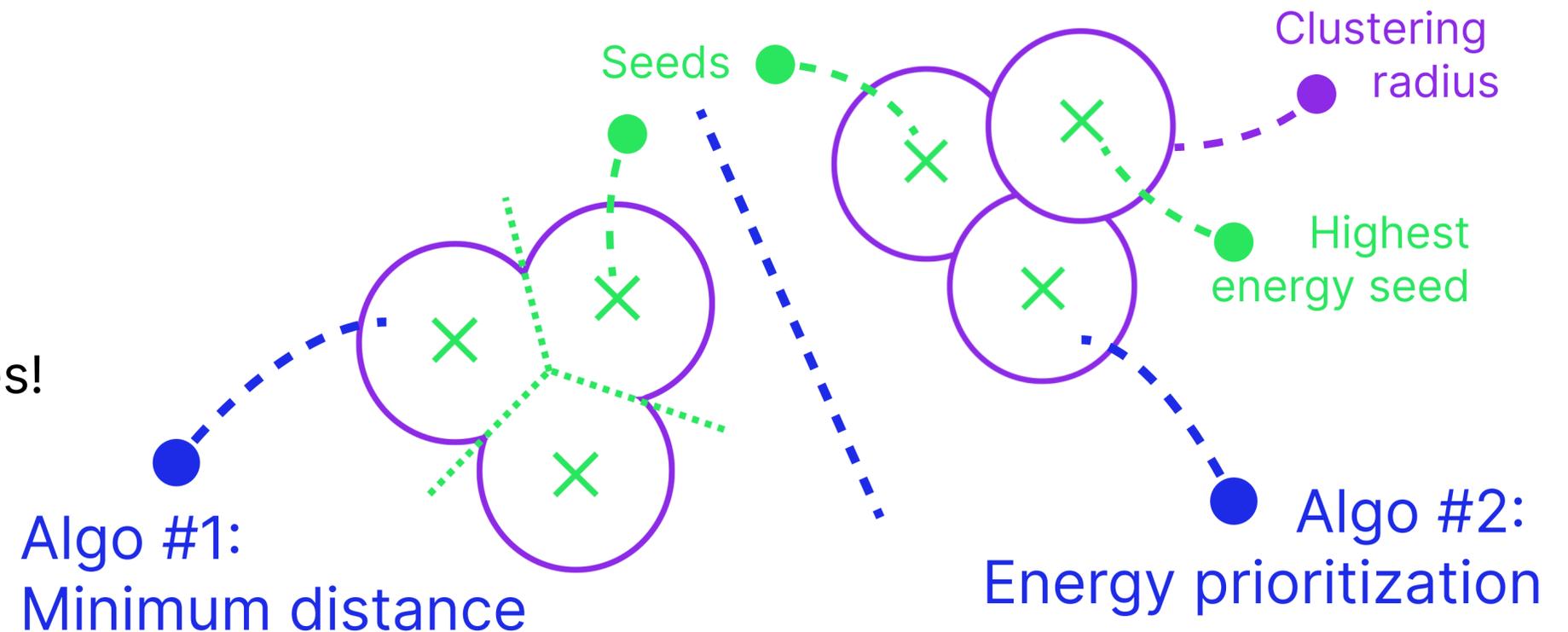
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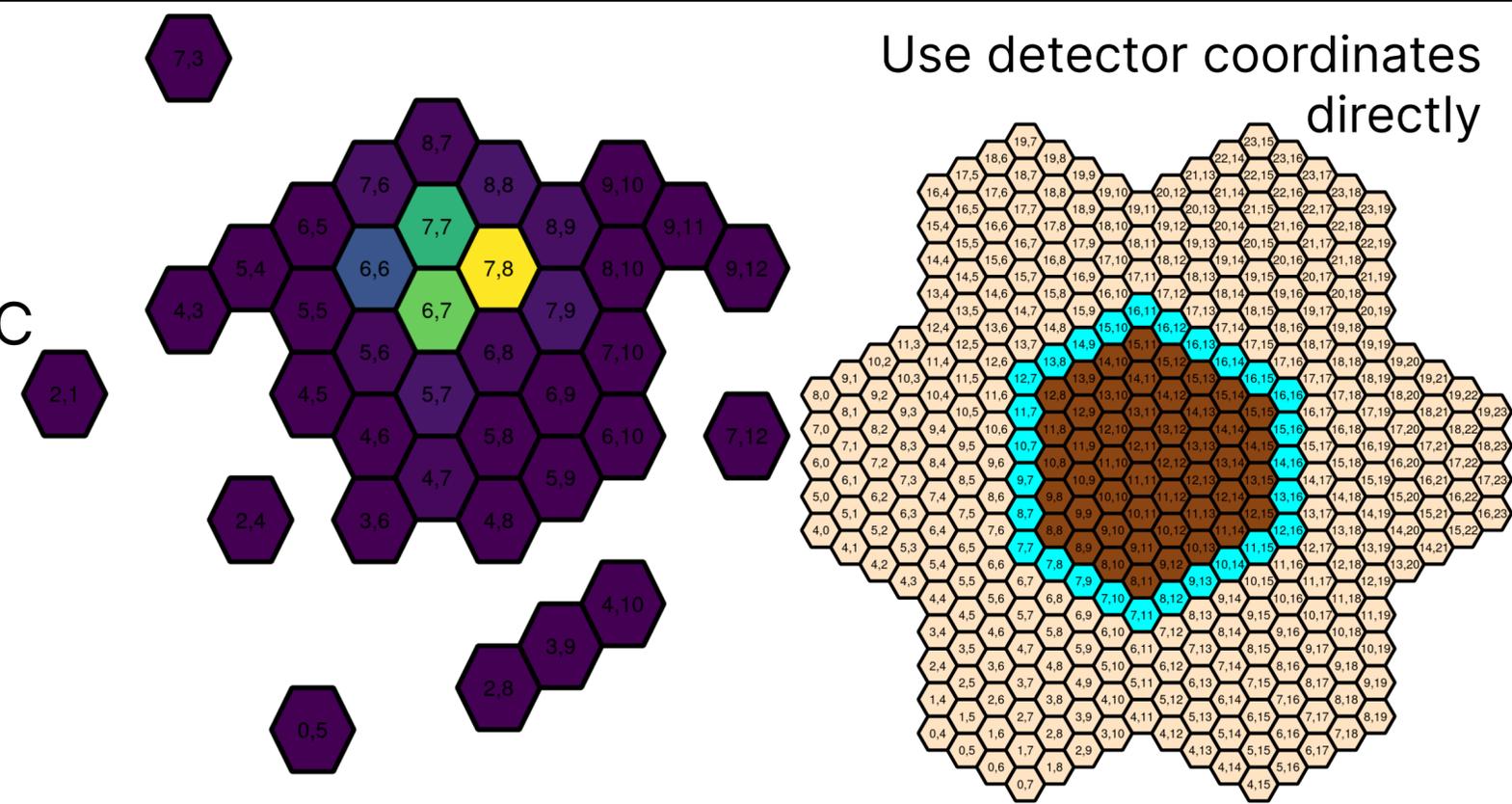
Clustering

- Associates TCs to seeds and calculates cluster properties
 - Energy, position, ...
 - Used for the previous resolution studies!
- Uses different projective coordinates
 - $x/z, y/z$
- Two algorithms currently defined



Conclusions & Next Steps

- HGCAL is a very significant CMS upgrade for the HL-LHC
- HGCAL trigger primitives for the L1 trigger are the final product of a complex processing chain
 - robust on- and off-detector electronics
 - resource-constrained algorithms perform computations on multiple ASICs and FPGAs
- A complete reconstruction chain is available in simulation, from hits to cluster-related variables
 - algos implemented in firmware, optimization on-going



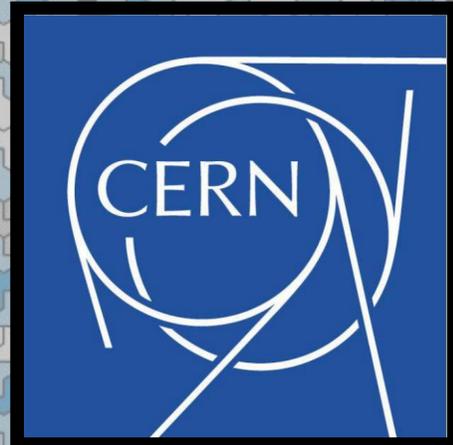
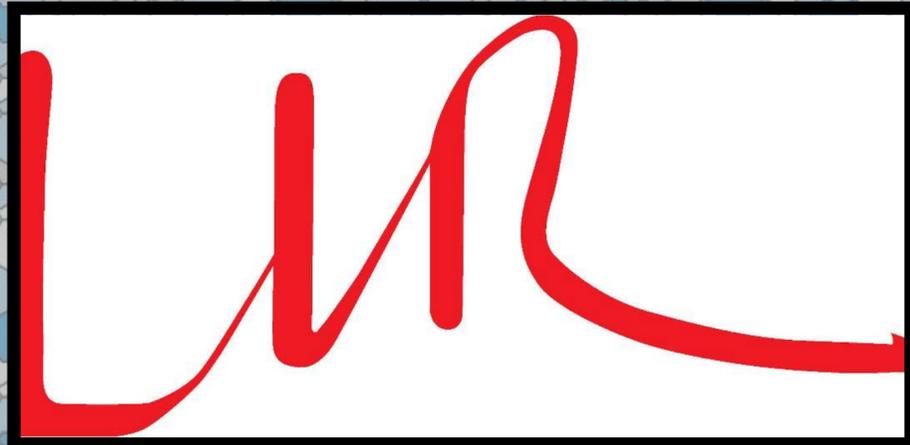
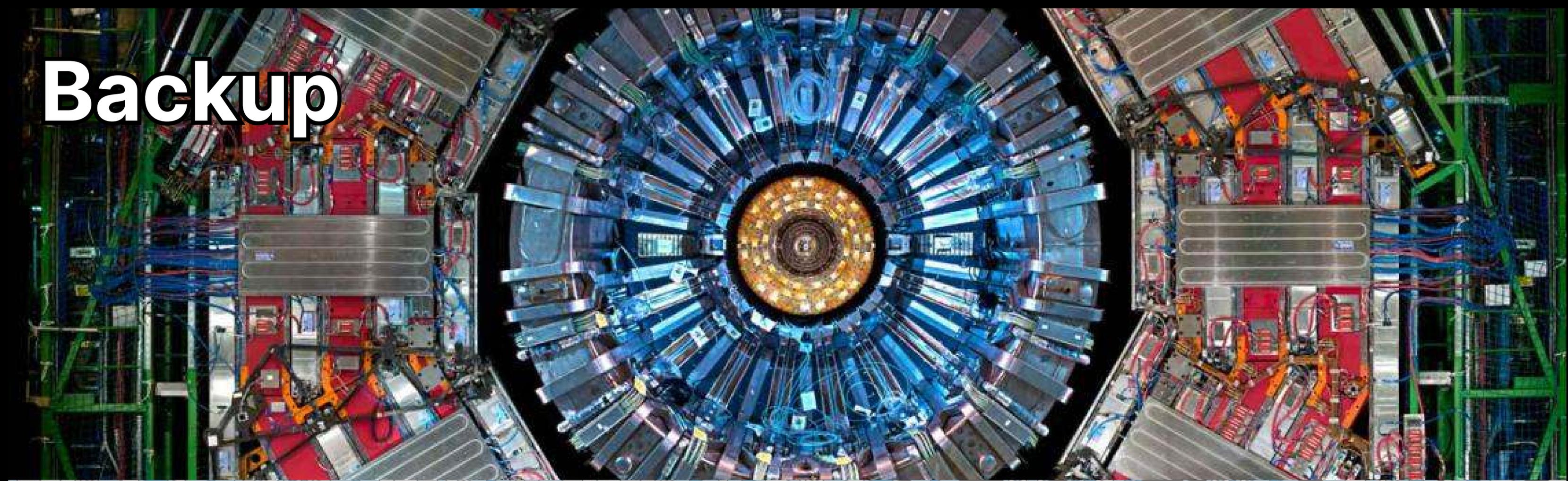
0 PU → 200 PU

Can we do better? **Yes!** Current work:

- Explore more detector-like coordinate systems to remove TC-bins routing complexity
- Focus on specific regions in the detector to further decrease the amount of data processed
- Assess the performance of new algorithms with 200PU
- Develop 2D and 3D event displays for quick inspection



Backup



Indico



References

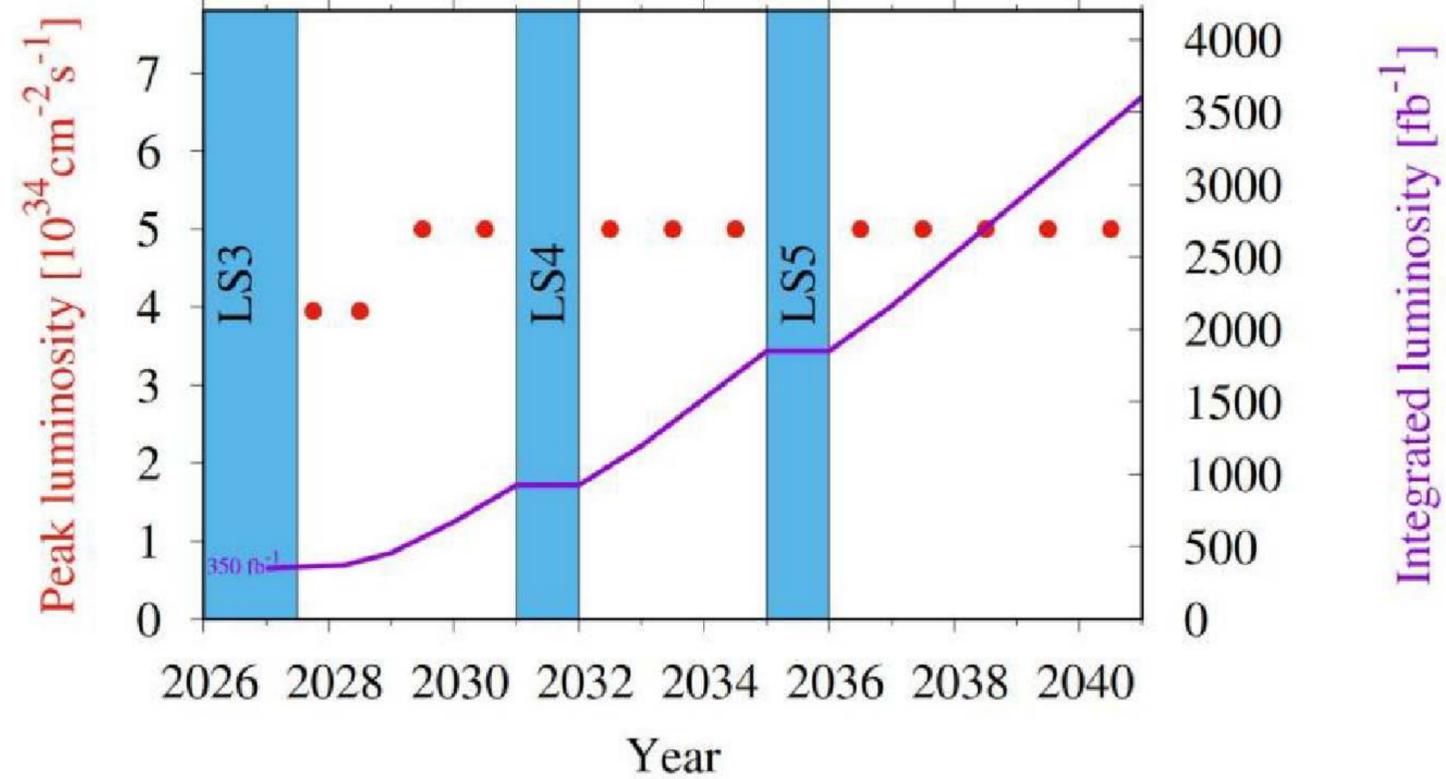
- [HL-LHC Official website](#)
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- L. Portales, [L1 triggering on High-Granularity information at the HL-LHC](#), CALOR 2022
- N. Strobbe, [The overall electronics chain \(powering and readout\) of the CMS HGICAL](#), CALOR 2022
- T. Kolberg, [The SiPM-on-Tile Section of the CMS High Granularity Calorimeter](#), CALOR 2022
- [Cover lower background image] An aperiodic monotile, [arXiv2303.10798](#)

Many thanks to:

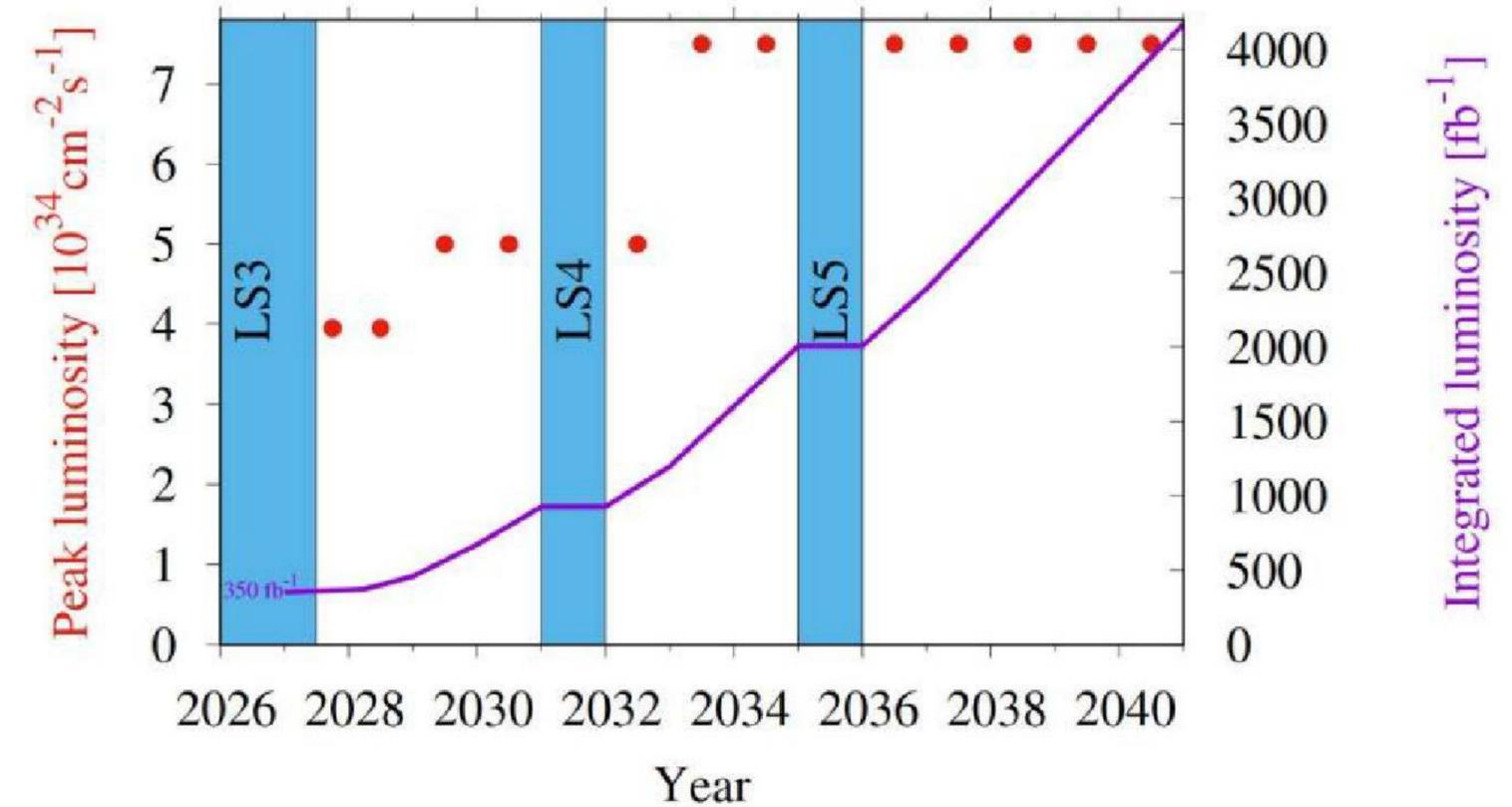
- J-B. Sauvan for reviewing and providing his own yet unpublished references.
- M. Chiusi for the help provided to produce 3d event displays

Projected Luminosity

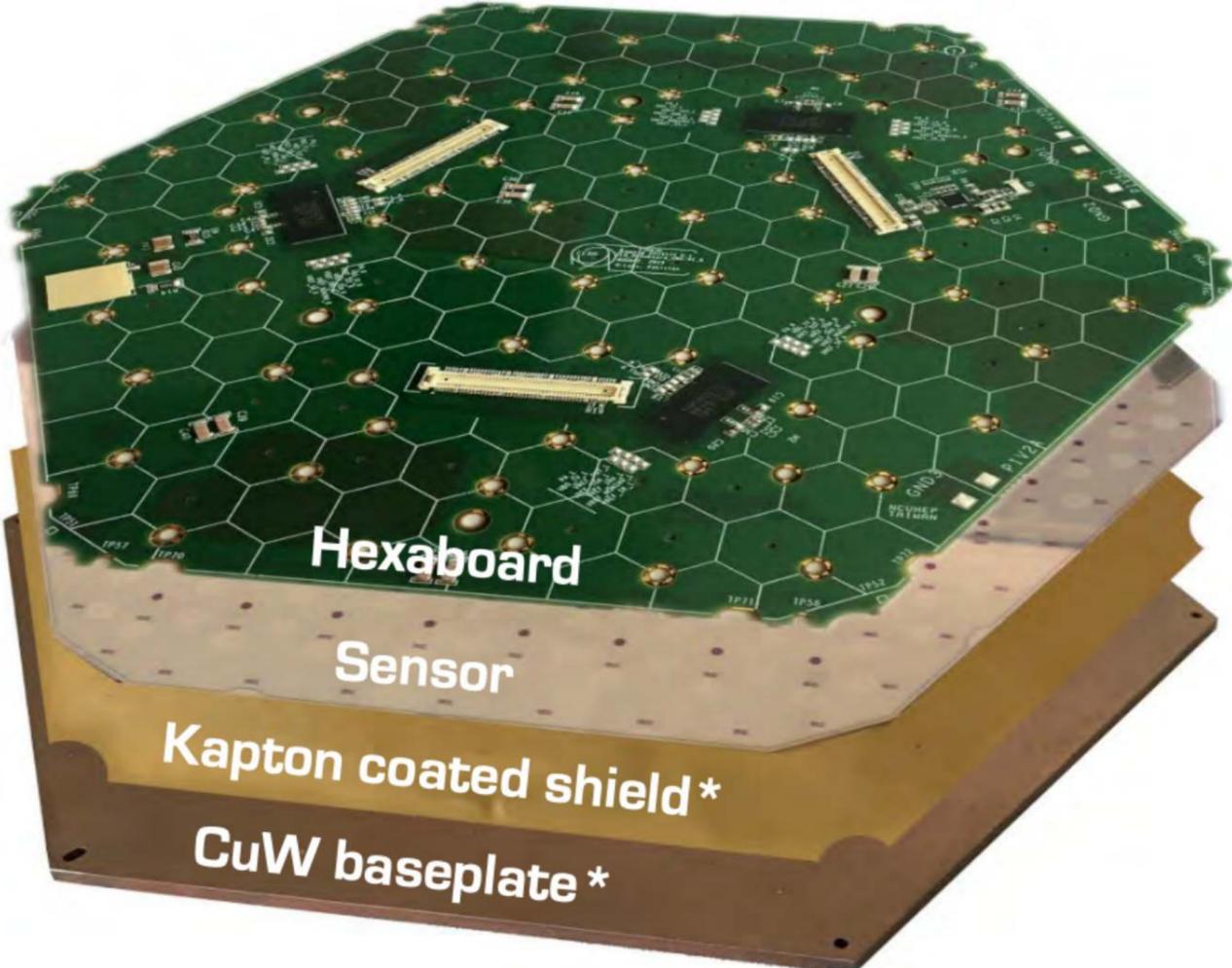
Nominal HL-LHC parameters



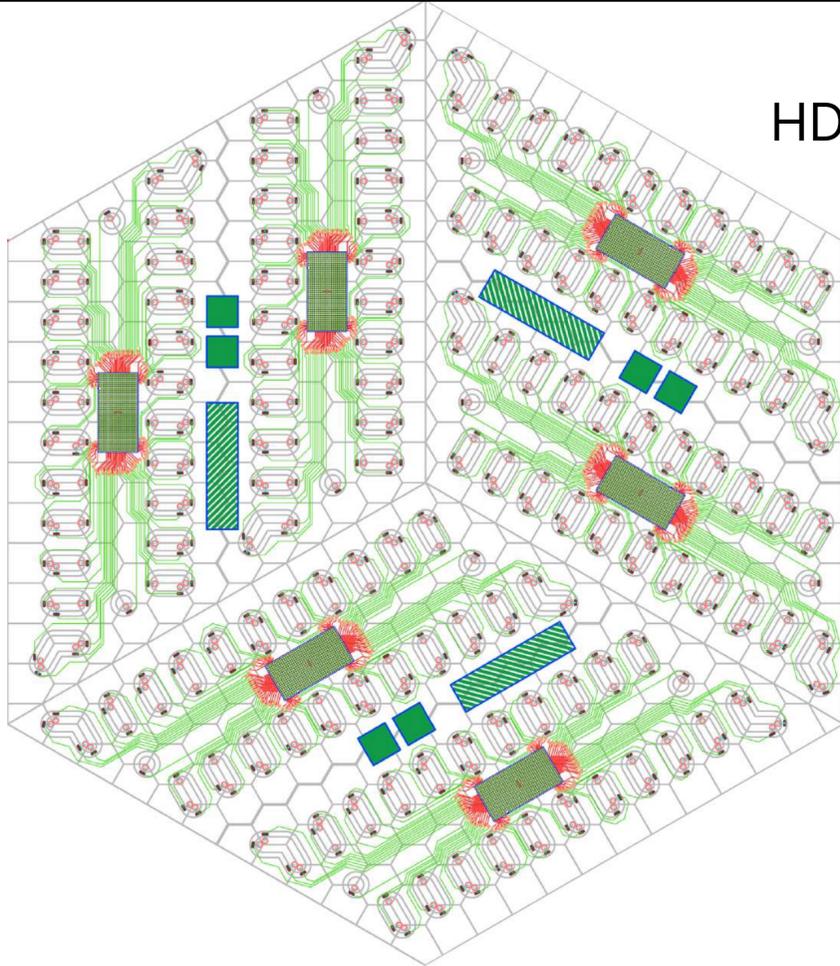
Ultimate HL-LHC parameters



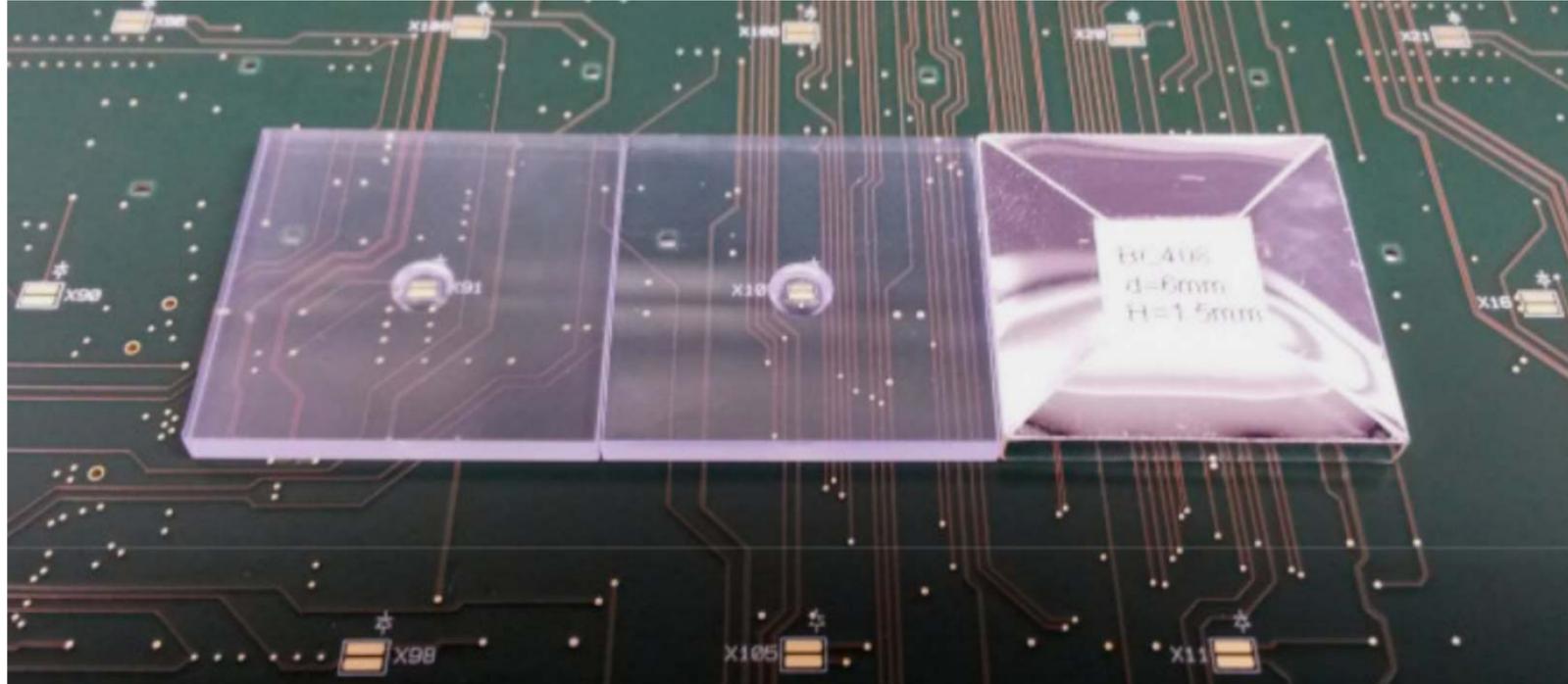
Module properties



Module structure



HD module schematic w/ 6 HGCROCs



Scintillator tiles w/ SiPM

HGCROC

- Front-end read-out ASIC @ 40MHz, outputs 1.28 Gb/s w/ 12.5μs latency!
- Low noise (<2500 electrons) and high dynamic range (0.2fC-10pC)
- Fast shaping time: <20% of the signal in other bunch crossings
- Timing information (ToA) down to 25ps
- Low power consumption: less than 20mW per channel
- Radiation tolerance: 200Mrad

Digitise collected charge:

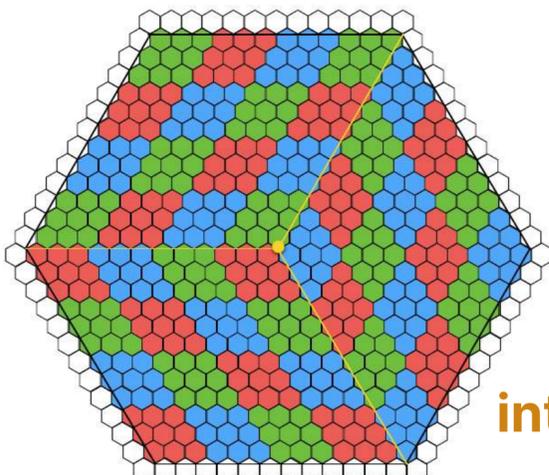
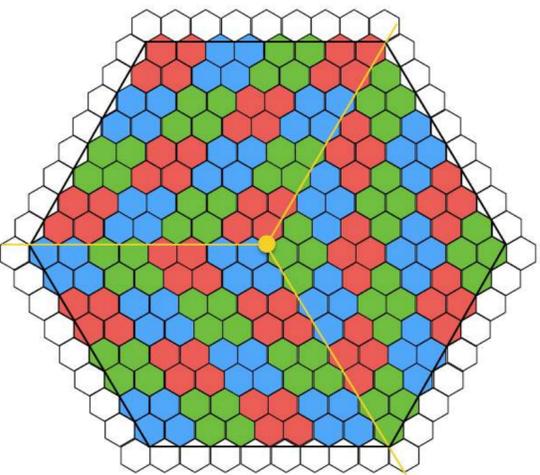
- <~100fC: 10bit ADC
- >~100fC: 12bit TDC ToT (up to 200ns)

Channels:

- 72 for energy and time measurements
- 2 for calibration
- 4 common mode

Sum channels into Trigger Cells (TCs)

- charge linearization
- compression to 7-bit floating point
- input to L1 trigger primitives

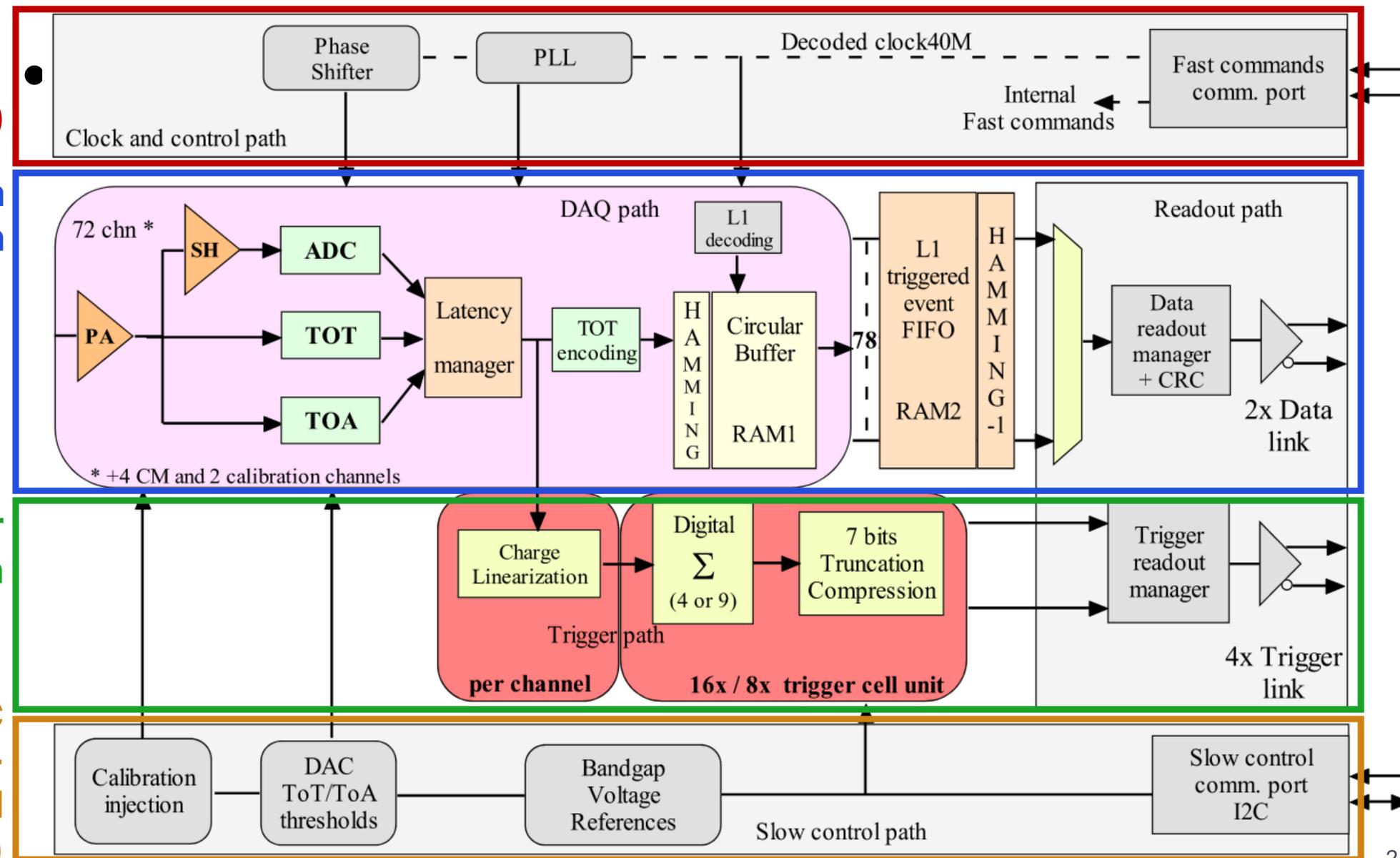


PLL
(phase-locked loop)

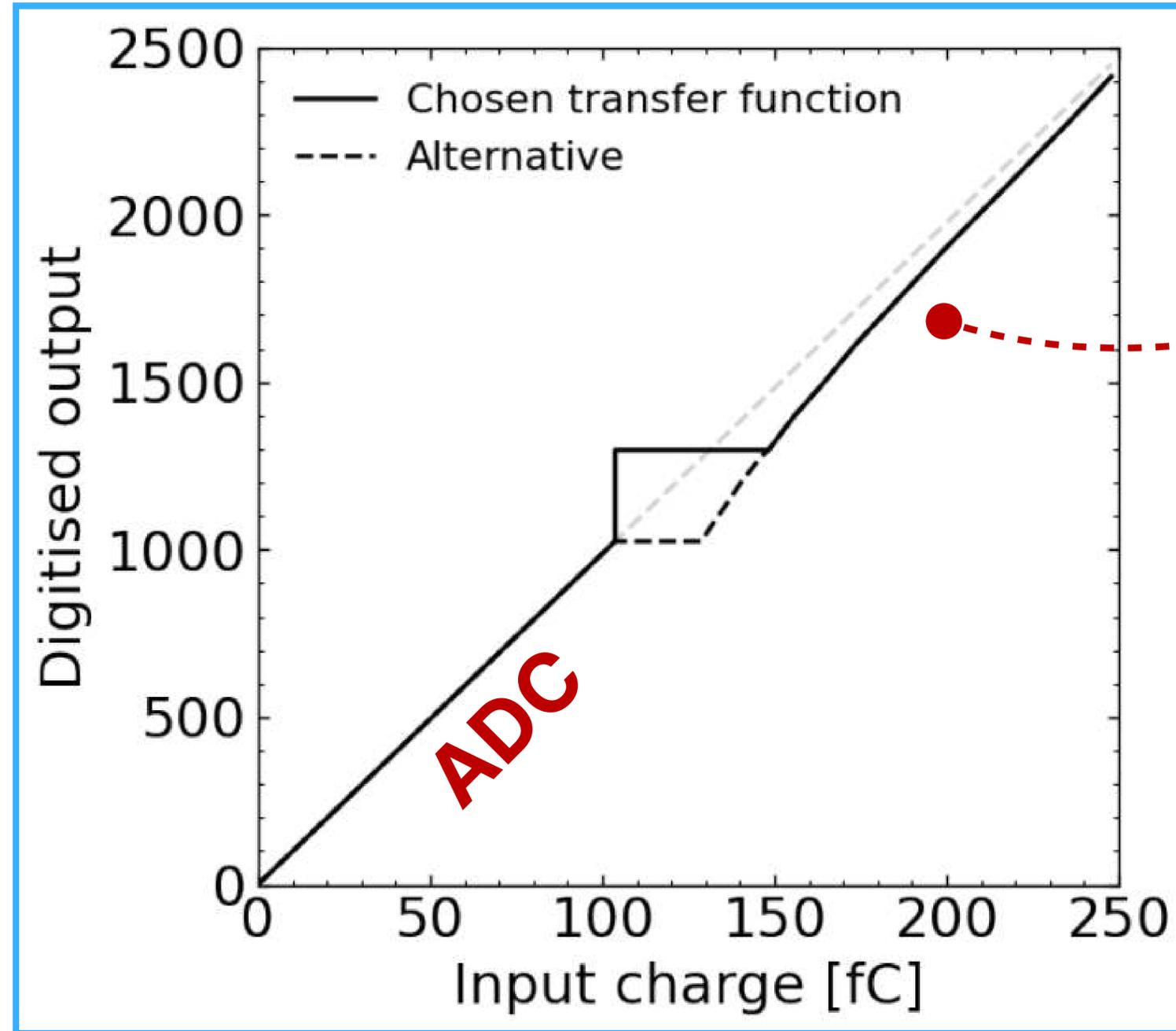
Data Path

Trigger Path

I2C
(inter-integrated circuit)



HGCROC Charge Linearization



**Time over
Threshold**

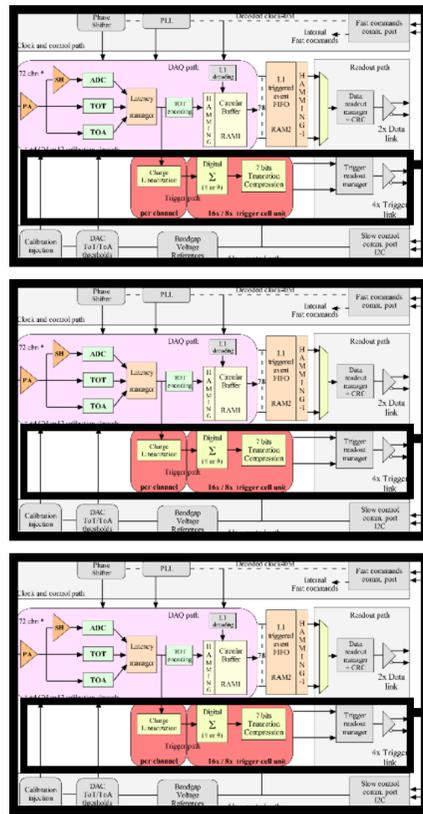
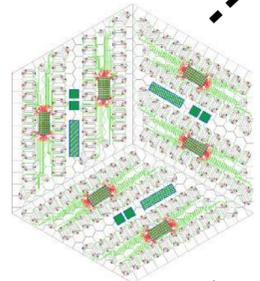
ECON-T

- Frontend chip: concentrates data w/ dedicated algorithms
- Selects or compresses HGCROC trigger data for transmission off-detector

Selects TCs above an energy threshold

- Variable data size
- Buffer to average output rate

One full module

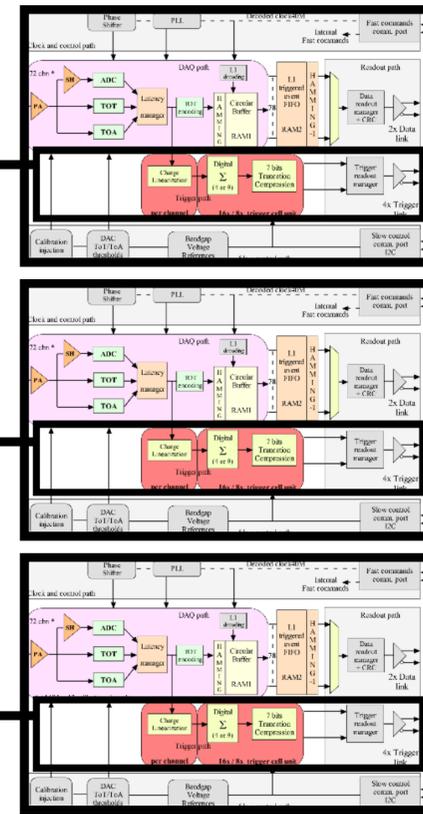
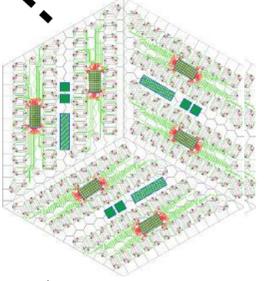


Algorithms

ECON-T



One full module

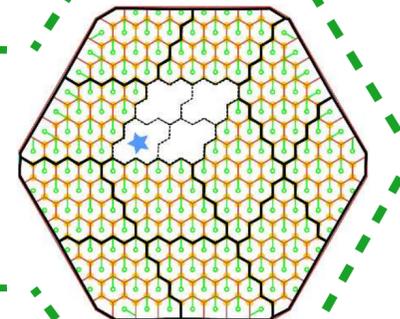


Selects N TCs with the highest energy

- Fixed data size
- Currently used for CE-E

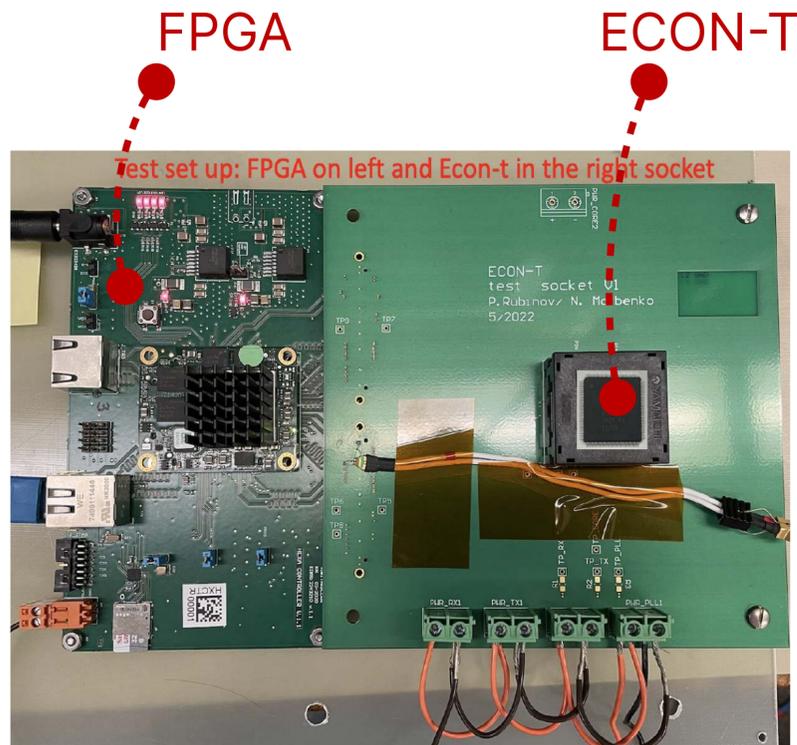
Sums TCs for further data reduction

- Keep location of highest energy TC in STC
- Currently used for CE-H

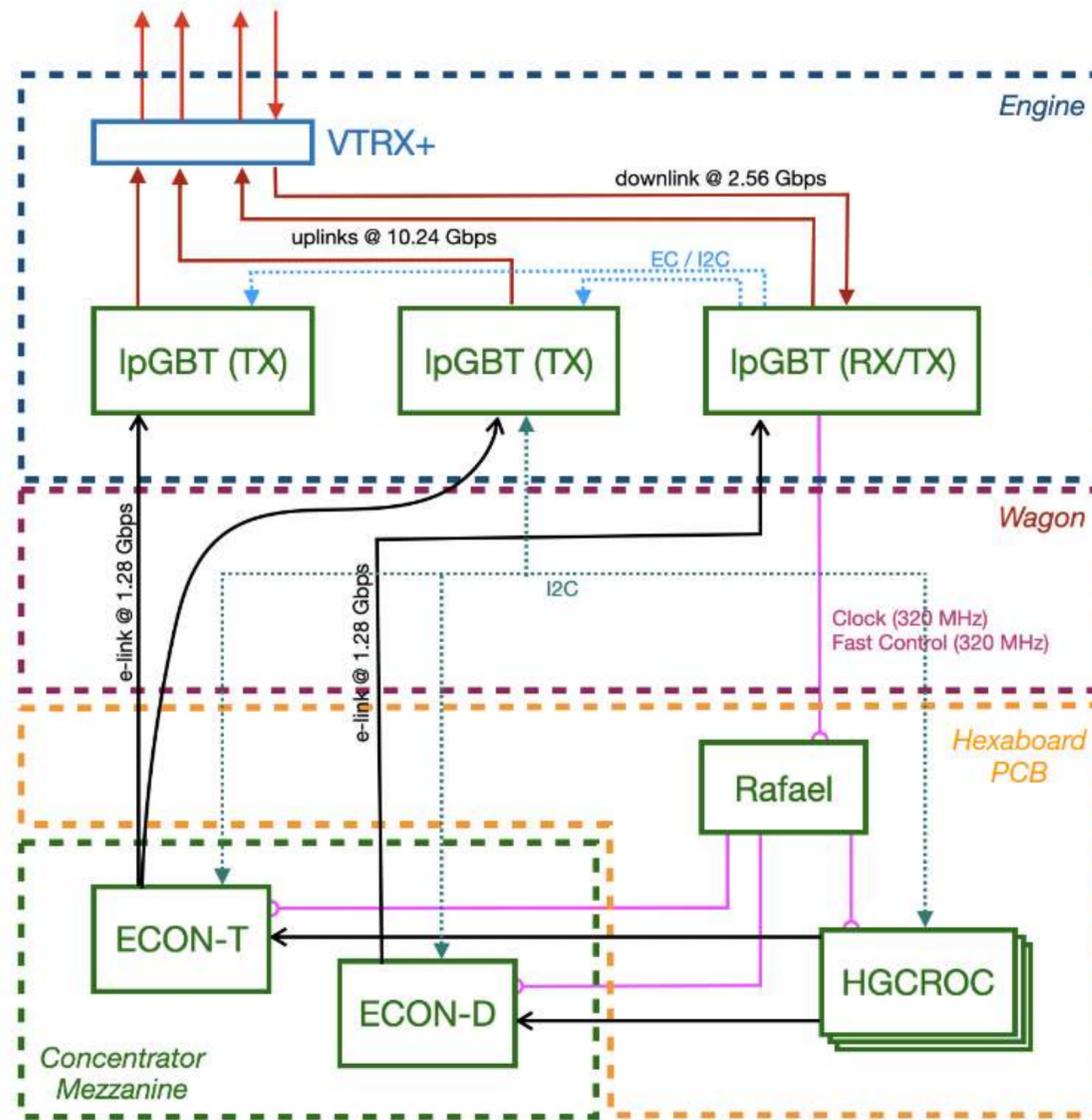


Frontend architecture

- **ECON-T**: frontend concentrator chip for trigger path, concentrates trigger data via one of 4 trigger algorithms
- **ECON-D**: frontend concentrator chip for DAQ path, performs channel alignment and zero suppression after L1 Accept
- **Rafael**: clock and fast control fanout
- **IpGBT**: for sending/receiving data/clock/control signals via optical link (and VTRX+)



Test setup



5

credit to N. Strobbe

Engine board is connected to 2 Wagon boards

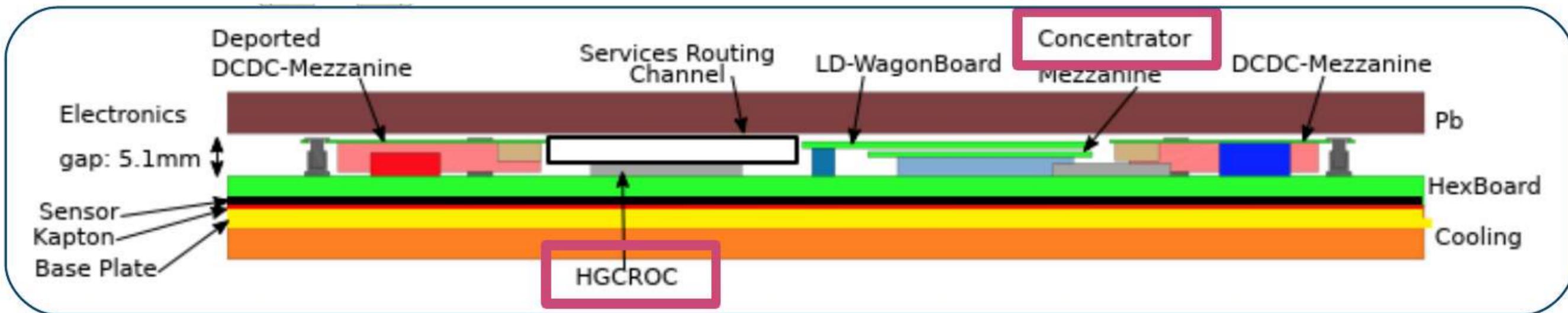
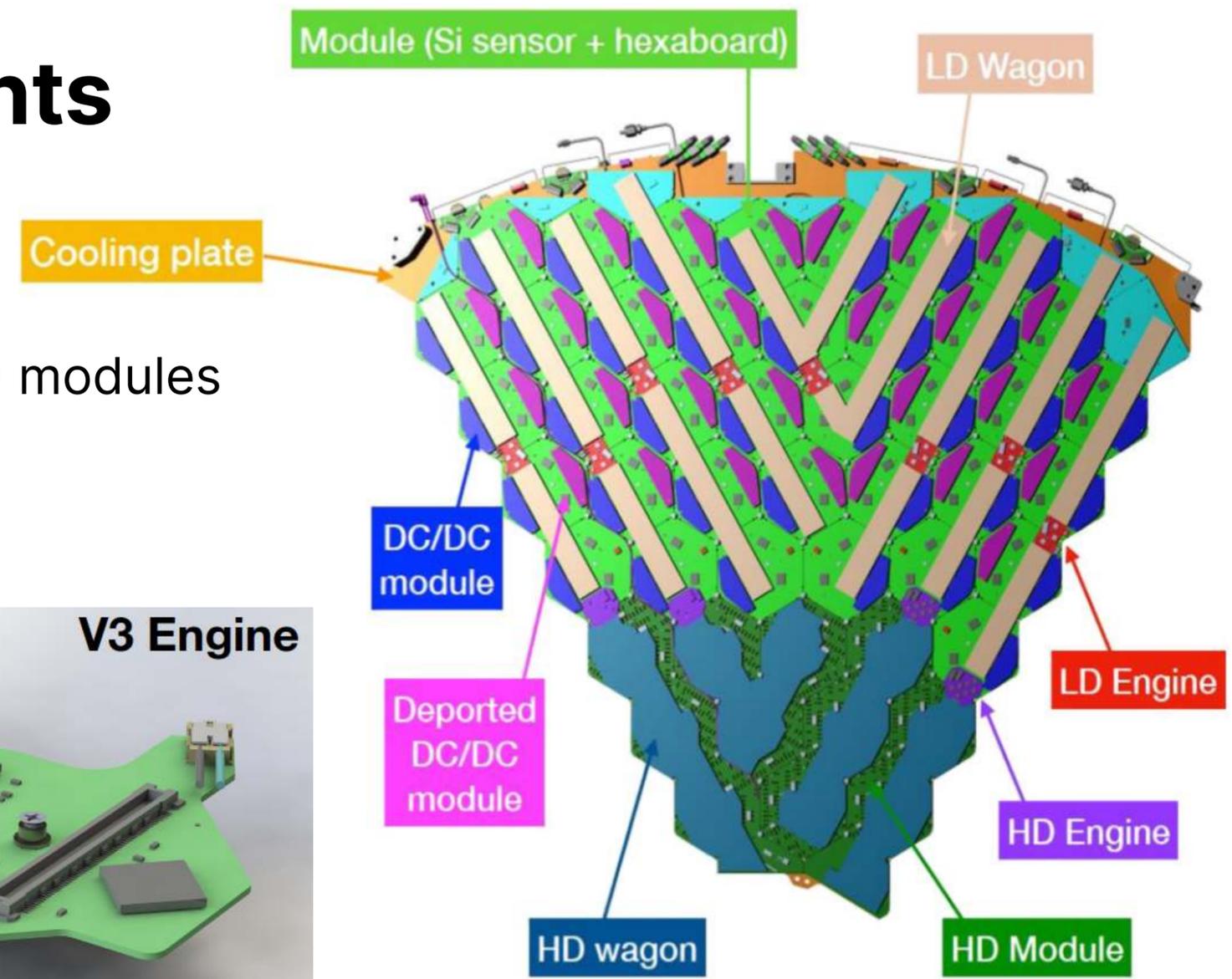
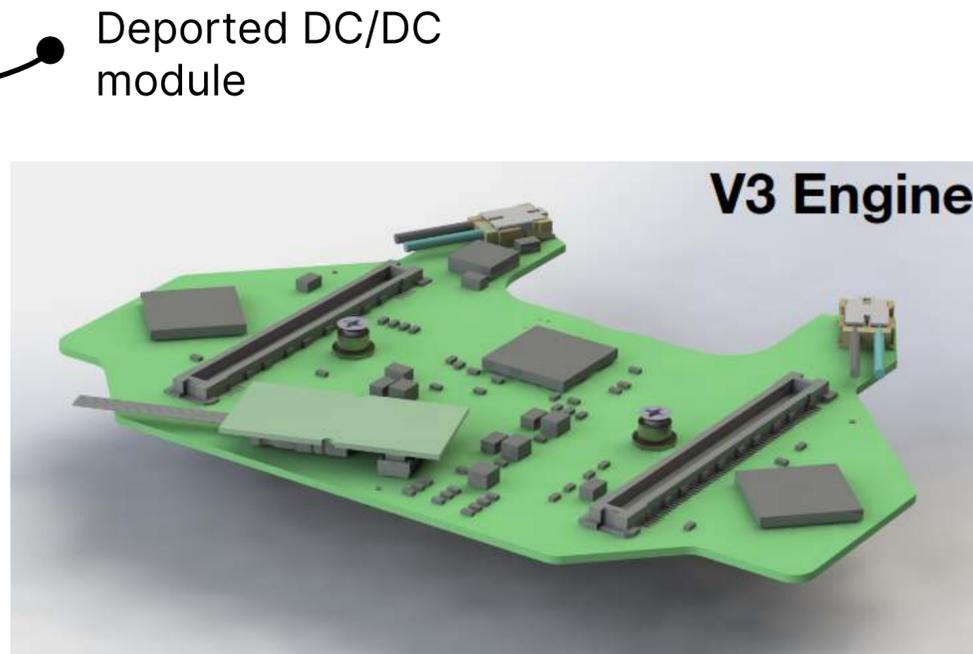
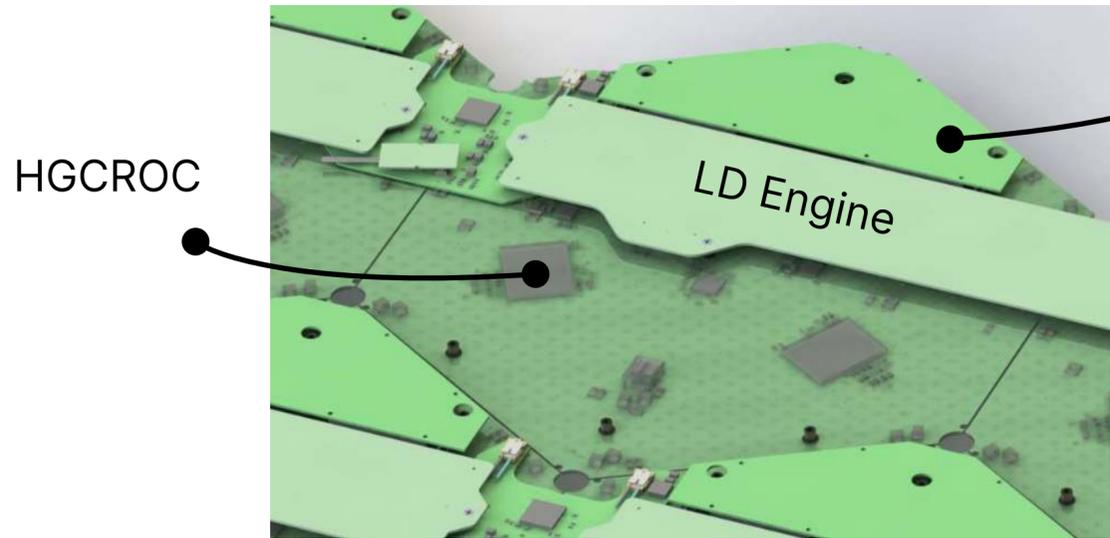
Passive Wagon board is connected to 1 – 3 Modules

Module is composed of the Si sensor and hexaboard PCB

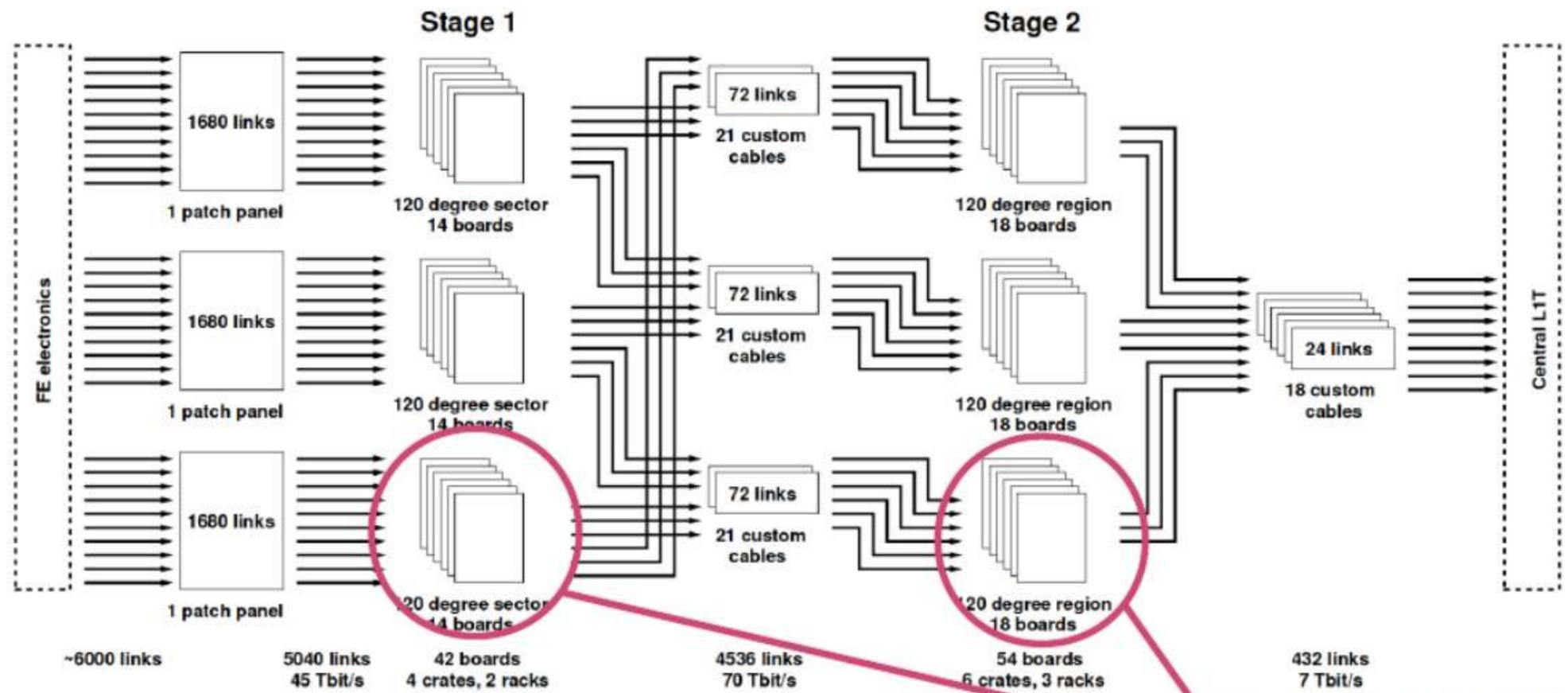
HGCROC and ECON are custom for this project, all other chips and components are common developments!

Hardware / Mechanical Components

- 60 degree symmetry for the cassettes
- Wagons are passive boards (many shapes and lengths)
- Engine is always the same and supports up to 6 LD (3 HD) modules



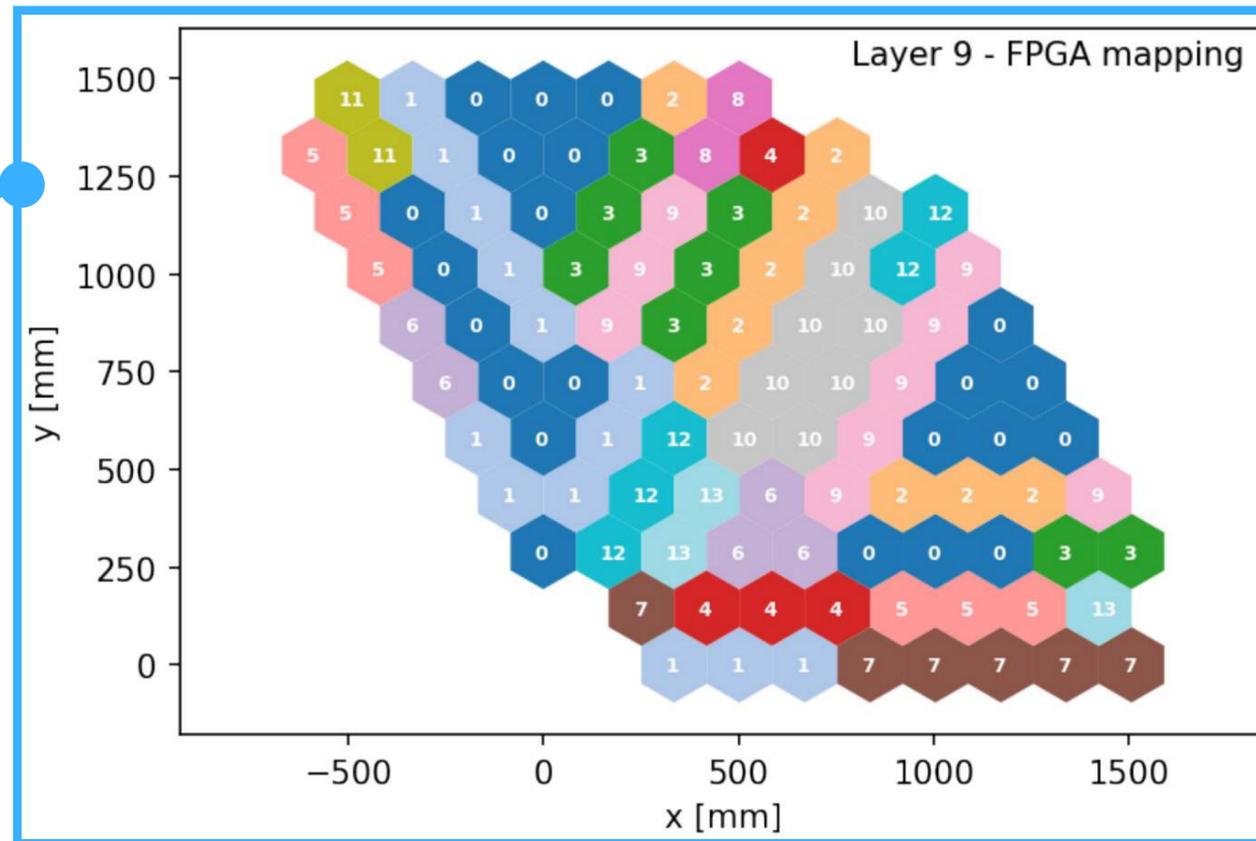
Frontend mapping to Backend



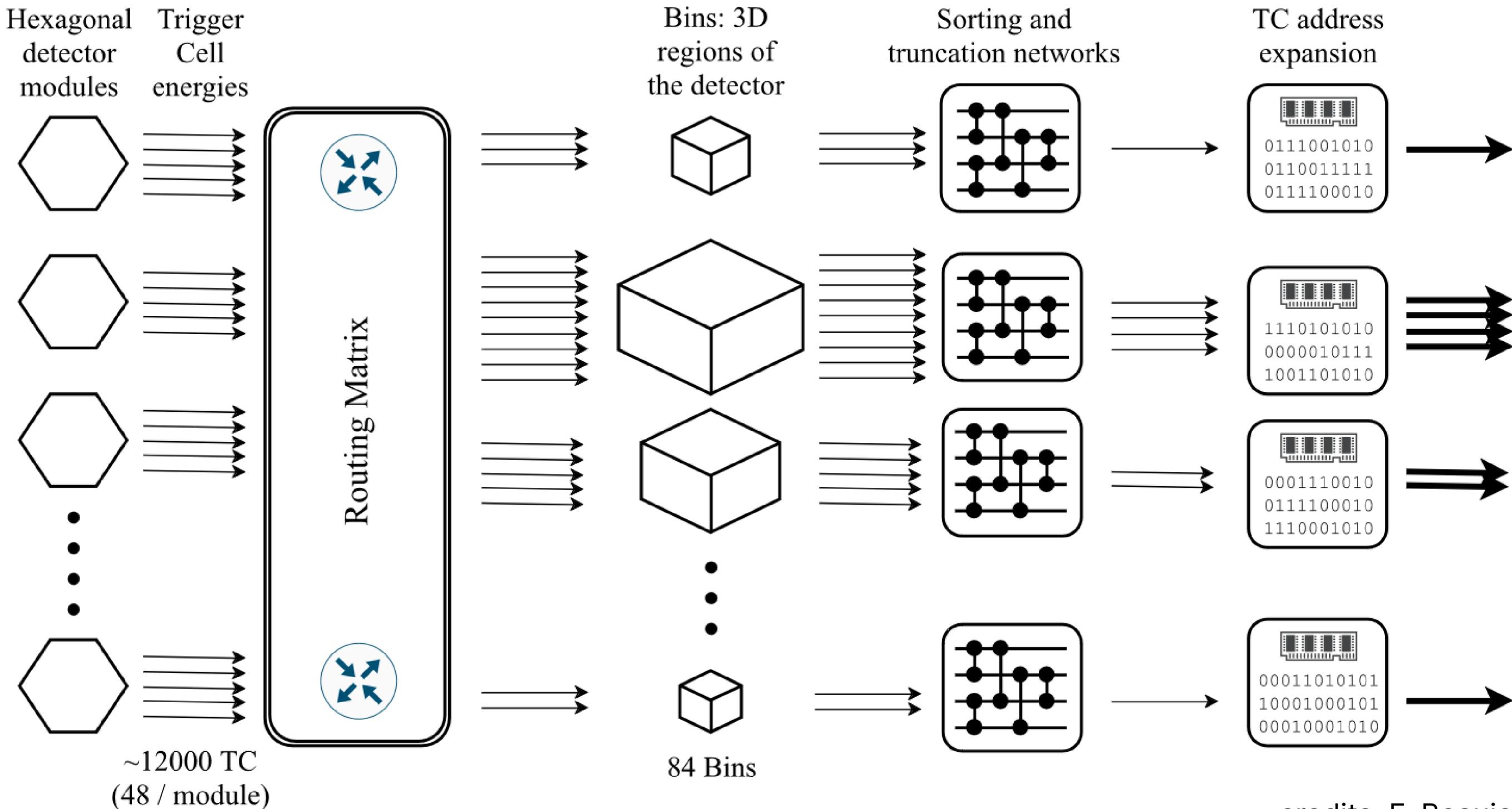
Serenity board prototype



Mapping of modules to FPGAs in a 120° sector (stochastic Hill Climbing algorithm)

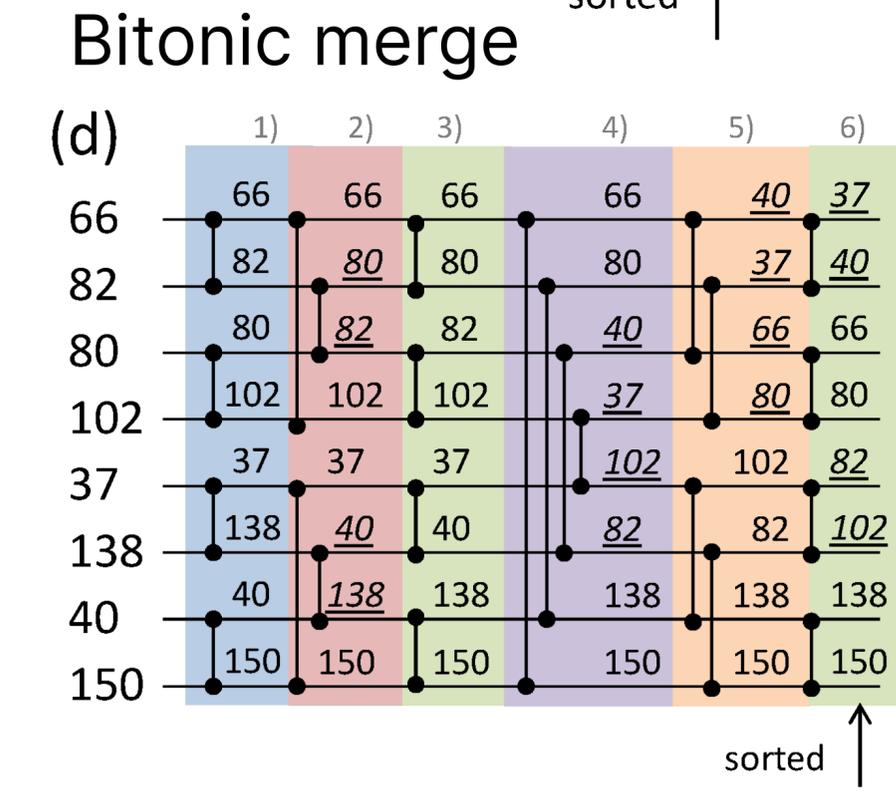
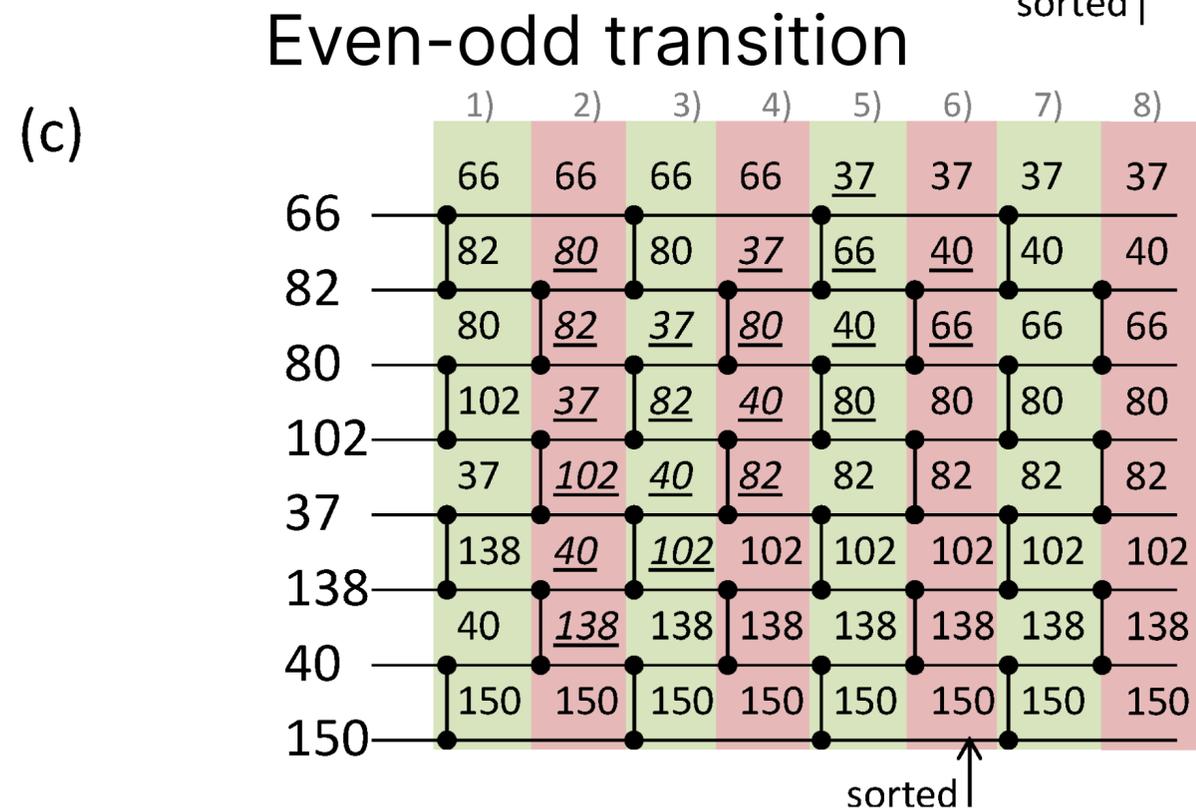
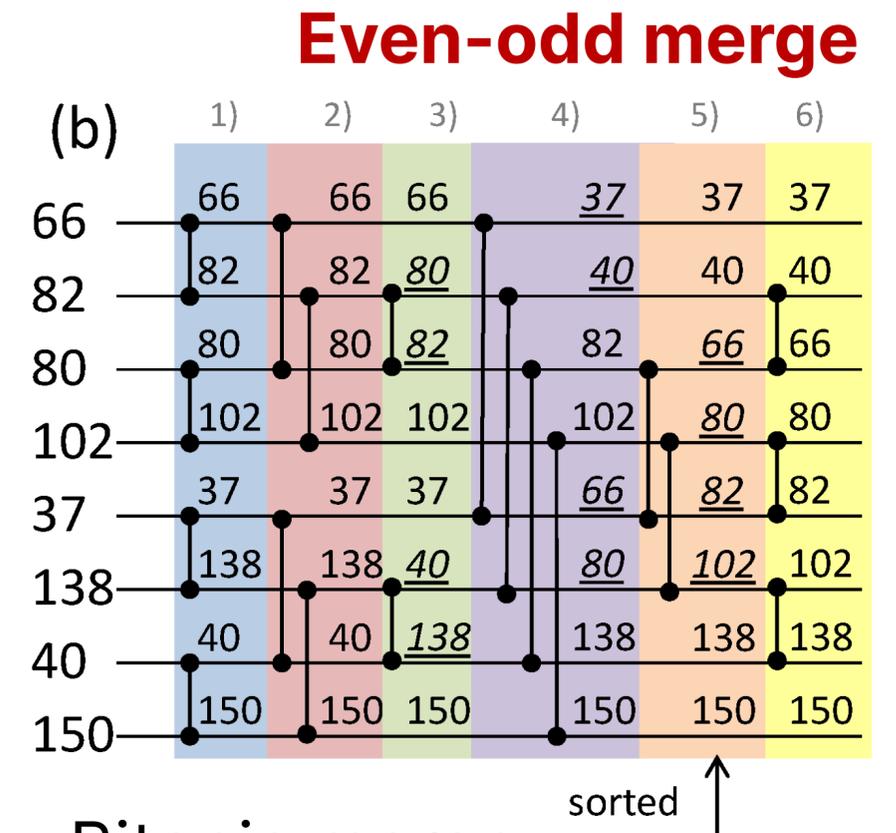
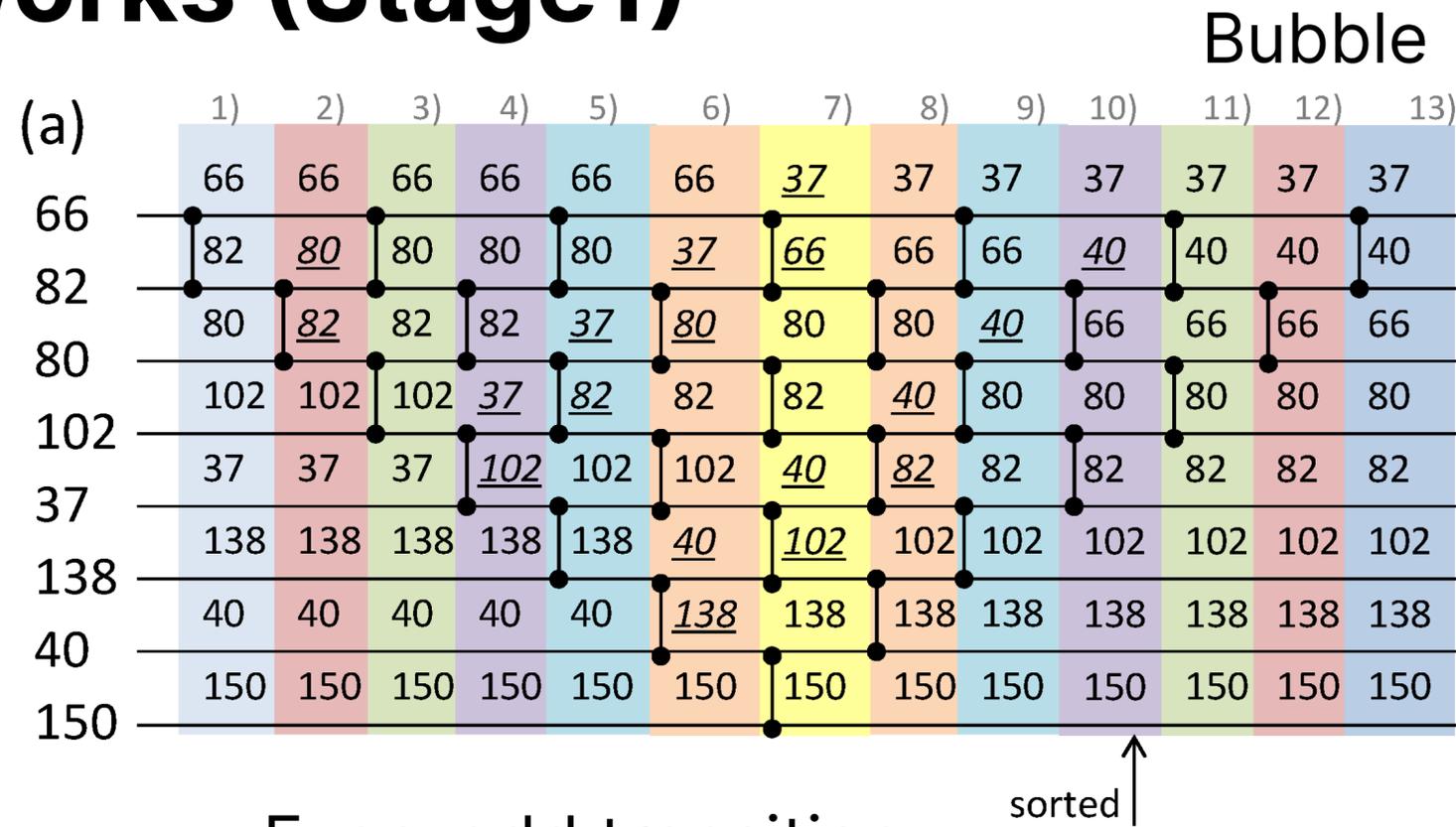


Backend Stage1

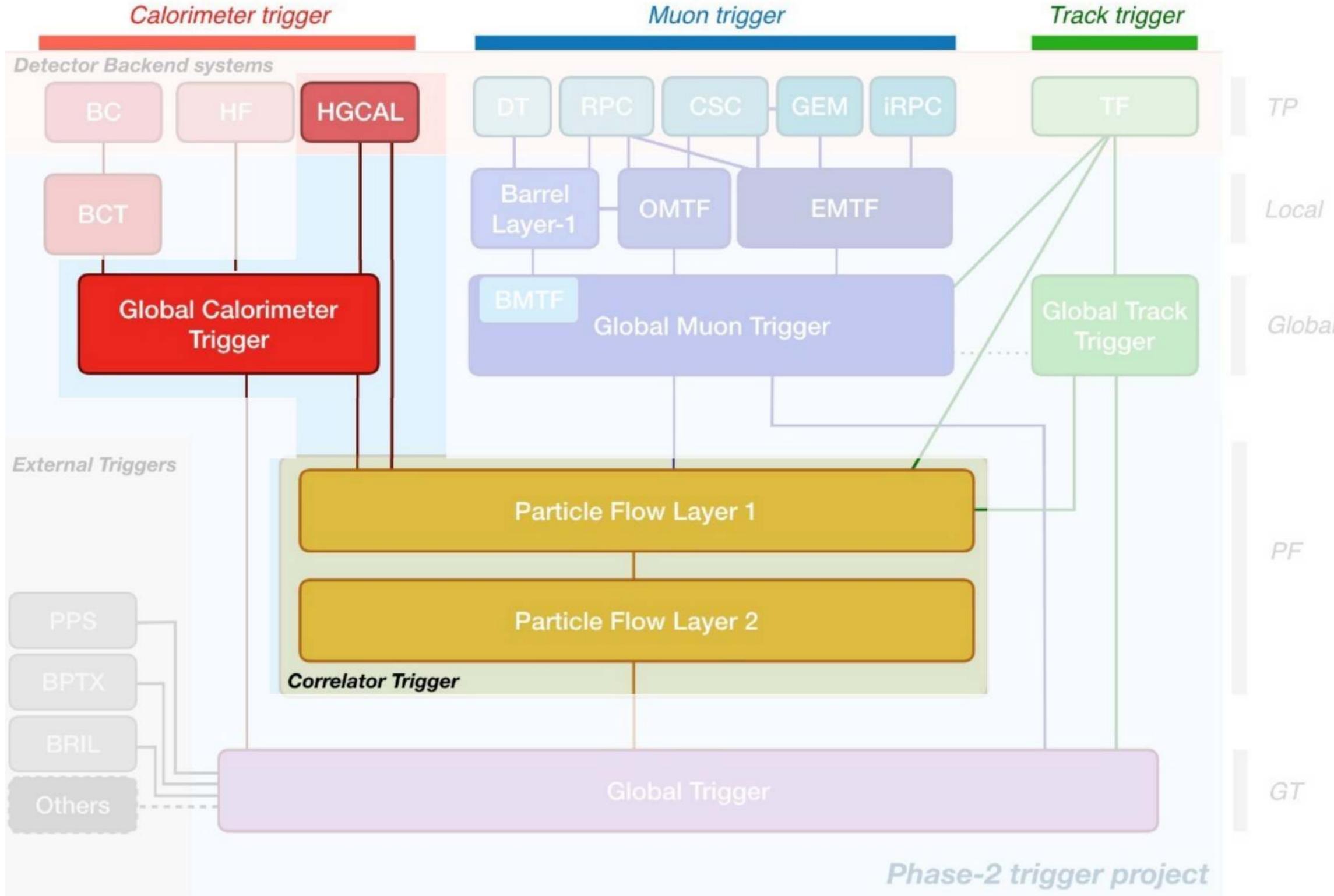


credits: F. Beaujean et al.

Sorting networks (Stage1)



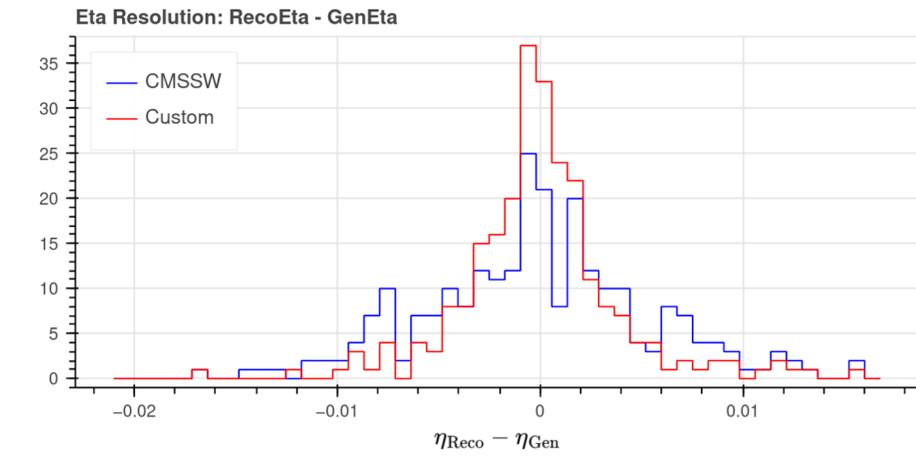
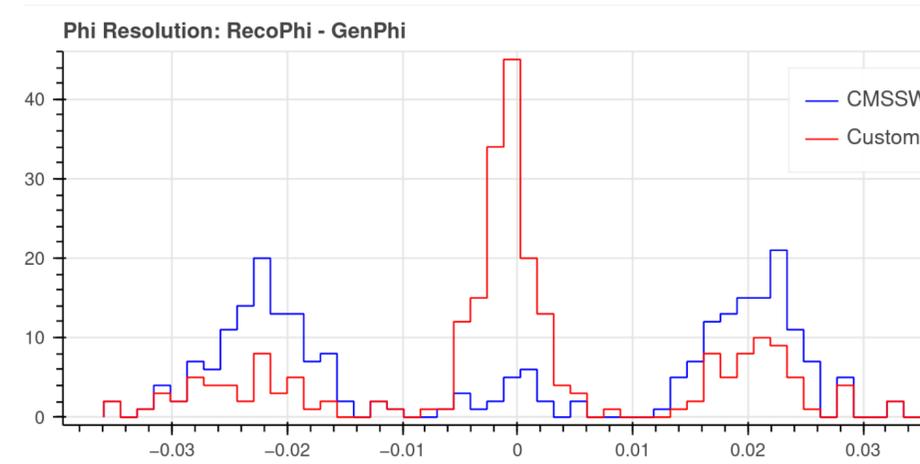
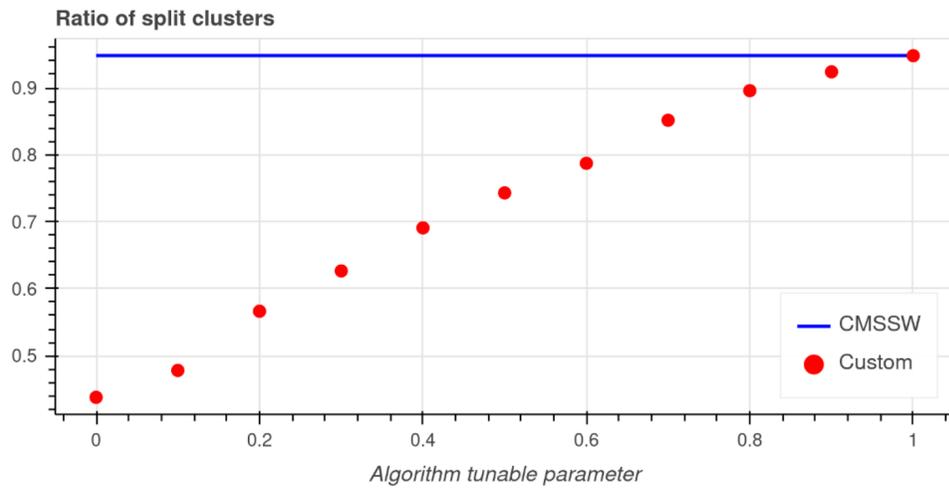
HGCAL TPG as part of the L1 trigger



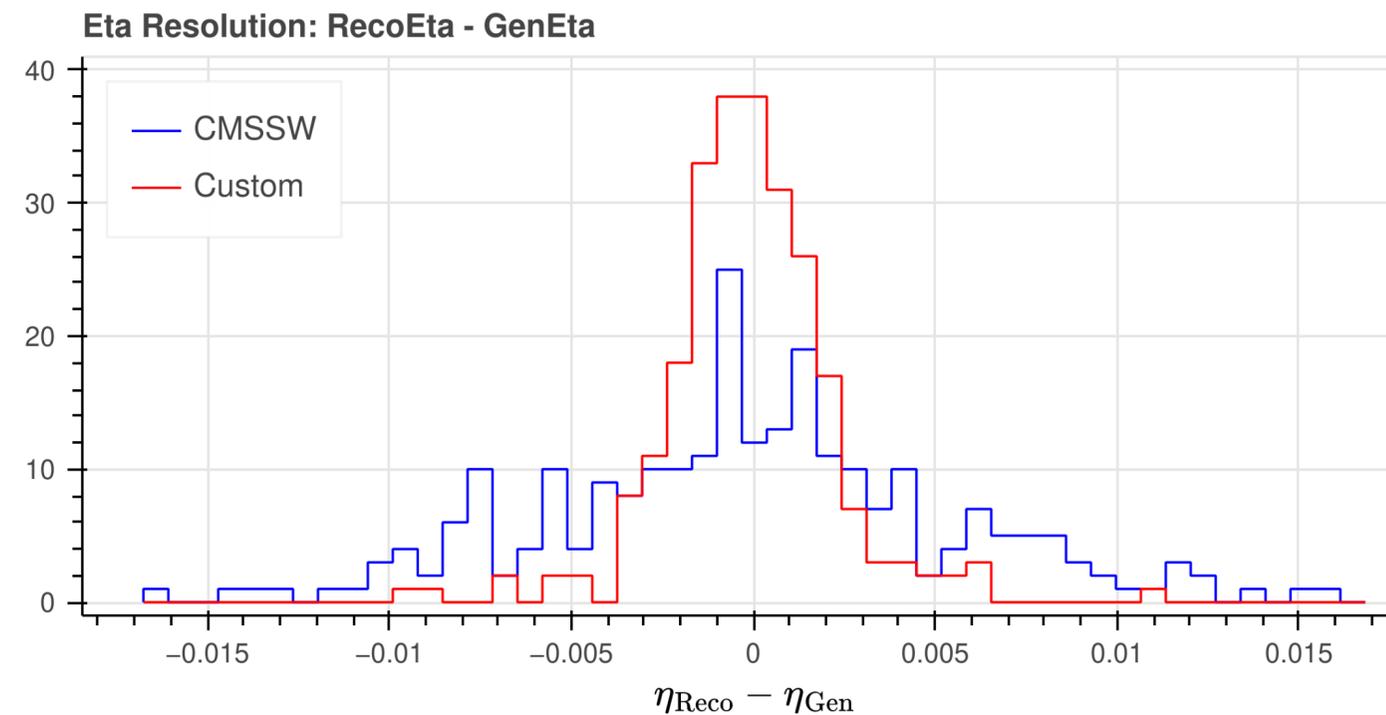
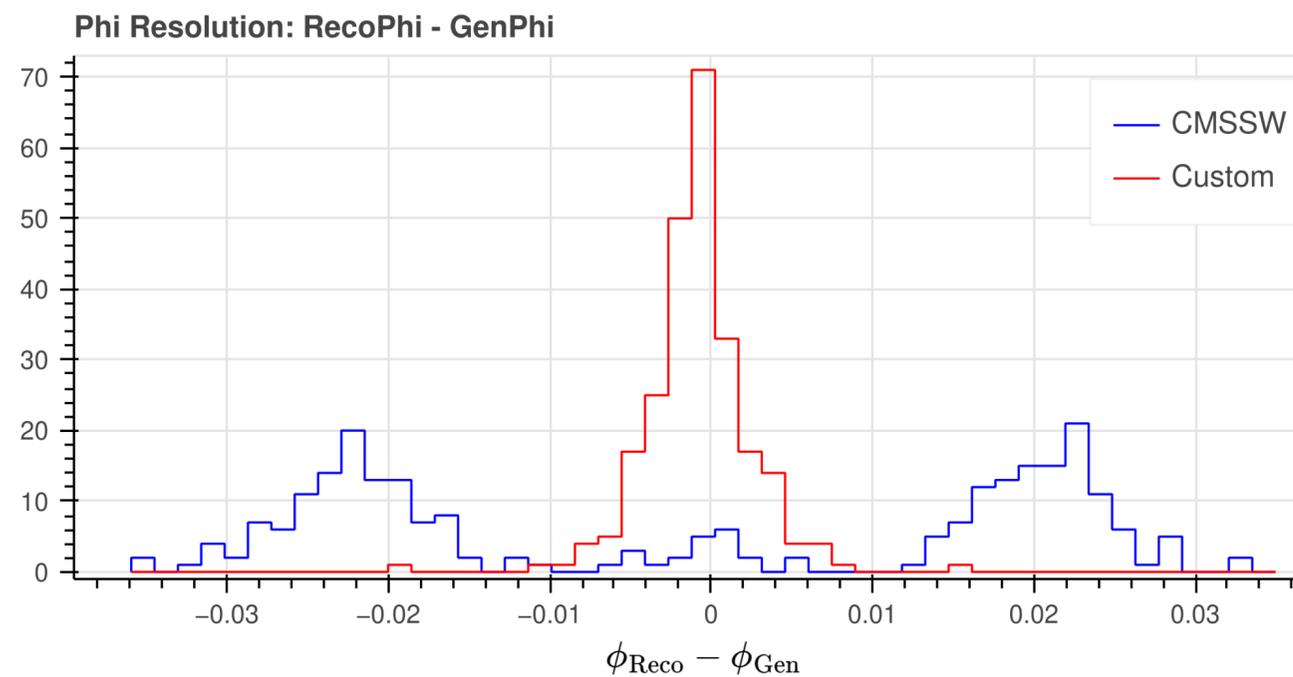
Note: resolutions showed for events with split clusters only

Cluster Splitting: Resolutions

- **“ByeSplits” algorithm:**



- **Add “2nd order neighbors” in the seeding:**

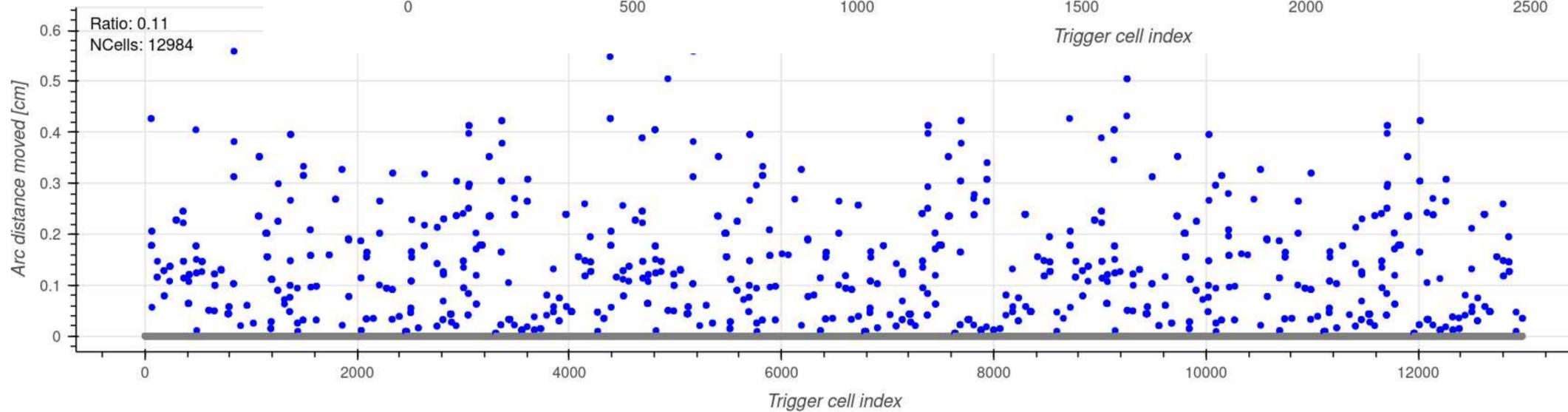
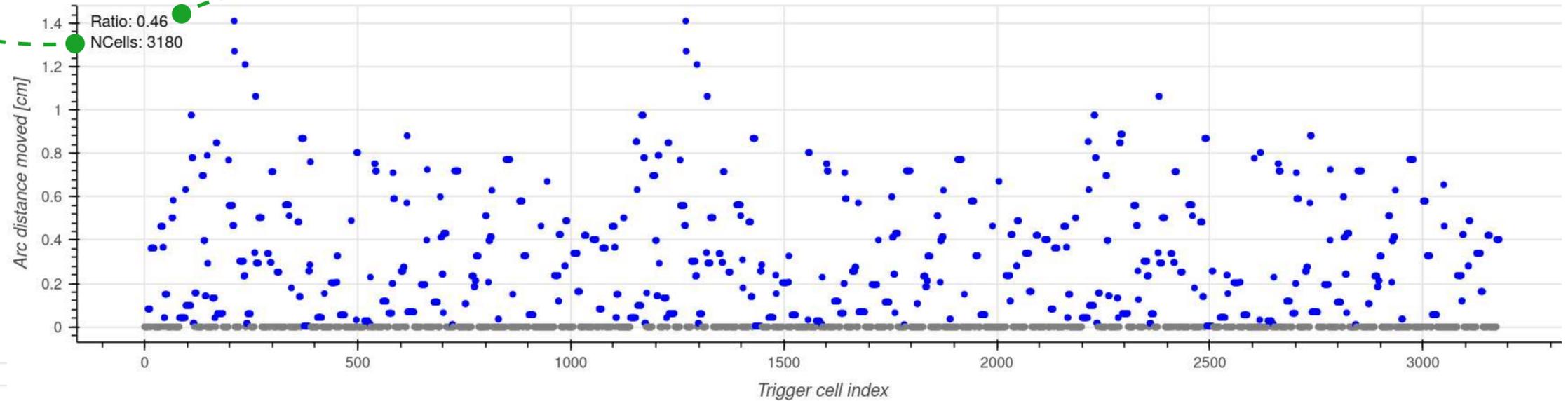


ByeSplits algorithm: TC arc distance moved along ϕ

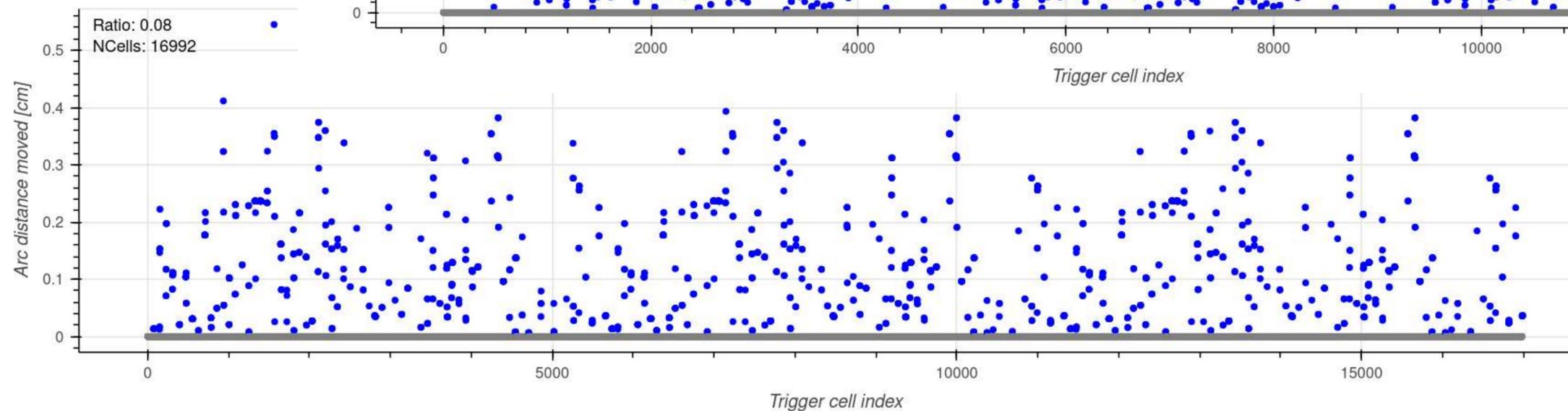
total number of TCs in a R/z slice

fraction of TCs that moved

R/z slice 0 (lower η)



R/z slice 3



R/z slice 6

(there are 42 slices in total)

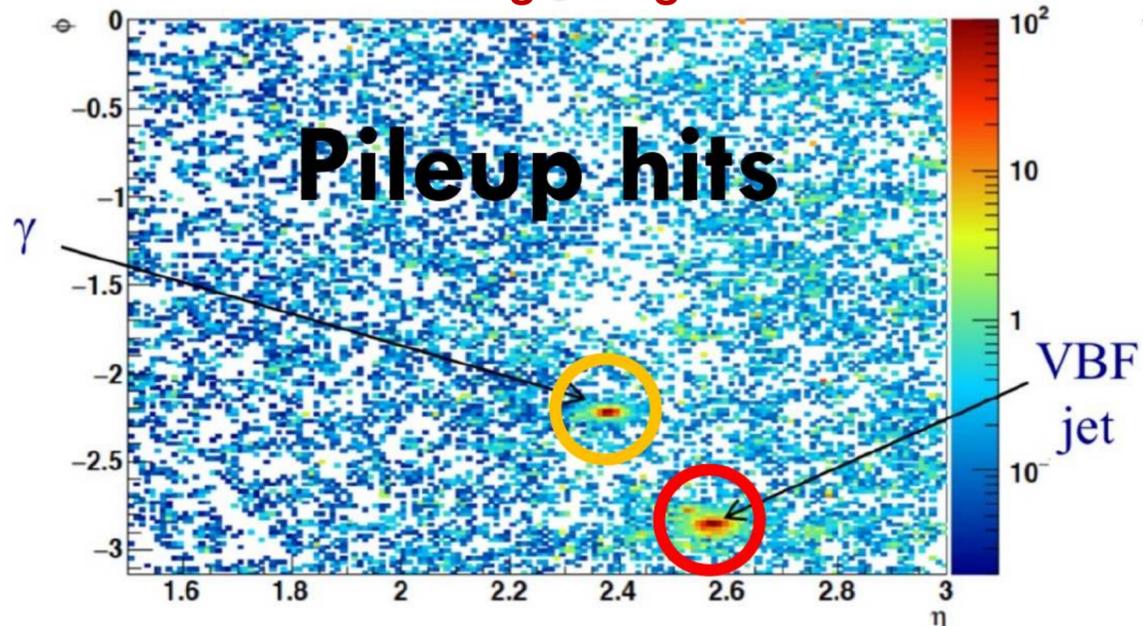
Software developments

- The reconstruction chain of the HGCAL TPGs was fully ported to Python. This enables:
 - quick prototyping of new ideas
 - quick testing of those ideas
 - easy parameter optimization studies
 - conveniently debug the original chain
- A simplified version of the HGCAL geometry was developed in Python. Benefits:
 - event displays
 - debugging the original geometry (some bugs already found!)
- The two above, when used together, make possible the visualization of specific events after running custom algorithm, for real-time inspection
- The framework is available [here](#).

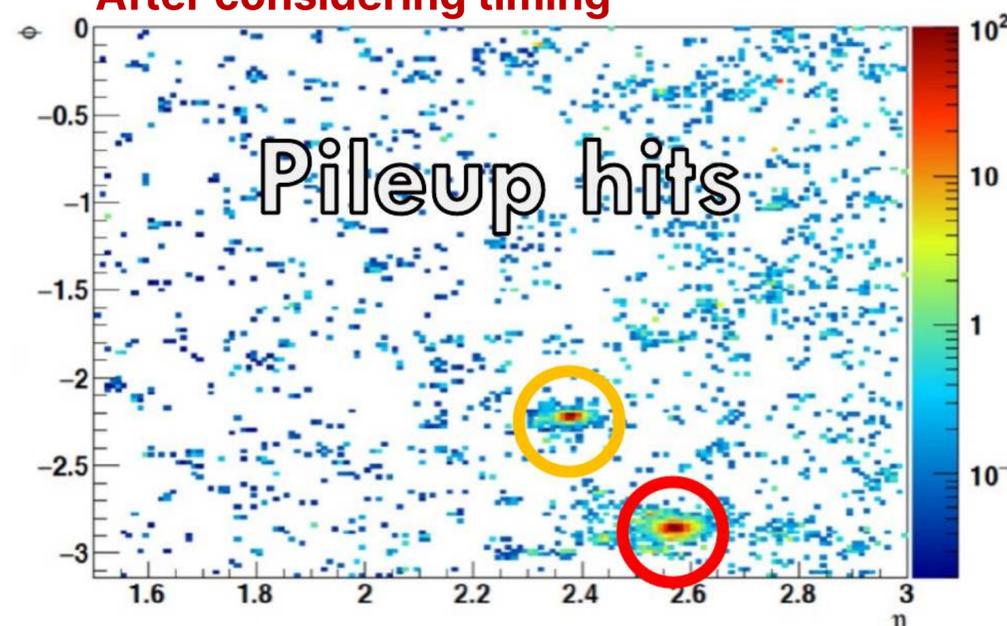
HGCAL Physics Motivation

- Extend Particle Flow measurements from the tracker into the calorimeter
- Subtract pileup energy leading to a good energy resolution even w/ high pileup
 - good time resolution is essential
- Merged jets can be reconstructed with higher efficiency and better energy resolution improving the boosted object reconstruction performance
- The high lateral granularity allows tagging of narrow jets originating from VBF-produced Higgses and jets from VBS.
- High granularity also allows efficient e/γ reconstruction/PID in the forward region w/ pileup
- Small expected energy resolution constant term (typically dominates the energy resolution at high energies) will lead to an EM resolution similar to the current one

Before considering timing



After considering timing



VBF $H \rightarrow \gamma\gamma$, Hits from all layers projected to the same depth

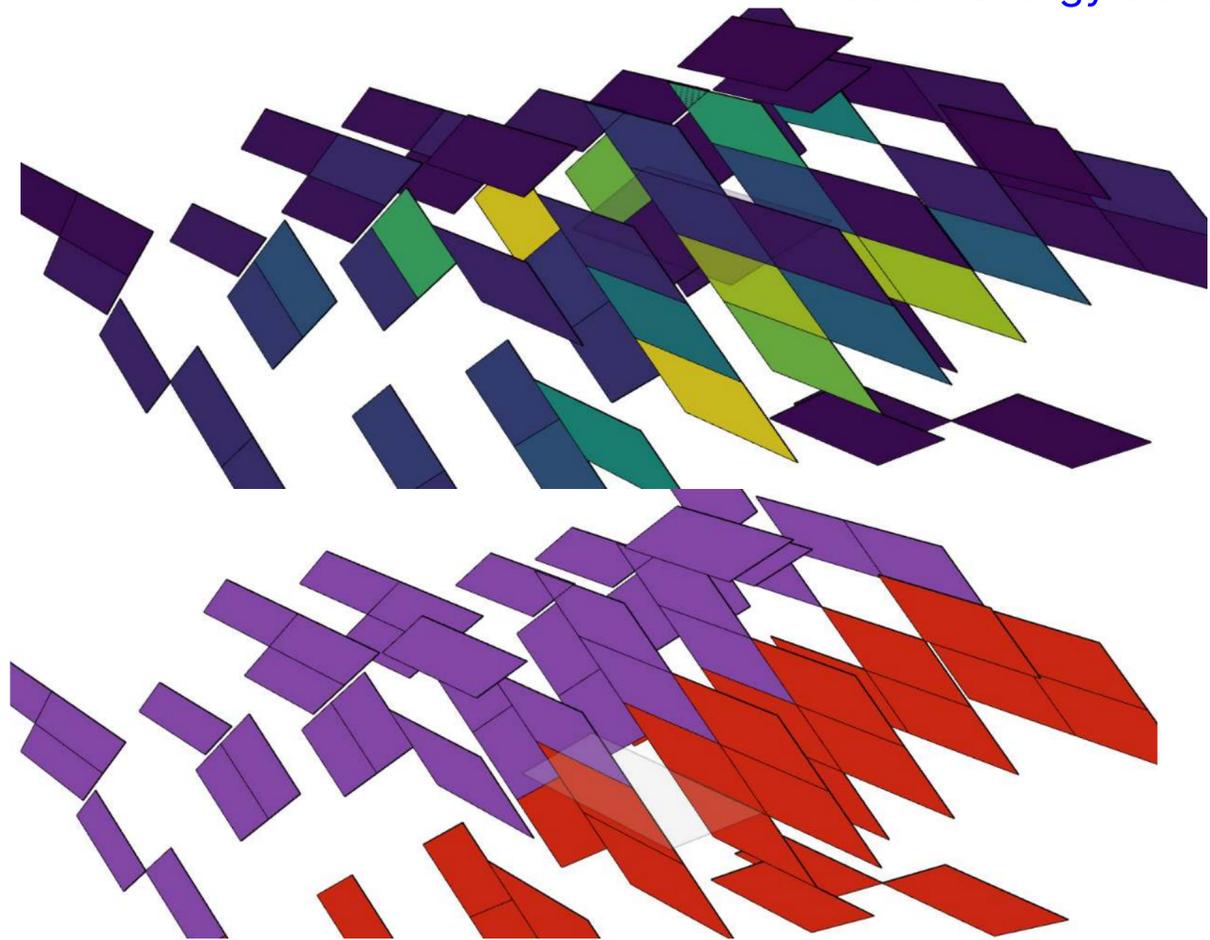
Typical jet:

- ~ 62% charged particles
- ~ 27% photons
- ~ 10% neutral hadrons
- ~ 1% neutrinos

3D Event Displays which originated cluster splits (OPU photons)

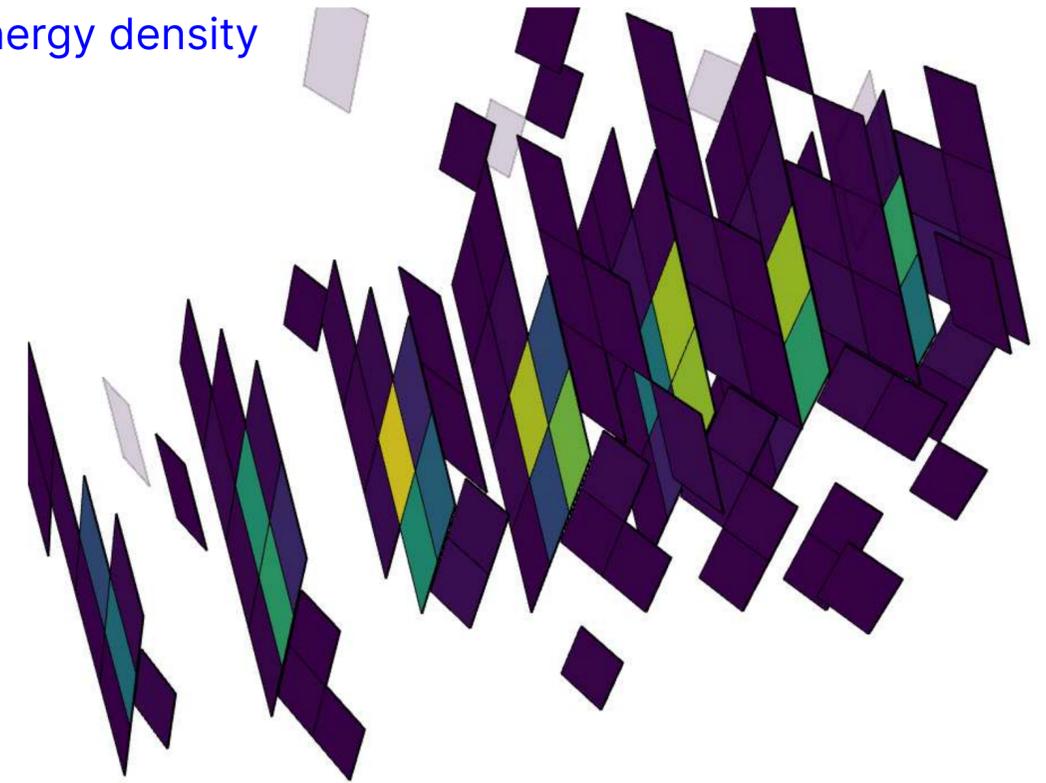
**this one really has
two “peaks”!**

color: energy density



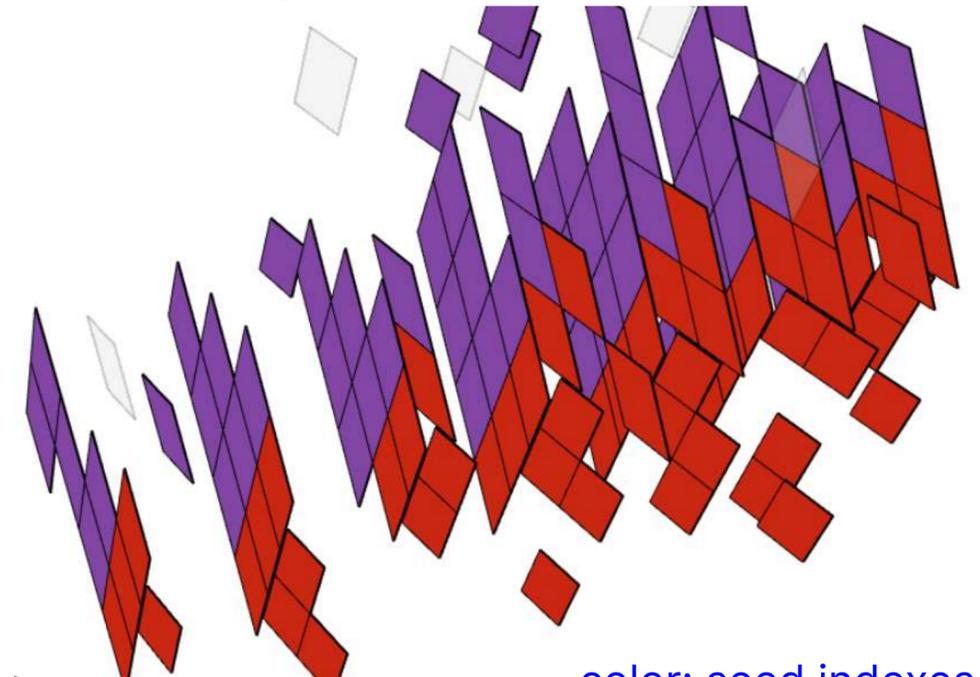
color: seed indexes

color: energy density



**most
showers are
standard,
where the
split clearly
is artificial**

color: seed indexes



Event Displays for 200PU single pion guns

