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Nb₃Sn growth by multilayer sequential sputtering for SRF application

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Outline

- Research motivation and approach
- Characterization results
- RF surface impedance characterization results
- Cylindrical magnetron for cavity coating







Research motivation

Why Nb₃Sn and why magnetron sputtering?

- Nb cavities are approaching the intrinsic material limit.
- Higher T_c and H_{sh} of Nb₃Sn promise potential cavity operation at higher temperatures and higher E_{acc} .
- Disadvantage- brittle structure and lower thermal conductivity.

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Phase	<i>T_c</i> (K)	$H_{sh}\left(\mathrm{mT} ight)$
Nb	9.25	200
Nb ₃ Sn	18.3	400

Research motivation

Why Nb₃Sn and why magnetron sputtering?

Advantages of magnetron sputtering:

- Stoichiometry of Nb and Sn can be controlled.
- Uniform Sn composition throughout the grain can be obtained.
- Relatively less annealing temperature ٠ is required- possible to use in copper cavities.





Critical temperature of Nb₃Sn as a function of atomic Sn content [2].

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Nb₃Sn by multilayer sequential sputtering

Ar gas

Substrate

Ar⁺

Vacuum pump

- Multiple layers of Nb and Sn sputter sequentially.
- Deposition rate: 1 Å/s.
- Sputtering pressure: 3 mTorr 20 SCCM.
- Substrate rotation: 30 rpm.
- Multilayers annealed: 850- 1200 °C, 1- 12 h.



Nb₃Sn by multilayer sequential sputtering

- Parameters to optimize
 - Effect of annealing temperature and time,
 - Effect of thickness,
 - Effect of substrate temperature,
 - Effect of annealing ramp rate.









Effect of annealing temperature

- 1 μ m thick multilayer of Nb (20 nm thick) and Sn (10 nm thick) deposited.
- Annealed temperature: 850, 950, 1000, 1100, and 1200 °C for 3 h.









Structural properties



Figure: X-ray diffraction pattern of as-deposited and annealed films.



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Effect of annealing temperature



Effect of annealing temperature





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Nb:Sn 10 nm:10 nm



Nb:Sn 10 nm:5 nm



Nb:Sn 20 nm:10 nm



Nb:Sn 20 nm:10 nm



Nb:Sn 30 nm:10 nm



Nb:Sn 50 nm:25 nm



Nb:Sn 40 nm:10 nm



Nb:Sn 200 nm:100 nm





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Effect of Nb buffer layer thickness



No buffer layer

20 nm buffer layer

100 nm buffer layer

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Figure: SEM images of the annealed films with different buffer layer thicknesses.



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Effect of substrate temperature (S.T.)



As-deposited – S.T. room temperature





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Annealed at 950 °C \times 3 h- S.T. room temperature





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As-deposited- S.T. 250 °C





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Annealed at 950 °C \times 3 h- S.T. 250 °C







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RF surface resistance measurement



Residual resistance: $3.87 \pm 0.28 \text{ m}\Omega$ Gap: $2.52 \pm 0.96 \text{ meV}$



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Critical temperature: 17.20 K

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Construction of cylindrical magnetron



Summary

- Superconducting Nb₃Sn with a T_c up to 17.93 K have been achieved.
- Film morphology modified with niobium buffer layer and increased substrate temperature.
- Uniform Sn composition through the whole surface after annealing.
- RF superconducting transition at 17.2 K.
- Problems to solve- Sn loss due to evaporation.









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THANK YOU!







