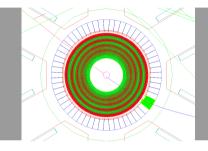
Update on the ALERT TOF detector ATOF mechanical design and readout electronics



Whitney Armstrong Tom O'Connor Argonne National Laboratory

April 28, 2020



Introduction

- Overview of ALERT Physics
- ALERT Detector and ATOF Requirements
- Mechanical design progress
- Readout electronics status





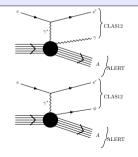
The ALERT Experiments

A comprehensive program to study nuclear effects

Coherent Processes on ${}^{4}\mathrm{He}$

- 4 He($e, e' {}^{4}$ He γ)
- 4 He($e, e' {}^{4}$ He ϕ)

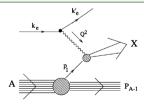
Explores the partonic structure of ${}^{4}\mathrm{He}$



DIS on ${}^{4}\text{He}$ and ${}^{2}\text{H}$: Tagged EMC Effect

- ⁴He(*e*, *e*'+³H)X (proton DIS)
- 4 He($e, e' + {}^{3}$ He)X (neutron DIS)
- ${}^{2}H(e, e' + p)X$ (neutron DIS)

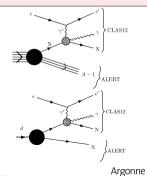
Test FSI and rescaling models



Incoherent processes on ${}^4 extsf{He}$ and ${}^2 extsf{H}$

- 4 He($e, e'\gamma p + {}^{3}$ H)
- 4 He $(e, e'\gamma + {}^{3}$ He)n
- ${}^{2}\mathsf{H}(e,e'\gamma+p)n$

Identify medium modified nucleons

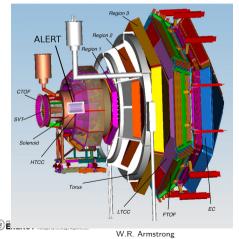


An energy more channels for free W.R. Armstrong

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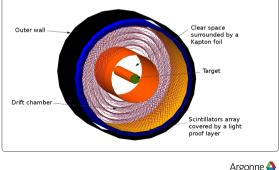
Proposed Setup: CLAS12 + ALERT

- Use CLAS12 to detect scattered electron, $e^\prime,$ and forward scattered hadrons.
- A low energy recoil tracker (ALERT) will detect the spectator recoil or coherently scattered nucleus

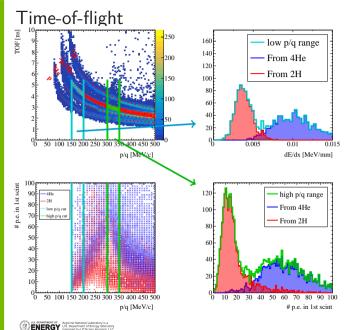


ALERT requirements

- $\bullet\,$ Identify light ions: H, $^2\text{H},\,^3\text{H},\,^3\text{He},$ and ^4He
- Detect the **lowest momentum** possible (close to beamline)
- Handle high rates
- Provide independent trigger
- Survive high radiation environment
 - \rightarrow high luminosity



• ALERT, will replace the CLAS12 silicon vertex



Design Parameters Need < 150 ps time resolution shooting for 50 ps Inner bar thickness : 3 mm. Outer wedge thickness : 2 cm.

TOF separates light ions, except 4 He and 2 H which have same m/q ratio

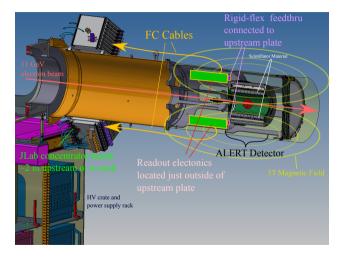


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ATOF System Layout

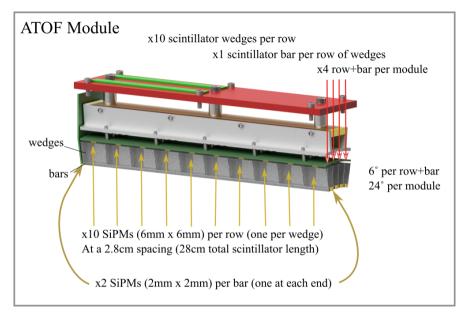
- Readout electronics will mount on the upstream plate of ALERT
- All active components will be outside of the drift gas volume
- Developing modular readout electronics with engineering support from Nalu Scientific





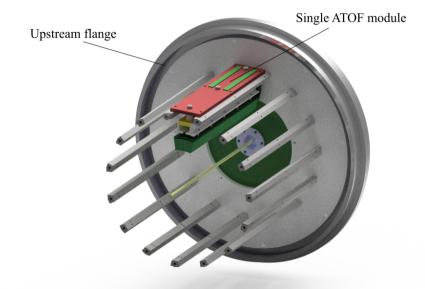


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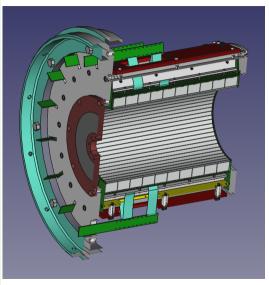


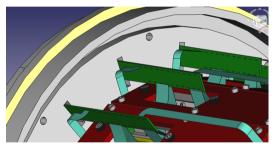




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Note this is a slightly older design.

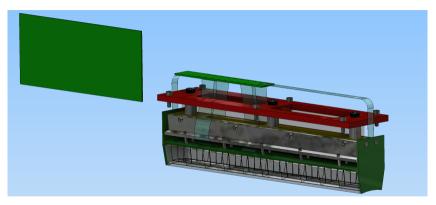


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ATOF Module boards



- Rigid-flex PCB will connect SiPMs to upstream readout electronics
- Upstream plate feed-through (from inside drift gas volume to air) being designed

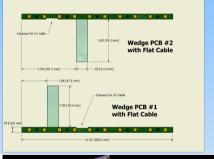


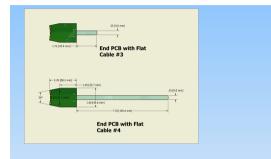
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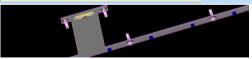
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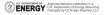


ATOF Rigid-Flex PCB





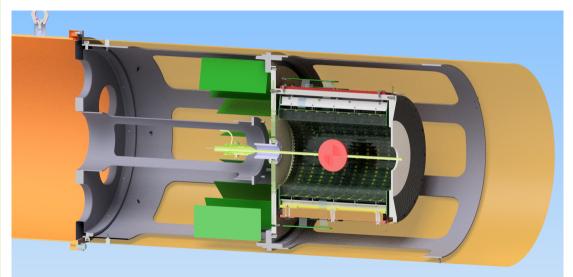




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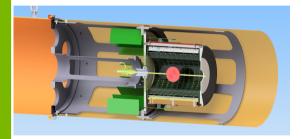


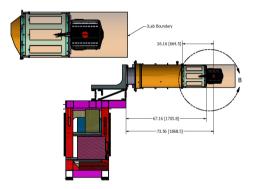




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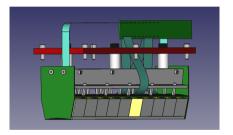
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Electronics

- Prototype Petiroc2A fabrication soon (Cuevas,Raydo)
- Working with Nalu Scientific for engineering of readout electronics
- Developing prototype zero dead-time, waveform readout with Nalu
- Collaborating with JLab to make modular system which interfaces with JLab FPGA
- Readout is mostly Petiroc2A boards with swappable waveform digitizing boards for systematic checks.
- JLab SSP concentrator board will be used.



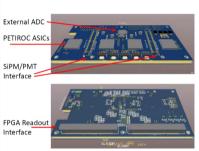


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Prototype Electronics



PETIROC ASIC PCB Status

- · Placement was redone to optimize SiPM -> ASIC traces
- Routing in progress probably another week or two before ready for assembly



Features:

- 128 channels (PMT or SiPM inputs)
- · Few to few thousand photoelectron charge measurement sensitivity
- <50ps hit timing resolution
- Expected >50kHz trigger rate capable
- Optical readout with Jlab FPGA readout board. Jlab CODA integration with SSP backend optical concentrator.

Courtesy Ben Raydo



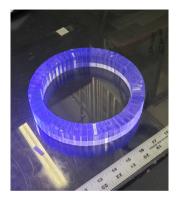
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Summary

- ATOF mechanical design is very far along
- Prototype Petiroc2A readout board ready for fabrication
- ATOF module rigid-flex PCB design well underway
- Trade study for readout system underway with expected report in July .



Many thanks to the ATOF team

Tom O'Connor, Todd Hayden (Argonne), Ben Raydo, Chris Cuevas (JLab Fast Electronics), the team at Nalu Scientific, and the ALERT Collaboration



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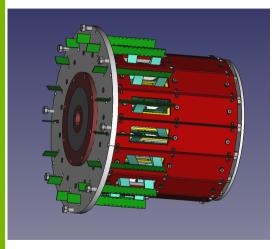
backup

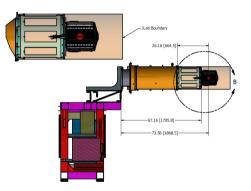


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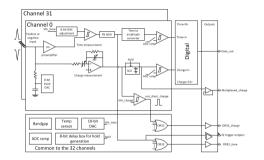
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Petiroc2A

- Readout electronics
- 32 Channels per chip
- 37 ps time resolution
- Time and charge mode readout takes 12 us per event





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