

LD2603 Q2 Report - Multiple Mode Excitation System for Processing Multicell SRF Cavities

• Progress

- Low-Cost RF FPGA – Red Pitaya
- Implementing Cavity Control Loop
- RF system ready to upconvert from 10 MHz to drive cavity in VTA

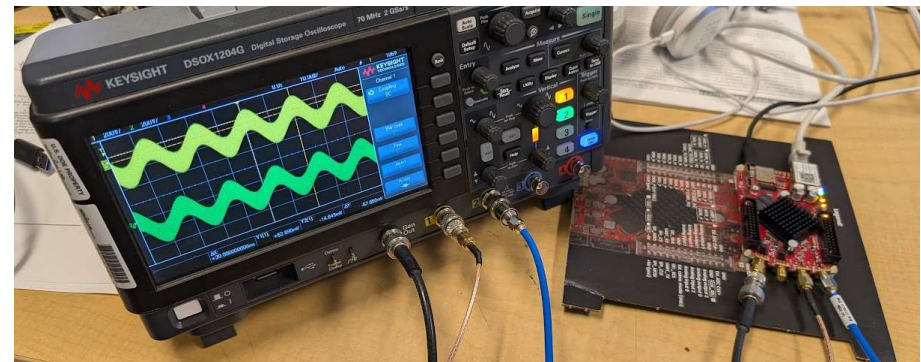
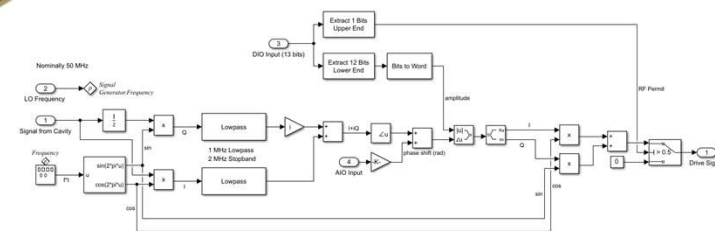
• Next Steps

- Try advanced signal processing tricks on cavities in VTA (LabVIEW+Matlab)
- Adapt Multi-mode control demonstrated last year to suppress Mode-mixing in LCLS-II cavity tests
 - Direct benefit to SRF cavity production
- Continue building Red Pitaya control scripts

• Issues

- Time constraints with other projects and end of the SAM
- Red Pitaya is powerful, but not much 1st or 3rd party support for our custom functions, much trial + error in code development
- PI going on Parental Leave in mid-April, planning to allocate work to ensure progress continues

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• Financial Report

- Using cheap FPGAs, not much spent on hardware so far
- Labor is the primary expense, constrained by other work demands
- Current Spending \$27.5k (Feb '26)

WBS 1.04 Accelerator LDRD Projects
FY26 Period 03 - December 2025

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